## PET ROBOT

By

## FARALIZA BT MOHAMED

## FINAL PROJECT REPORT

Submitted to the Electrical & Electronics Engineering Programme in Partial Fulfillment of the Requirements for the Degree Bachelor of Engineering (Hons) (Electrical & Electronics Engineering)

> Universiti Teknologi Petronas Bandar Seri Iskandar 31750 Tronoh Perak Darul Ridzuan

© Copyright 2008 by Faraliza Bt Mohamed, 2008

ü

# **CERTIFICATION OF APPROVAL**

Pet Robot

by

Faraliza Bt Mohamed

A project dissertation submitted to the Electrical & Electronics Engineering Programme Universiti Teknologi PETRONAS in partial fulfilment of the requirement for the Bachelor of Engineering (Hons) (Electrical & Electronics Engineering)

Approved:

whey

Dr Mumtaj Begam Project Supervisor

UNIVERSITI TEKNOLOGI PETRONAS TRONOH, PERAK

June 2008

# **CERTIFICATION OF ORIGINALITY**

This is to certify that I am responsible for the work submitted in this project, that the original work is my own except as specified in the references and acknowledgements, and that the original work contained herein have not been undertaken or done by unspecified sources or persons.

aliza Bt Mohamed

## ABSTRACT

The idea of the PET ROBOT is highly appropriate due to the rapid changing technology in the modern world and the humans changing ways of life. This technology could not only be the replacement for house pets but with detailed design and innovation it could be an assistant to humans at homes. Pet robot uses a microcontroller to control its functions. The microcontroller will carry out instructions from the designed coding that is implemented to the microcontroller. Coding is designed and compiled using PIC Programming software. Different types of sensors are placed to the robot to give it intelligence. The pet robot will be not only be able to move forward, backward and turn but with the ability to 'see' by implementing sensors, the robot is also able to avoid object obstacles along the way. Besides that, the robot can react to certain external input such as performing certain functions when it detects light and can react to sound. The project work requires both mechanical field for movement and electrical field for controlling the robot.

## ACKNOWLEDGEMENT

Alhamdulillah, thank you to Allah s.w.t for giving me the strength to complete this project.

First of all, I would like to thank Dr Mumtaj Begam, my project supervisor for leading me and showing me the correct ways in pursuing this project. Without her I would be lost and may not be able to complete this project on time.

I would also like to thank the technicians for giving me a chance to use the equipments in the lab as well as giving me guidance on how to use them properly and safely. They are most appreciated.

Secondly, I would like to thank my parents as well as my friends who had indirectly supported in completing this project. Their eagerness to see my project complete raises my spirit and strengthens my motivation to complete this project. Thank you.

# TABLE OF CONTENTS

ABSTRACT			<b>v</b>
ACKNOWLEDG	EMENT	<b>.</b>	vi
LIST OF FIGUR	ES		ix
LIST OF TABLE	ES		xi
CHAPTER 1:	INT	RODUCTION	1
	1 1.1 1.2	Background Problem Statement	
	1.3	1.3.1	of Study
		1.3.2	Scope Of Study
CHAPTER 2:	LITI	ERATURE REVIEW	AND/OR THEORY7
	2.1	Microcontroller and	Circuits7
		2.1.1	Main Circuits7
			2.1.1.1 Microcontroller8
			2.1.1.2 Voltage Regulator10
			2.1.1.3 Oscillator Clock12
		2.1.2	H-Bridge13
		2.1.3	Infrared14
		2.1.4	Light Sensor15
			2.1.4.1 Light Dependent Resistor15
	2.2		nt17
		2.2.1	Four Wheeled Robot18
	2.3		rds (PCB)20
		2.3.1	Patterning (etching)20
		2.3.2	Lamination21
		2.3.3	Drilling21

CHAPTER 3	MET	HODOLOGY/PROJECT WORK	23
	3.1	The Body	24
		3.1.1 Wheeled Robot	24
	3.2	Designing the Circuit	26
	3.3	Programing the PIC	27
		3.3.1 PIC C Compiler Software	28
	3.4	Implementing The circuits	
		3.4.1 Printable Circuit Board Method	31
CHAPTER 4	RES	ULTS & DISCUSSIONS	33
	4.1	Results	33
		4.1.1 Robot Function Flow Diagram	33
		4.1.2 Main Circuit	34
		4.1.2.1 Microcontroller PIC16F84A	
		4.1.3 PIC Programming	37
		4.1.4 Light Sensor Circuit	40
		4.1.5 Infrared Transmiter & Reciever	42
		4.1.6 Overall Circuit Connection	43
		4.1.7 Pictures of Pet Robot	45
	4.2	Discussion	46
		4.2.1 Speed Control	46
		4.2.2 Circuit's Stability & Sensitivity	46
		4.2.3 Light Sensor Sensitivity	47
		4.2.3 Reprogrammable Chip	47
CHAPTER 5	CON	CLUSION & RECOMMENDATIONS	48
	5.1	Conclusion	48
	5.2	Recommendations	49
REFERENCI	ES		50
APPENDIXE	S		51
	APPENDIX	A PIC 16F84A Datasheet	52
	APPENDIX	B BoostC C Compiler Manual	53

•

# LIST OF FIGURES

Figure 1: Pin diagram of PIC16F84A
Figure 2: Voltage Regulator 785011
Figure 3: Voltage Regulator Connection Diagram
Figure 4: Crystal Clock Oscillator
Figure 5: H-Bridge Connection Diagram
Figure 6: Two States of H-Bridge14
Figure 7: LDR Circuit
Figure 8: Light Dependent Resistor
Figure 9: Legged Robot and Wheeled Robot17
Figure 10: Wheeled Robot with One Motor
Figure 11: Wheeled Robot with Two Motor
Figure 12: Wheeled Robot with Two Motor
Figure 13: Flow Diagram of Building the Robot
Figure 14: Motor Connection
Figure 15: Front Wheels and Back Wheels
Figure 16: Designing the Circuit Flow Diagram
Figure 17: Programming the Chip Flow Diagram
Figure 18: C Compiler Software
Figure 19: PIC Simulator IDE
Figure 20: PIC Programming Software

Figure 21: PIC Programming Device	
Figure 22: Schematics Drawings in Eagle 4.13 Light	31
Figure 23: PCB Route Designing.	
Figure 24: Function Flow Diagram	34
Figure 25: Pin Connection of PIC 16F84A	
Figure 26: Main Circuit	
Figure 27: Light Sensor Circuit.	40
Figure 28: Infrared Transmitter Circuit.	42
Figure 29: Infrared Reciever Circuit	42
Figure 30: Overall Circuit Connection	43

# LIST OF TABLES

Table 1: H-Bridge Summarize Function	n		 17
		,	

## CHAPTER 1

## INTRODUCTION

#### 1. PET ROBOT

The title of this project is 'PET ROBOT'. The idea is to build a robot that acts and behaves similar to a pet. It is called a pet robot due to its ability to perform functions imitating a real life pet. The functions of the robot will be controlled by a programmed chip.

## 1.1 Background of Study

The word robot gives meaning of a machine that can do work by itself, often work that humans do. [10] The concept of robots is a very old yet the actual word robot was invented in the 20th century from the Czechoslovakian word robota or robotnik meaning slave, servant, or forced labor. [12] Robots are very visible machines, ranging from small, miniature machines, to large crane size constructions with intelligence varying from simple programming to perform mechanical tasks, such as painting a car or lifting cargo, to highly complex reasoning algorithms mimicking human thought. [11] Historically, we have sought to endow inanimate objects that resemble the human form with human abilities and attributes. From this is derived the word anthrobots, robots in human form. Robots are especially desirable for certain work functions because, unlike humans, they never get tired; they can endure physical conditions that are uncomfortable or even dangerous; they can operate in airless conditions; they do not get bored by repetition; and they cannot be distracted from the task at hand. Robots have been useful in industrials, hazardous duty, maintenance work, fire – fighting, medical, space explorations as well as wars. Early industrial robots handled radioactive material in atomic labs and were called master/slave manipulators. They were connected together with mechanical linkages and steel cables. Remote arm manipulators can now be moved by push buttons, switches or joysticks. Current robots have advanced sensory systems that process information and appear to function as if they have brains. Their "brain" is actually a form of computerized artificial intelligence (AI). AI allows a robot to perceive conditions and decide upon a course of action based on those conditions. [12]

#### **1.2 Problem statement**

The ability to produce a functioning robot with good stability and control takes high technology as well as research. Robots are closely related to AI (artificial intelligence) where scientists are still on research to produce a robot which is capable of thinking and making its own decision (unprogrammed).

The main idea of this project is to build a robot that can imitate a pet (for example a cat or a dog). There are a few points of significance in having a pet robot to human beings. When pet robots are designed as close as being to a real life pet, it could be the next innovation of replacing real life pets. Owning a pet is something people desire to have but in this modern evolving era, people are too busy to handle and take care of pets at home. By having a pet robot instead of a real one, people can now have the pleasure of owning a pet without the fuss of maintaining or care taking them. Besides that, pet robots can also be a companion to humans. It can play the role of a 'mans best friend' just like dogs. For example with the rising statistics of senior citizens, pets can be a

great companion to them and accompany them throughout their remaining life. Unfortunately, majority of them do not have the capability of holding the responsibility of maintaining a pet (for example giving them baths and feeding them). By replacing these pets with a pet robot, they are able to keep a pet without pressuring their ability to take care of these them. Another application of pet robot in our everyday life is that it can be a good assistant to humans. By enhancing and adding more features to the robot, it can help people in various ways. For example, it can help blind people guiding them in walks by implementing sensors to the robot to detect object obstacles blocking their way. It can also be enhanced to supervise children. The pet robot can be a toy to the child and also be a nanny for parents to ensure their child is safe and sound.

Throughout the years many robots have been built and enhanced to perform different tasks for humans. Every type of robot was given specific functions and task just by enhancing the basic foundation of a robot. In this project, the author is rebuilding the foundation of all robots and adding features to make it as similar as a pet robot. The pet robot will have basic fundamental functions. By building the basic pet robot it can later be enhanced with more features and more sophisticated code programming to achieve the goals mentioned above.

### 1.3 Objective and Scope of Study

The main idea of this project is not only to build a robot, but to make it able to imitate the behavior of pets. It is difficult to make the robot to fully imitate all the behaviors of a cat or a dog, but some personality can be implemented to the robot as so it is acceptable as a pet. The behavior and characteristics of the robot is very subjective and general. Some of the ideas considered for this project are:

#### 1) Random movement functions

Most pets have their own behavior and characteristics. We need not order them on how to move or how to act. In order to implement these criteria to the robot, it will need to have random movements on its own. It will choose its own path and its own way of movements to go forward, reverse, turn or turn, reverse and turn again, it is all to be decided randomly. The movements will be different in each cycle. This will give the robot an essence of life. It will also make the robot seem more active.

#### 2) Detecting and avoiding object obstacles

Like most living creatures, the robot has 'eyes' which enable it to see what is in front of it. With this feature, the robot is able to see the object blocking its path. The robot is then given the intelligence to avoid the object and prevent it from bumping into things. This gives the robot a characteristic of independence.

#### 3) Light detecting

Every living creature has a natural feeling of fear. Therefore by implementing this feature it could reduce the impression of robotic towards the robot. The robot will have a fear of darkness. When it is in the dark, it will stop and start to glow in the dark as if it wants us to find it. Besides glowing in the dark, it will also start to behave strangely by moving in a peculiar way (for example shaking) to show its reaction of fear.

These are the main characteristics that the author has considered to implement in the robot. Once the main circuit has been completed, more functions and characteristics could be added to enhance the robot.

#### 1.3.1 Objective:

1) To design and build a robot with the ability to:

- Move forward
- Move backward
- Turn right and turn left
- Detect object obstacles
- Avoid object obstacles
- Detect light
- React to light
- To design the codings and implement them in a microcontroller to control the functions and movements of the robot.

3) To program the robot to have its own behavior and characteristics by having random

movements

#### 1.3.2 Scope of study

To achieve the objective of this project, studies and research on areas related with robotics is concentrated. In order to build a basic robot, basic functions such as moving forward, reverse and turning will have to be applied to the robot. This will require a combination of electrical and mechanical field knowledge and application where integrated electrical circuits will guide the mechanical movements of the robot.

In the mechanical area, the robot must have components that will enable it to move forward, reverse and turn. Commonly there are two types of robot to enable these basic functions. First is the walking legged robot and the other is a wheeled robot. Both of these two options are considered to design a robot most suitable for the specifications of a pet robot. In the electrical area, the circuit for the brain of the robot is built. The main component of the brain is a microcontroller which will control the movement of the robot. A programmable microcontroller is needed to provide flexibility to the functions and its special features. There are many types of microcontroller and the most suitable one will be selected to be used in this project. To control the robots basic movement (forward, reverse, turn), a dc motor is needed and it will be controlled by the chip.

In order to program the microcontroller, a basic knowledge in programming is needed. Flexibility in coding can provide a wider variety and possibility to the functions of the robot. Software in these areas is needed to design and implement the coding to the chip.

Since the robot is required to detect objects and lights, sensors are needed to perform these functions. When these sensors are triggered, they will send a signal to the microcontroller. The microcontroller will then send out a signal to the necessary component to show reaction to the sensors. There are many types of sensors to choose from but for this project only two types of sensors are being considered that is the infrared sensor and the ultrasonic sensor. Another type of sensor needed for the robot is the light sensor. Photocells are used and will serve the purpose of detecting light when they are illuminated. It will be integrated to a circuit to send signal to the microcontroller to perform the appropriate action. This will enable the robot to differentiate between dark and bright areas.

## CHAPTER 2

## LITERATURE REVIEW AND/OR THEORY

Research has been done on different areas of this project to learn more on how to build a pet robot. The research of this project has been divided into three sections which is the microcontroller circuits, the mechanical movements and the programming.

#### 2.1 Microcontroller and Circuits

There will be several circuits implemented to the robot. All of these circuits will be combined and integrated together to create the fundamental of a basic robot. All input and output signals of each circuit will be sent to the main circuit which will play the role of commanding and giving instructions to other components. Basically there are three major circuits:

- a) Main circuit
- b) H-bridge circuit
- c) Infrared sensor circuit
- d) Light sensor circuit

#### 2.1.1 Main Circuit

All the movements and decisions of the robot will be controlled by one main circuit. This circuit contains the PIC microcontroller which is programmed to control the functions of the robot. The output the pins of the microcontroller is then connected to the motors of the robot and other output components.

The main circuit contains these major components:

- Microcontroller PIC 16F84A
- Voltage Regulator
- Oscillator Clock

#### 2.1.1.1 Microcontroller

The brain of the robot will be controlled by a microcontroller chip. Various types of chip have been researched and the author prefers to use PIC 16F84A. This is due to a few advantages that this chip holds. This programmable microcontroller is commonly used in integrated circuits. The size of the robot is designed to be small to achieve its motive of imitating a pet. PIC 16F84A is small enough to be placed in the main circuit of the robot. Although it has less number of inputs and output pins, it is enough to cater for all the functions for the pet robot in this project. This chip will be programmed to perform the required tasks. The program memory contains 1K words, which translates to 1024 instructions, since each 14-bit program memory word is the same width as each device instruction .The data memory (RAM) contains 68 bytes. Data EEPROM is 64 bytes.

There are also 13 I/O pins that are user-configured on a pin-to-pin basis. Some pins are multiplexed with other device functions. These functions include:

- External interrupt
- Change on PORTB interrupts
- Timer0 clock input
  - Features:
- Operating speed: DC 20 MHz clock input
- DC 200 ns instruction cycle
- 1024 words of program memory
- 68 bytes of Data RAM
- 64 bytes of Data EEPROM

- 14-bit wide instruction words
- 8-bit wide data bytes
- 15 Special Function Hardware registers
- Eight-level deep hardware stack
- Direct, indirect and relative addressing modes
- Four interrupt sources:
- External RB0/INT pin
- TMR0 timer overflow
- PORTB<7:4> interrupt-on-change
- Data EEPROM write complete

## **Peripheral Features:**

- 13 I/O pins with individual direction control
- High current sink/source for direct LED drive
- 25 mA sink max. per pin
- 25 mA source max. per pin
- TMR0: 8-bit timer/counter with 8-bit

programmable prescaler

### **Special Microcontroller Features:**

• 10,000 erase/write cycles Enhanced FLASH

Program memory typical

• 10,000,000 typical erase/write cycles EEPROM

Data memory typical

- EEPROM Data Retention > 40 years
- In-Circuit Serial Programming<sup>™</sup> (ICSP<sup>™</sup>) via two pins

• Power-on Reset (POR), Power-up Timer (PWRT),

Oscillator Start-up Timer (OST)

• Watchdog Timer (WDT) with its own On-Chip RC

Oscillator for reliable operation

- Code protection
- Power saving SLEEP mode
- Selectable oscillator options

Below is a pin diagram of PIC 16F84A:

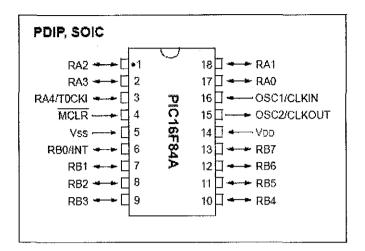


Figure 1: Pin Diagram of PIC 16F84A

The software that the author used to program the PIC16F84A is the PIC C Compiler and it will be simulated by PIC simulator IDE. The datasheet for PIC 16F84A is shown in appendix A.

#### 2.1.1.2 Voltage Regulator

A voltage regulator is an electrical regulator designed to automatically maintain a constant voltage level. Essentially, all a voltage regulator does is, obviously, regulate voltage; That is, it limits the voltage that passes through it. Each regulator has a voltage rating; For example, the 7805 IC (these regulators are often considered to be ICs) is a 5-volt voltage regulator. No matter how many volts is put into it, it will output only 5 volts. This means that by connecting 9-volt battery, a 12-volt power supply, or virtually anything else that's over 5 volts, and have the 7805 will give a of 5 volts out. There are also 7812 (12-volt) and 7815 (15-volt) three-pin regulators in common use. The pinout for a three-pin voltage regulator is as follows [13]:

- 1. Voltage in
- 2. Ground
- 3. Voltage out

For example, with a 9-volt battery, connect the positive end to pin 1 and the negative (or ground) end to pin 2. A 7805 would then give a +5 volts on pin 3. Voltage regulators are simple and useful. There are only two important drawbacks to them: First, the input voltage must be higher than the output voltage. For example, we cannot give a 7805 only 2 or 3 volts and expect it to give a 5 volts in return. Generally, the input voltage must be at least 2 volts higher than the desired output voltage, so a 7805 would require about 7 volts to work properly. The other problem: The excess voltage is dissipated as heat. At low voltages (such as using a 9-volt battery with a 7805), this is not a problem. At higher voltages, however, it becomes a very real problem and you must have some way of controlling the temperature so you don't melt your regulator[13].

This is why most voltage regulators have a metal plate with a hole in it; That plate is intended for attaching a heat sink to [13]. Figure 2 shows the voltage regulator pins.

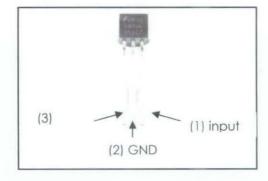


Figure 2: Voltage Regulator 7805

The circuit is supplied with a 9Volt Battery. The PIC only uses 5volts. A 5 volt voltage regulator 7850 is used to step down the power supply from 9V to 5V. Figure 3 shows the connection diagram of the voltage regulator.

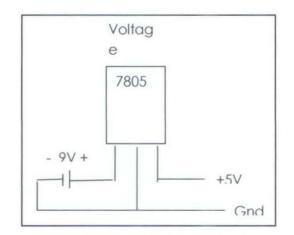


Figure 3: Voltage Regulator Connection Diagram

## 2.1.1.3 Oscillator Clock

A crystal oscillator is an electronic circuit that uses the mechanical resonance of a vibrating crystal of piezoelectric material to create an electrical signal with a very precise frequency. This frequency is commonly used to keep track of time (as in quartz wristwatches), to provide a stable clock signal for digital integrated circuits, and to stabilize frequencies for radio transmitters/receivers.

The crystal oscillator circuit sustains oscillation by taking a voltage signal from the quartz resonator, amplifying it, and feeding it back to the resonator. A regular timing crystal contains two electrically conductive plates, with a slice or tuning fork of quartz crystal sandwiched between them. During startup, the circuit around the crystal applies a random noise AC signal to it, and purely by chance, a tiny fraction of the noise will be at the resonant frequency of the crystal. The crystal will therefore start oscillating in synchrony with that signal. As the oscillator amplifies the signals coming out of the crystal, the signals in the crystal's frequency band will become stronger, eventually dominating the output of the oscillator. Natural resistance in the circuit and in the quartz crystal filter out all the unwanted frequencies.One of the most important traits of quartz crystal oscillators is that they can exhibit very low phase noise. In many oscillators, any spectral energy at the resonant frequency will be amplified by the oscillator, resulting in a collection of tones at different phases. In a crystal oscillator, the crystal mostly vibrates in one axis. Therefore, only one phase is dominant. This property of low phase noise makes them particularly useful in telecommunications where stable signals are needed, and in scientific equipment where very precise time references are needed.[2]



Figure 4: Crystal Clock Oscillator

#### 2.1.2 H-BRIDGE

An H-bridge is an electronic circuit which enables DC electric motors to be run forwards or backwards. These circuits are often used in robotics. H-bridges are available as integrated circuits, or can be built from discrete components. Figure 5 shows the H-bridge connection circuit.

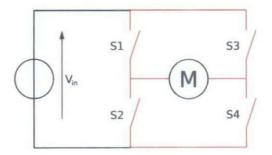


Figure 5: H-Bridge Connection circuit

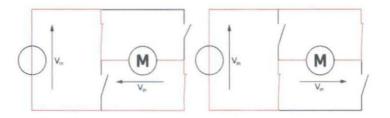


Figure 6: Two States of H-Bridge

Using the nomenclature above, the switches S1 and S2 should never be closed at the same time, as this would cause a short circuit on the input voltage source. The same applies to the switches S3 and S4. This condition is known as shoot-through.

The H-Bridge arrangement is generally used to reverse the polarity of the motor, but can also be used to 'brake' the motor, where the motor comes to a sudden stop, as the motors terminals are shorted, or to let the motor 'free run' to a stop, as the motor is effectively disconnected from the circuit. Table 7 summarises the operation [3].

<b>S1</b>	<b>S</b> 2	\$3	\$4	Result
1	0	0	1	Motor moves right
0	1	1	0	Motor moves left
0	0	0	0	Motor free runs
0	1	0	1	Motor brakes

Table 1: H-Bridge Summarize Function

### 2.1.3 Infrared Radiation

Infrared (IR) radiation is electromagnetic radiation of a wavelength longer than that of visible light, but shorter than that of microwaves. The name means "below red" (from the Latin infra, "below"), red being the color of visible light with the longest wavelength. Infrared radiation has wavelengths between about 750 nm and 1 mm, spanning five orders of magnitude. Humans at normal body temperature can radiate at a wavelength of 10 microns.

Infrared light lies between the visible and microwave portions of the electromagnetic spectrum. Infrared light has a range of wavelengths, just like visible light having wavelengths that range from red light to violet. "Near infrared" light is closest in wavelength to visible light and "far infrared" is closer to the microwave region of the electromagnetic spectrum. The longer, far infrared wavelengths are about the size of a pin head and the shorter, near infrared ones are the size of cells, or are microscopic.

Shorter and near infrared waves are not hot at all - in fact you cannot even feel them. These shorter wavelengths are the ones in TV's remote control [7].

The Infrared emitter detector circuit is very useful to make a line following robot, or a robot with basic object or obstacle detection. Infrared emitter detector pair sensors are fairly easy to implement, although involved some level of testing and calibration to get right. They can be used for obstacle detection, motion detection, transmitters, encoders, and color detection (such as for line following) [8].

#### 2.1.4 Light Sensor

One of the functions of the robot is when it detects that there is no lights, it will stop every movement and blink a set of LEDs. Light Dependent Resistor (LDR) is a light sensor that can be used for this function.

#### 2.1.4.1 Light Dependent Resistor (LDR)

LDRs or Light Dependent Resistors are very useful especially in light/dark sensor circuits. Normally the resistance of an LDR is very high, sometimes as high as 1000 000 ohms, but when they are illuminated with light, resistance drops dramatically.

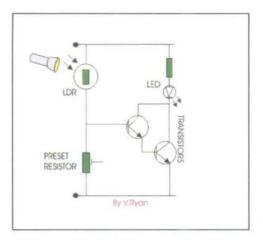


Figure 7: LDR Circuit

The figure above shows a simple LDR circuit with transistors and LEDs. When the light level is low the resistance of the LDR is high. This prevents current from flowing to the base of the transistors. Consequently the LED does not light. However, when light shines onto the LDR its resistance falls and current flows into the base of the first transistor and then the second transistor. The LED lights. The preset resistor can be turned up or down to increase or decrease resistance, in this way it can make the circuit more or less sensitive [4].

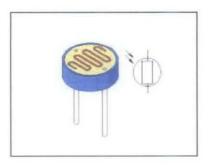


Figure 8: Light Dependent Resistor

#### 2.2 Mechanical Movement

The most important mechanical aspects of this robot is it is constructed so it can perform the basic functions of a robot which is moving forward, reverse and turn. In the early stage of this project, two types of robot are considered which are the legged type and the wheeled type. The author has done studies on the mechanical attachment of these two types of robot and the advantages and disadvantages are considered. For legged robot, it is more sophisticated compared to a wheeled robot. It will required much more control and more motors. It will require a more complex circuit to give it more control over the legs. Legged robot is useful in unlevel terrains where as wheeled is robot require more control to establish stability. The ultimate problem with legged robot is balancing. The robot's body and legs need to be designed to achieve proper balancing to ensure stability during its different movements.

The wheeled robot requires a simpler circuit and minimum number of motors can be used. It is much faster and easier to balance compared to a legged robot. It is less complicated to build and requires less control.

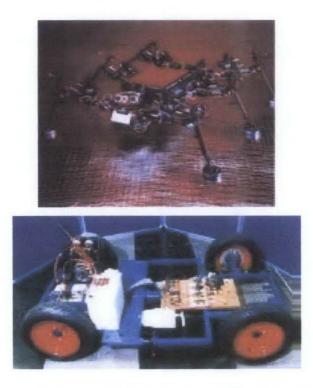


Figure 9: Legged Robot (Left) and Wheeled Robot (Right)

The figure 9 shows a six legged robot (right) and a four wheeled robot (left). Most legged robot is built on six legs to achieve maximum stability and most wheeled robot are built with four wheels. The figure shows the difference of construction complexity between a legged robot and a wheeled robot.

## 2.2.1 Four Wheeled Robot

A four wheeled robot is suitable to build a pet robot. When deciding to build a four wheeled robot, the author needs to decide the different mechanical wheel connection of the pet robot.

1. Using one motor to connect all wheels.

In this method, all four wheels are dependent on one another. There will be only one motor controlling all the wheels at one time. The functions therefore will only be limited to move forward and backward. The robot will not be able to make a turn.

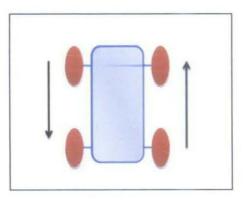
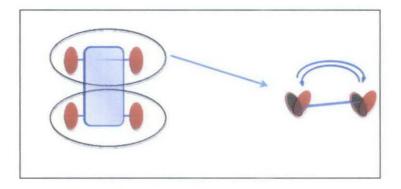


Figure 10: Wheel robot with one motor

2. Using two motors for four wheels, each front and back.

In this method, one motor is connected to two wheels. Two wheels in the front are connected to one motor and the other two at the back to another motor. The wheels at the back are connected to the motor in a function that it moves forward and backward. As for the front two wheels it is connected in such a way that it can move forward and backward and the angular position of the motor can be changed. This can cause the robot to be able to make a turn.





3. Two motors, side by side

In this method, one motor is connected to two wheels. Two wheels at one side will be connected to a motor and the other will be connected to a different motor. These two sets of wheels will move independently. By having this connection, the robot is able to move forward, reverse and turn. It can turn to a large radius by having one side of the wheels to move and the other is not.

It is also able to turn in a smaller radius by having one side of the wheels to move forward and the other side to move in reverse.

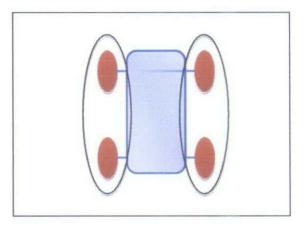


Figure 12: Wheel robot with two motors (left,right)

## 2.3 Printable Circuit Board (PCB)

A printed circuit board, or PCB, is used to mechanically support and electrically connect electronic components using conductive pathways, or traces, etched from copper sheets laminated onto a non-conductive substrate. Alternative names are printed wiring board (PWB), and etched wiring board. A PCB populated with electronic components is a printed circuit assembly (PCA), also known as a printed circuit board assembly (PCBA).

PCBs are rugged, inexpensive, and can be highly reliable. They require much more layout effort and higher initial cost than either wire-wrapped or point-to-point constructed circuits, but are much cheaper, faster, and consistent in high volume production. Much of the electronics industry's PCB design, assembly, and quality control needs are set by standards that are published by the IPC organization [9].

#### 2.3.1 Patterning (etching)

The vast majority of printed circuit boards are made by bonding a layer of copper over the entire substrate, sometimes on both sides, (creating a "blank PCB") then removing unwanted copper after applying a temporary mask (eg. by etching), leaving only the desired copper traces. A few PCBs are made by adding traces to the bare substrate (or a substrate with a very thin layer of copper) usually by a complex process of multiple electroplating steps [9].

There are three common "subtractive" methods (methods that remove copper) used for the production of printed circuit boards:

1. Silk screen printing uses etch-resistant inks to protect the copper foil. Subsequent etching removes the unwanted copper. Alternatively, the ink may be conductive, printed on a blank (non-conductive) board. The latter technique is also used in the manufacture of hybrid circuits.

2. Photoengraving uses a photomask and chemical etching to remove the copper foil from the substrate. The photomask is usually prepared with a photoplotter from data produced by a technician using CAM, or computer-aided manufacturing software. Laser-printed transparencies are typically employed for phototools; however, direct laser imaging techniques are being employed to replace phototools for high-resolution requirements.

3. PCB milling uses a two or three-axis mechanical milling system to mill away the copper foil from the substrate. A PCB milling machine (referred to as a 'PCB Prototyper') operates in a similar way to a plotter, receiving commands from the host software that control the position of the milling head in the x, y, and (if relevant) z axis. Data to drive the Prototyper is extracted from files generated in PCB design software and stored in HPGL or Gerber file format. [9]

2.3.2 Lamination

Some PCBs have trace layers inside the PCB and are called multi-layer PCBs. These are formed by bonding together separately etched thin boards.[9]

#### 2.3.3 Drilling

Holes, or vias, through a PCB are typically drilled with tiny drill bits made of solid tungsten carbide. The drilling is performed by automated drilling machines

with placement controlled by a drill tape or drill file. These computer-generated files are also called numerically controlled drill (NCD) files or "Excellon files". The drill file describes the location and size of each drilled hole.

When very small vias are required, drilling with mechanical bits is costly because of high rates of wear and breakage. In this case, the vias may be evaporated by lasers. Laser-drilled vias typically have an inferior surface finish inside the hole. These holes are called micro vias.

It is also possible with controlled-depth drilling, laser drilling, or by pre-drilling the individual sheets of the PCB before lamination, to produce holes that connect only some of the copper layers, rather than passing through the entire board. These holes are called blind vias when they connect an internal copper layer to an outer layer, or buried vias when they connect two or more internal copper layers and no outer layers.

The walls of the holes, for boards with 2 or more layers, are plated with copper to form plated-through holes that electrically connect the conducting layers of the PCB. For multilayer boards, those with 4 layers or more, drilling typically produces a smear comprised of the bonding agent in the laminate system. Before the holes can be plated through, this smear must be removed by a chemical de-smear process, or by plasma-etch.[9]

## **CHAPTER 3**

# **METHODOLOGY/PROJECT WORK**

The robot was built step- by-step through different sections. Figure 13 shows the flow diagram of the building the robot.

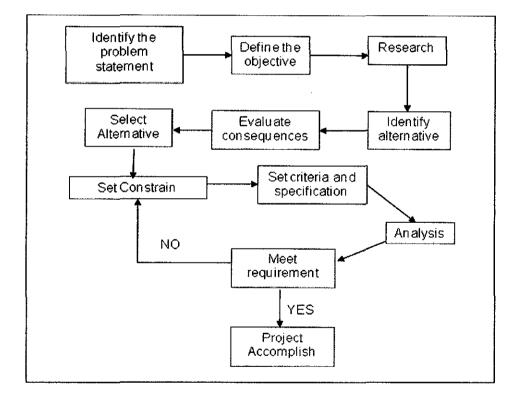


Figure 13: Project Flow Diagram

Process of designing the robot consists of three different sections, the body (mechanical parts), circuit (electrical) and programming (PIC16F84A).

#### 3.1 The Body

The bottom part of the body is very important to determine how the robot will be able to move. The options of weather to build a legged robot or wheeled robot was laid out in theory and after all advantages and disadvantages has been considered, the author has decided to build a wheeled robot. In this project, the auther will not be building the robots body but will use the base of the robot from what is available on the market and reconstruct its circuitry to function as required.

#### 3.1.1 Wheeled Robot

The robot is designed to be a wheeled robot. This is because robots with wheels are able to move faster and has better balancing control compared with legged robots. This is type of mechanical connection is suitable for this project as common pets are usually fast and need to have better stability to be able to move in different ways. The robot will have 4 wheels controlled by two motors. Each motor will control two sets of wheels independently. Two wheels are placed in front and the other two at the back. Figure 14 shows the motor connections to the wheels:

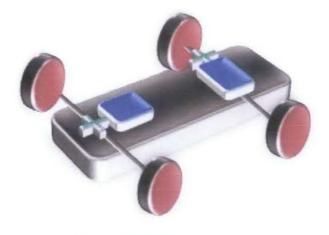


Figure 14: Motor Connection

The mechanical connection of the back wheels to its motor will enable the robot to move forward and reverse. The wheels will move forward when the motor is connected to the positive voltage and reverse when the motor is connected to negative voltage. The positive and negative voltage input to the motor can be controlled by the PIC. The mechanical connection of the front motor to its wheels will enable the wheels to change angle and thus allowing the robot to move right or left. Whether the wheels are to turn left or right is once again controlled by the positive input voltage to the motor.



Figure 15: Front Wheels and Back Wheels

## 3.2 Designing the Circuit

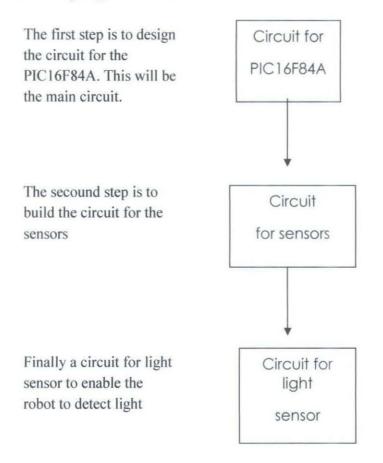
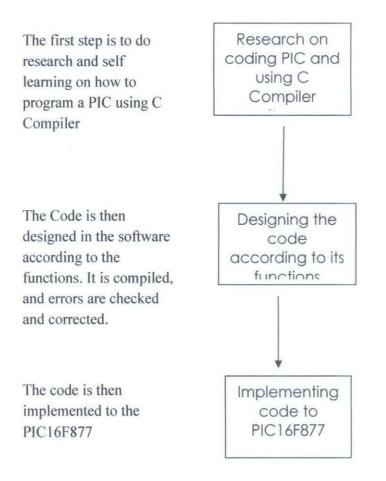


Figure 16: Designing the Circuits flow Diagram

## 3.3 Programming the PIC



## Figure 17: Programming the chip flow diagram

## 3.3.1 PIC C Compiler Software

There are many soft wares used to program a PIC. In this project, the PIC C compiler Software is used to program the PIC 16F877. This software is chosen because it is easy to work with and is user friendly.

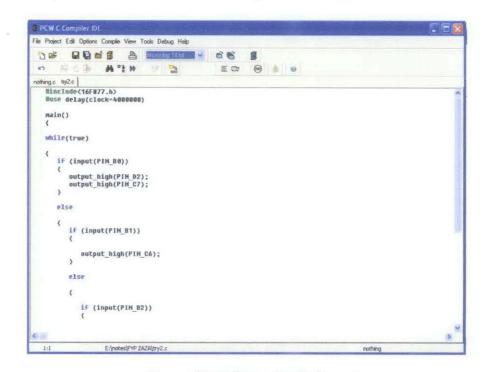


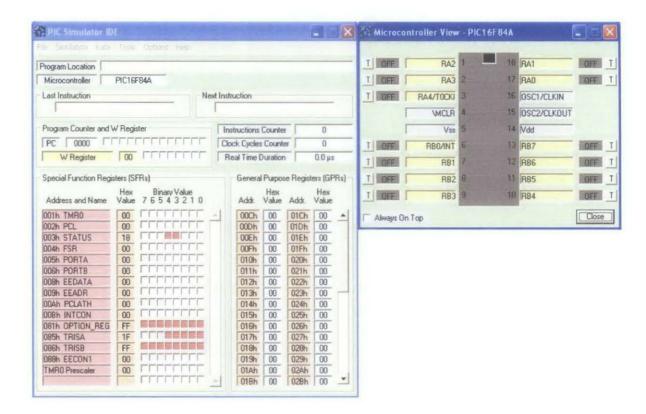
Figure 18: C Compiler Software

The figure 18 shows the interface of PIC C Compiler. The code is written and designed in the workspace area.

After the code is completed, it is compiled. Codes with errors are then analyzed and corrected.

The successfully compiled codes need to be tested whether it functions properly before it is implemented onto the chip. This is done by using software called PIC

Simulator IDE. The code is loaded into this software and the simulation. The inputs and outputs of the microcontroller can be viewed from the virtual microcontroller view window and any errors from the coding can be detected.



## Figure 19: PIC Simulator IDE

Figure 19 shows the interface of PIC simulator IDE. By using the PIC simulator IDE, errors can be detected easily and time can be saved. This is because the codings are checked before implementing them onto the PIC.

After the code is checked with the PIC Simulator IDE, it now can be implemented onto

÷ • 1		1 1		* *	5	6 6	E	10	30425	18	-	3
Address	- Progra	m Code									Configuration	
0000:	OOFF	OOFF	OOFF	OOFF	OOFF	OOFF	OOFF	OOFF	*****	-		
0008:	OOFF	OOFF	OOFF	OOFF	DOFF	OOFF	OOFF	OOFF	YYYYYYYY			
8018:	OOPF	OOFF	OOFP	OOFF	DOFF	OOFF	OOFF	OBFF	YYYYYYYY			
0018:	OOFF	BBFF	OOFF	OOFF	DOFF	OUFF	OOFF	OOFF	YYYYYYYY			
0020:	OOFF	BOFF	OOFF	OOFF	OOFF	OOFF	OOFF	OOFF	<u> </u>			
0028:	OOFF	OOFF	OOFF	OOFF	OOFF	OOFF	OOFF	OOFF	******			
0030:	OOFF	SOFF	OOFF	OOFF	BOFF	SOFF	OOFF	OOFF	******			
0038:	OOFF	BOFF	OOFF	OOFF	OOFF	OOFF	OOFF	OOFF	YYYYYYYY			
0040:	OOFF	OOFF	OOFF	OOFF	DOFF	OOFF	OOFF	OOFF	YYYYYYYY			
8048:	OOFF	OOFF	OOFF	OOFF	OOFF	OOFF	OUFF	OOFF	*******			
0050:	OOFF	OOFF	OOFF	OUFF	OOFF	BOFF	OOFF	OOFF	YYYYYYYY			
0058:	OOFF	DOFF	OOFF	OOFF	OOFF	OUFF	OOFF	OOFF	*******			
0060:	OOFF	OOFF	OOFF	OOFF	OOFF	OOFF	OOFF	OOFF	*****			
0068:	OOFF	OOFF	OOFF	OOFF	DOFF	OOFF	OOFF	OUFF	YYYYYYYY			
0070:	OOFF	OOFF	OOFF	OOFF	OOFF	OOFF	OOFF	OOFF	YYYYYYYY			
0078:	OOFF	OOFF	OOFF	OOFF	OOFF	OOFF	OOFF	OOFF	*******			
0080:	OOFF	OOFF	OOFF	OOFF	OOFF	OOFF	OBFF	OOFF	*******			
8880	OOFF	OOFF	OOFF	OOFF	OOFF	OOFF	OOFF	OOFF	*******			
0090:	OOFF	OOFF	OOFF	OOFF	OOFF	OOFF	OOFF	OUFF	YYYYYYYY		Checksum	
8098:	OOFF	OOFF	OOFF	OUFF	OOFF	OOFF	OOFF	OOFF	******		and the second se	
00A0:	OOFF	OOFF	OOFF	OOFF	OOFF	COFF	OOFF	OOFF	XXXXXXXXX	- 220	FEOO	
- 8400	OULE	-	OULD	ORFW	ORFF	ONFF	-	ORFE	******	M		

Figure 20: PIC Programming Software

the microcontroller. This is done by using a PIC Programming Software (as shown in figure 20) and a universal PIC programming device. The code that have been compiled and checked is loaded to the PIC programming software. The chip is inserted in the PIC programming device as shown below:

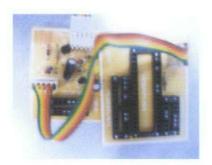


Figure 21: PIC Programming Device

The microcontroller chip is now programmed and is ready to be used in the main circuit of the robot.

## 3.4 Implementing the Circuits

After collecting all the components for the circuits, it can be implemented to the circuit board. For initial implementation for designing and testing, the components are soldered onto a simple breadboard. Although this circuit board is functioning correctly and successfully, it is not a very stable circuit. Short circuits can occur because the cuprum line is connected all over the board. This problem can be solved by implementing the circuits onto a printable circuit boards (PCB)

## 3.4.1 Printable Circuit Board Method

Creating a PCB involves 2 main process. The first process involves using a software called Eagle 4.13 Light. The schematic drawing of the designed circuit is first created using this software. The figure 22 is an example of a schematic drawing using Eagle software:

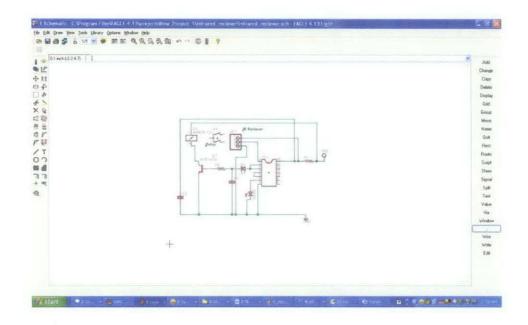


Figure 22: Schematics Drawings in Eagle 4.13 Light Software

After all the components are in place, the next step is to convert the drawings to a virtual circuit board as shown below. Here all the components will be arranged in a way where no wires can cross each other and cause a short circuit. The software will automatically create a route for the path of the cuprum line which will be etched later.

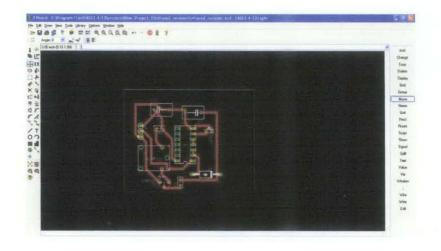


Figure 23: PCB Route Designing

The second process involves drilling holes and etching cuprum onto the circuit board. This will be done at the lab assisted by the technician on duty.

## **CHAPTER 4**

## **RESULTS & DISCUSSIONS**

## 4.1 RESULTS

After considering the theories and methods of each section of the robot, the author has implemented and design the to robot to meet the objective of this project.

# START IDLE IDLE ISLIGHT VES VES VES REVERSE TURN FORWARD

## 4.1.1 Robot Function Flow Diagram

Figure 24: Function Flow Diagram

Figure 24 shows a flow diagram designed to lay out the functions of the robots. It consists of the main and basic functions of the robot. The codings designed will be base on this flow diagram.

Firstly, the robot is started. This consists of turning on the power switch of the robot. An LED indicator which is placed on the main circuit will light up to indicate that it has been powered up.

Next, the robot will be in idle mode. This means that the robot will delay any movements for a few seconds before performing any functions. This mode is important to ease the flow of the next step.

The robot will now check for the first condition that is to check weather there is any light. If there is light, then the robot will go to a mode where a number of LEDs will blink. The LEDs will blink until there is light turned on and move on to the next step function.

If there is no light detected, the robot will then check for the next condition which is to check weather there is an object blocking its way in front of it. If there is an object, the robot will reverse, turn and move forward avoiding the object.

If there is no object detected, the robot will then go to having random movements mode. In this mode, the robot will constantly check weather there is light detected and object detected and interrupt the random movement when one of the condition is met.

## 4.1.2 Main Circuit

## 4.1.2.1 Microcontroller PIC16F84A Circuit

Figure below shows the pins of the PIC 16F84A. Pin 14 (VDD) and pin 4 (MCLR) is connected to 5V. Pin 5 (VSS) is connected to ground. Pin 15 and 16 (OSC1&2) is connected to a 4MHz two legged clock which will run the circuit. The outputs that will connect to the motor circuit are B4,B5, B6, and B7. Pin B4 and B5

is connected to the back motor. Pin B5 will allow negative voltage to flow to the motor which will make the wheels turn in reserve mode. Pin B4 will allow positive voltage to flow to the motor which will make the wheels turn in forward mode. Pin B6 and B7 is connected to the front motor which will determine weather the robot is to turn left or right. Pin B6 will allow positive voltage to flow through the motor and enables the wheel to turn right while pin B7 enables flow of negative voltage through the motor and enables the wheel to turn left. An LED is connected to pin B3 as an 'ON' indicator. This LED will light up when the circuit is connected to a power supply.

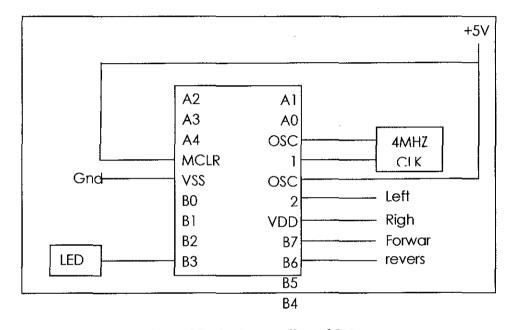


Figure 25: Pin Connection of PIC 16F84A

The circuit diagram is then implemented to a circuit board. All the components are soldered accordingly. Figure below shows the completed main circuit:

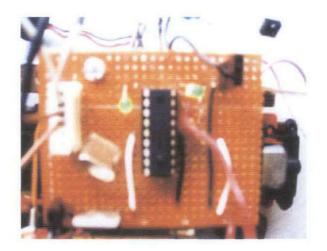


Figure 26: Main Circuit

When the circuit is successfully connected, a simple program is encoded to the PIC to test the movement of the robot. Below shows a simple coding used to test the robot.

```
main()
{
while(true)
1
   output high(PIN B3);
                                   //LED ON indicator lights up
   output_low(PIN_B4);
                                   //reverse mode off
                                   //forward mode on
   output high(PIN B5);
   output low(PIN B6);
                                   //right mode off
                                   //left mode off
   output_low(PIN_B7);
   delay ms(5000);
                                 // delay for 5 secs
   output high(PIN B3);
                                   //LED ON indicator lights up
                                   //reverse mode on
   output_high(PIN_B4);
   output_low(PIN_B5);
                                   //forward mode off
                                   //right mode off
   output_low(PIN_B6);
                                   //left mode off
   output low(PIN B7);
   delay_ms(5000);
                                 //delay for 5 secs
   output_high(PIN_B3);
                                   //LED ON indicator lights up
   output_low(PIN_B4);
                                   //reverse mode off
   output_low(PIN_B5);
                                   //forward mode off
   output_high(PIN_B6);
                                   //right mode on
                                   //left mode off
   output_low(PIN_B7);
                                 //delay for 5 secs
   delay ms(5000);
                                   //LED ON indicator lights up
   output high(PIN B3);
                                   //reverse mode off
   output low(PIN B4);
                                   //forward mode off
   output low(PIN B5);
   output low(PIN B6);
                                   //right mode on
                                   //left mode off
   output high(PIN_B7);
   delay_ms(5000);
                                 //delay for 5 secs
}
}
```

After the code is implemented to the pic, the robot seems to function correctly according to the code. This shows the implementation of the main circuit is successful.

## 4.1.3 PIC Programming

Below is the coding that has been redesigned. Sub functions has been included inside the previous coding to simplify it. An initial start-up sequence was also inserted to check the initial condition of the robot when it is turned on before starting any other functions. The initial start-up sequence will check each forward, reverse, left and right mode of the robot is fully functioning.

#include<16F84a.h>
#use delay(clock=4000000)
#fuses XT,NOPROTECT,NOWDT

//all subfunctions
void all\_pause(void);
void delay\_gap(void);
void intervention(void);

//start main function
main()
{
//INITIATE START-UP SEQUENCE

output_high(PIN_B3);	//B3 is POWER indicator LED ON
output_low(PIN_B4);	//B4 is LEFT
output_high(PIN_B5);	//B5 is RIGHT
output_low(PIN_B6);	//B6 is FORWARD
output_low(PIN_B7);	//B7 is REVERSE
delay_ms(200);	//DELAY BEGIN AFTER STARTUP
output_low(PIN_B3);	<pre>//B3 is POWER indicator LED ON</pre>
output_high(PIN_B3);	<pre>//B3 is POWER indicator LED ON</pre>
output_low(PIN_B4);	//B4 is LEFT
output_low(PIN_B5);	//B5 is RIGHT

output_low(PIN_B6);	//B6 is FORWARD
output_low(PIN_B7);	//B7 is REVERSE
delay_ms(200);	//DELAY BEGIN AFTER STARTUP
output_high(PIN_B3);	<pre>//B3 is POWER indicator LED ON</pre>
output_low(PIN_B3);	<pre>//B3 is POWER indicator LED ON</pre>
output_low(PIN_B4);	//B4 is LEFT
output_low(PIN_B5);	//B5 is RIGHT
output_high(PIN_B6);	//B6 is FORWARD
output_low(PIN_B7);	//B7 is REVERSE
delay_ms(100);	//DELAY BEGIN AFTER STARTUP
output_low(PIN_B3);	<pre>//B3 is POWER indicator LED ON</pre>
output_low(PIN_B3);	<pre>//B3 is POWER indicator LED ON</pre>
output_low(PIN_B4);	//B4 is LEFT
output_low(PIN_B5);	//B5 is RIGHT
output_low(PIN_B6);	//B6 is FORWARD
output_high(PIN_B7);	//B7 is REVERSE
delay_ms(100);	//DELAY BEGIN AFTER STARTUP

This coding will create an initiate start up sequence. In this sequence, the robot will first check weather all of the wheels movements are working. It will command the back wheels to go forward, then to go reverse. This is to check the functionality of the back wheels. Then it will check the functionality of the front wheels and command the wheels to go right first and then left.

```
//START LOOP
while(true)
{
output high(PIN B3);
                         //B3 is POWER indicator LED ON
if (INPUT(PIN_B2))
Ł
intervention();
}
else
{
   //FORWARD LEFT FOR 5 MILLISECONDS
   output high(PIN B4);
   output_low(PIN_B5);
   output_high(PIN_B6);
   output_low(PIN_B7);
```

delay\_ms(700);

all\_pause();

```
//REVERSE RIGHT FOR 5 MILLISECONDS
output_low(PIN_B4);
output_high(PIN_B5);
output_low(PIN_B6);
output_high(PIN_B7);
delay_ms(400);
```

all\_pause();

```
//FORWARD LEFT FOR 5 MILLISECONDS
output_high(PIN_B4);
output_low(PIN_B5);
output_high(PIN_B6);
output_low(PIN_B7);
delay_ms(700);
```

This is the loop function that will make the robot go forward for 5 milisecounds and stop for 5 milliseconds repeating constantly. This will make the robot move slower that just making it go forward without stopping for a few seconds. The speed of the robot is controlled by using this coding method.

```
void delaygap()
{
```

```
output_high(PIN_B3);
output_low(PIN_B4);
output_low(PIN_B5);
output_low(PIN_B6);
output_low(PIN_B7);
delay_ms(200);
```

}

This is the sub function for stopping all movements for 200 milliseconds.

```
void all_pause()
{    output_low(PIN_B4);
    output_low(PIN_B5);
    output_low(PIN_B6);
    output_low(PIN_B7);
    delay_ms(500);
```

}

This is the sub function for pausing all movements for 5 miliseconds.

```
void intervention()
{
    output_high(PIN_B4);
    output_low(PIN_B5);
    output_low(PIN_B6);
    output_low(PIN_B7);
    delay_ms(200);
    output_low(PIN_B4);
    output_low(PIN_B5);
    output_low(PIN_B5);
    output_low(PIN_B7);
    delay_ms(200);}
```

This is the sub function that will put a high output to the forward mode of the robot.

## 4.1.4 Light Sensor Circuit

When the LDR detects light, it will give a signal to the PIC and the PIC will command the robot to perform its normal function such as moving forward, backward and turning under certain conditions. But if the photocell does not detect any light, the PIC will command the robot to be in idle mode while blinking the LED. Below is the diagram of the light sensor circuit:

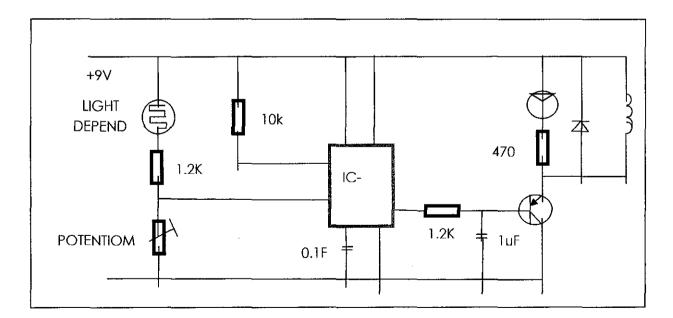


Figure 27: Light Sensor Circuit

40

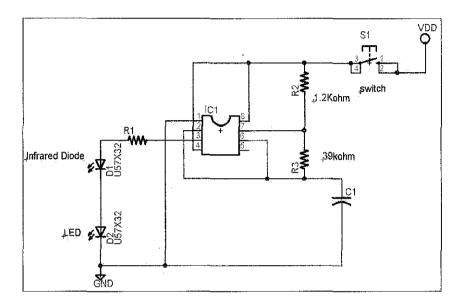
When LDR detects light, a signal will be sent to a relay. This signal will trigger the relay. The output of the relay is then connected to the main circuit which contains the pic. Pin B2 is assigned as an input to the pic and is then connected to the output of the relay. By this connection, when ever the LDR detects light, a an input signal will be sent to the pic. By using programming, the pic can be set to control the function that the robot should perform when there light. Below is a simple code program that shows an output function when the LDR sends a signal to the input pic indicating there is light. This function uses an 'If Else' function in the programming. If there is an input signal in pin B2, the output signal B4 will be high. Else, B4 will be low. This means if there is light, the back wheels will move forward. If there is not light, the robot will not move at all.

```
main()
{while(true)
£
 output high(PIN_B3);
  if (input(PIN_B2))
                                        //if there is light
  ł
   output high(PIN B4);
                                 //forward mode on
  }
 else
                                //if there is no light
 ł
                                //forward mode off
     output low(PIN B4);
  }
}
}
```

After implementing the following program to the circuit, the robots seems to function as instructed in the coding. this shows the circuit successfully detects the input from the LDR when there is light and gives command to the output to perform certain functions.

## 4.1.5 Infrared Transmitter and Receiver

In order for the robot to detect object obstacles, an infrared circuit will be implemented to the robot. The infrared circuit consists of sender circuit which contains the infrared emitter diode and receiver circuit which consists of infrared receiver device. Below is a circuit of the infrared circuit sender and receiver.





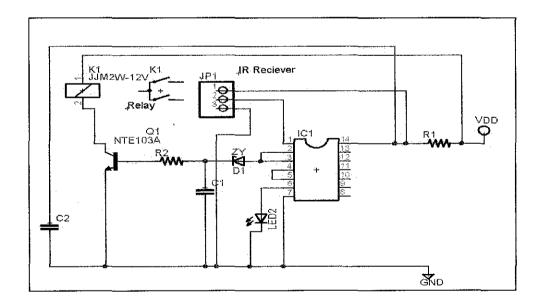


Figure 29: Infrared Receiver Circuit

Both of these circuits will be placed in front of the robot. It is powered up by an on switch placed at the side of the robot. The transmitter circuit contains the infrared diode and will emit the infrared wave. The infrared wave can not be seen by the human eye. There for an LED in placed to this circuit to indicate that the infrared diode is emitting the waves. When there is an object in front of it, the infrared wave will hit the object, reflecting it and making it bounce back hitting the receiver circuit which contains the infrared receiver component. This will then trigger the relay in the receiver circuit which acts as a switch allowing current to flow through and signal to pass by to the PIC. The PIC will then send a signal to the appropriate output to change the movement of the robot to avoid from bumping into the object.

## 4.1.6 Overall circuit connection

When all the circuits are finished, they are all connected and integrated together to form a robot. Below is a diagram of the overall connection:

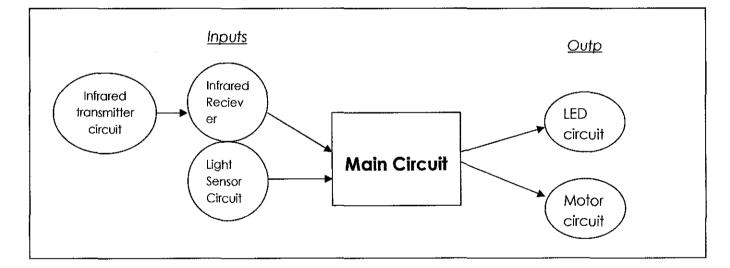
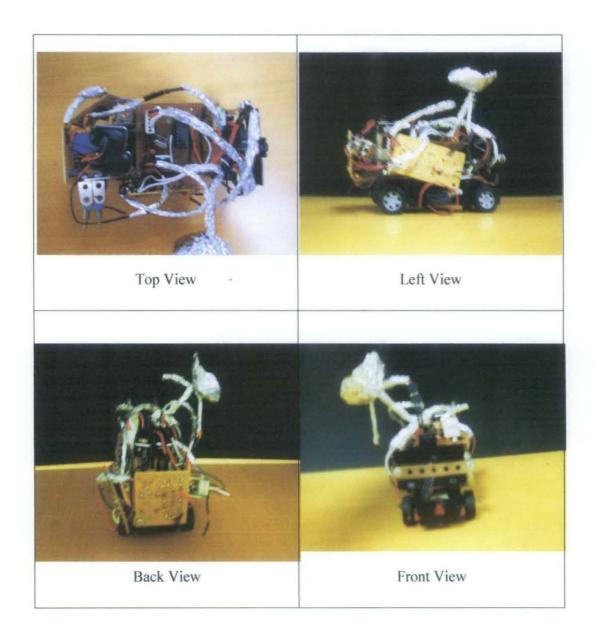


Figure 30: Overall Circuit Connection

All inputs and outputs will be connected to the main circuit. The input circuits are the infrared receiver circuit and the light sensor circuit. The infrared transmitter circuit is connected to the infrared receiver circuit. The output circuits are the LED circuits and the motor circuit which will then be connected to the wheels.

## 4.1.6 Pictures of Pet Robot



## 4.2 Discussion

## 4.2.1 Speed Control

When the first code was implemented to the robot, the author noticed that the robot was moving very fast with a constant speed. The robot like pets, must be able to move slowly or very fast every now and then. The speed needs to vary differently to give some characteristics an essence of life. It is also important to slower down the speed in situations where a condition need to be checked. For example, the robot needs to move slowly before checking for object obstacles else it will end up bumping into it. To control the speed, the author needs to implement a coding to the PIC which will slow down the signal sent out to the motor which controls the wheels. Each time a high signal is sent to an output of the wheels, it will be followed by a low signal to the same output and delay it for a certain time. This is repeated for a number of times. For example, the speed control for forward mode is as below:

output_high(PIN_B5);	//high output to forward pin
delay_ms(100);	//forward mode for 0.1 secs
output_low(PIN_B5); //lov	w output to forward pin
delay_ms(100);	//delay stop for 0.1 secs

Implementation of this code to the chip will cause the forward wheels to run for 0.1 second and stop running for 0.1 second. When this cycle is repeated constantly, it will make the robot seem to be moving smoothly but at a slower rate then by just making the signal high all the time.

## 4.2.2 Circuit's Stability and Sensitivity

During troubleshooting, all the components were connected onto a regular simple breadboard. After the circuits are finalized, it is then transferred onto a veraboard which is a type of board that has cuprum connected all over the circuit. This board requires soldering of the components onto the board. When this is done, all the circuits are then put together and tested. The author later notices that the circuit is not very stabile and is very sensitive as the connection of cuprum under the board can easily cause a short circuit which can cause failure to the robot's functions and can also cause chips and components to burn. This is later avoided by transferring these circuits on to a printable circuit board which has better connections and the probability of a short circuit is less compared to a vera board. The main circuit remains connected to a veraboard. This is because by using this board, additional inputs and outputs can easily be implemented without changing the main circuits. This makes the robot more flexible and provides a wider range of possibilities to its functions.

## 4.2.3 Light sensor sensitivity

A light sensor is implemented onto the robot to enable it to detect light. A variable resistor is placed in the circuit to adjust the sensitivity of the LDR. Although the variable resistor helps adjust the robot to react to how much brightness and darkness, there is still a problem when the robot is a situation where it's not so bright and not so dark. At this point the robot will confuse and start to do movements in dark mode and bright mode alternately. The robot can only work in a condition where it is purely bright or purely dark.

## 4.2.4 Reprogrammable chip

This robot uses a microcontroller PIC 16F84A. This PIC can be reprogrammed over and over again. This feature allows can us to change the movement of the robot from time to time. Changes can be made easily by just altering the codes and implement them back onto the PIC. The robot is more flexible on its functions and features. It can be enhanced by adding more inputs and outputs. Additional circuits could be easily added without building a new main circuit or changing other existing circuits. More intelligence could be added that enables it to meet certain goals depending on its purposes.

## **CHAPTER 5**

## **CONCLUSION & RECOMMENDATIONS**

## 5.1 Conclusion

The robot is now able to perform all the basic functions such as moving forward, reverse and turn. The main circuit which contains the microcontroller PIC 16F84A is built and implemented onto the robot that controls all functions, it is connected to the motor circuit and is able to move the mechanical movement of the robot. This circuit is also able to detect input signals by other external circuits and trigger the appropriate output according to the codes designed. The codes are designed to move the basic movements of the robot and conditions have been implemented to enable the robot to make decisions according to certain inputs. The circuit of the light sensor is designed, built and implemented onto the robot to enable it to detect light. When light is detected, a signal will be sent to the PIC of the main circuit and the PIC will instruct the robot to react to it. The robot is able to differentiate between brightness and darkness. The infrared sensor has also been designed, built and implemented onto the robot. it is placed in front of the robot to detect object in front of it. The robot is able to detect object blocking its way about one feet away. All the circuits to achieve the objective of this project is now completed and running successfully. The robot is now able to move forward, reverse, turn, detect and avoid object obstacles, move randomly and sense

light. Enhancement of the coding will be done which will add more feature to the robot and make it more similar to a pet robot. Once this coding is completed, the overall coding can be finalize and thus implemented onto the robot to finalize the final product of the Pet Robot.

## 5.2 Recommendations

The objective of this project is to built a basic robot which imitates the behavior of a pet. The robot is built through designing basic circuits to perform basic functions. This topic of a pet robot is very wide and general. The functions and purposes of this pet robot is not limited to a specific standard. Improvements and enhancements can be easily done by adding more circuits to implement more inputs and outputs of the robot making it more sophisticated and interesting. The PIC used is a PIC16f84A which is a reprogrammable chip. This enables us to alter the codings of the robot to change its movements and add inputs and output easily. The characteristics and behavior of this robot can be erased and a new personality can be implemented to it. Other features such as sound detecting and sound making can be implemented later to make the pet robot more real and give it more life.

This PIC 16f84A can be changed to a PIC16F877 which has the same function but with more input and output pins. This enables us to add more circuits and widen the robots functions and features by using the same coding designed in PIC 16f84A.

More sensors can be added to the robot to give it more intelligence to on avoiding bumping into objects. The infrared sensor can be implemented not only at the front of the robot but also at the back to ensure the robot does not crash into anything while it is in reverse mode.

## REFERENCES

- 1. http://en.wikipedia.org/wiki/Voltage\_regulator
- 2. http://en.wikipedia.org/wiki/Crystal\_oscillator
- 3. http://en.wikipedia.org/wiki/H-bridge
- 4. http://www.technologystudent.com/elec1/ldr1.htm
- 5. www.reuk.co.uk
- 6. en.wikipedia.org/wiki/Infrared
- 7. http://science.hq.nasa.gov/kids/imagers/ems/infrared.html
- 8. http://www.societyofrobots.com/schematics\_infraredemitdet.shtml
- 9. http://en.wikipedia.org/wiki/Circuit\_board
- 10. Macmillan English Dictionary Fundamental Student Edition
- 11. http://links999.net/robotics/robots/robots\_introduction.html
- 12. http://inventors.about.com/library/inventors/blrobots.htm
- 13. http://www.geocities.com/siliconvalley/2072/3pinvolt.htm

APPENDIX



# **PIC16F84A**

# 18-pin Enhanced FLASH/EEPROM 8-Bit Microcontroller

## High Performance RISC CPU Features:

- Only 35 single word instructions to learn
- All instructions single-cycle except for program branches which are two-cycle
- Operating speed: DC 20 MHz clock input DC - 200 ns instruction cycle
- 1024 words of program memory
- 68 bytes of Data RAM
- 64 bytes of Data EEPROM
- 14-bit wide instruction words
- · 8-bit wide data bytes
- 15 Special Function Hardware registers
- · Eight-level deep hardware stack
- Direct, indirect and relative addressing modes
- · Four interrupt sources:
  - External RB0/INT pin
  - TMR0 timer overflow
  - PORTB<7:4> interrupt-on-change
  - Data EEPROM write complete

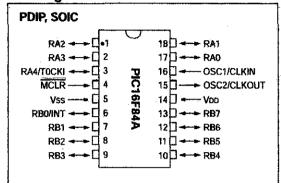
## **Peripheral Features:**

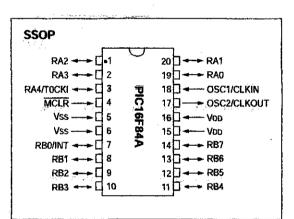
- 13 I/O pins with individual direction control
- High current sink/source for direct LED drive
  - 25 mA sink max. per pin
  - 25 mA source max, per pin
- TMR0: 8-bit timer/counter with 8-bit programmable prescaler

## **Special Microcontroller Features:**

- 10,000 erase/write cycles Enhanced FLASH Program memory typical
- 10,000,000 typical erase/write cycles EEPROM Data memory typical
- EEPROM Data Retention > 40 years
- In-Circuit Serial Programming<sup>™</sup> (ICSP<sup>™</sup>) via two pins
- Power-on Reset (POR), Power-up Timer (PWRT), Oscillator Start-up Timer (OST)
- Watchdog Timer (WDT) with its own On-Chip RC Oscillator for reliable operation
- Code protection
- Power saving SLEEP mode
- Selectable oscillator options

## Pin Diagrams





## CMOS Enhanced FLASH/EEPROM Technology:

- · Low power, high speed technology
- Fully static design
- Wide operating voltage range:
- Commercial: 2.0V to 5.5V
- Industrial: 2.0V to 5.5V
- Low power consumption:
  - < 2 mA typical @ 5V, 4 MHz</li>
  - 15 μA typical @ 2V, 32 kHz
  - < 0.5 µA typical standby current @ 2V</li>

© 2001 Microchip Technology Inc.

DS35007B-page 1

Data type	
	la types es end unions
	ze vs Data Types
<u>Volatile</u> Static	
Constau	
Strings	
Variables	Man Manual Manual and Anna and
	r mapped variables
	<b>11</b>
Аптаув	
Pointera.	۲
Coerator	as function grownents
	<b></b>
Arithme	fc Operator Exemples
	1001
	neni Operator Examples
	ison Operator Exemples
	Operator Examples
	Operator Ezemples
	nel Examples
Proprar	n Flow Examples
inline and	embly
<u>3\$77)</u>	Referencing in esm
	sembly example 1
	sambly exemple 2
Intine a	sembly example 3
	19
unction	
	nctions
	Innctions and internals
	momory management
ลกฉนลอง	superael
	es as function arguments
	n usina references as function erguments.
	overloading
	templates
	timing functions
	av usi unsigned char ti
	av 10us( unsigned chart )
	ay ms/ unsigned char()
	ay si unsigned char Li
	72163
General	purpose functions.
	<u>К уюг. пита )</u>
_	



# BoostC C Compiler for PICmicro Reference Manual

<u>sət bili vər. num )</u>		
MAKESHORT( dst. lobyte, hib)	ta )	
void noof void )		
	115.	
roid strcovi char "dst. const ch		
wid streay/ char "dal. rom cha	*arc }.	
void strinepy[ char*dst. const o	har "arc, unsigned char len }	
void stracpy/ char "dst. tom ch	er sic, unsigned char len }	······
unaigned char strien( const ch		
msigned char strient rom char signed char strong( const char	Sent const the remain	·
soned char strenge consi char	stof, const char store 1	
signed char stremp! const chai	"src1, rom char *src2)	
signed char stremot rom char	sro1. rom char *src2.j	
icened Char schome/ const cha	r "sitt", const char "sec2 }	
ligned char stricmpt rolly char ligned char stricmot const cha	SEL CONST CHAP SUCE	
coned char strictmol const char	<u>"src1, rom char "src2 )</u>	-
signed char simemol char sm	1. char src2. unsigned char lan )	
caned char stracmo( rom char	"sm1_char "src2_unsigned_char lan )	
sanea char stracada cher "sic	1. forn char "src2, unsigned char ion i	
signed char stracmp( rom char	"src1, rom cher "src2, unsigned Char Ian )	·····
signed char structure char "se	1. char src2, unsigned char len )	
signed cuar sunicipal rum cha	r*src1, char*src2, Unsigned char len ) c1, runt char*src2, unsigned char len )	
screet capt samenus cour se	1. Juli John Sicz. Unsumed Shallen I t 1961. rom char 1962. Unsigned Charlen I	
roid streat/ char *dst. const ch	ar "src )	
roid streat( char "dst, rom chai		······
void strineatt char *dst_const_c	nar src. unsigned cherten t	
void stimpel( char "dst. fom ch char" stipperkt const char "obit.	er *arc. unsigned char len )	****
char stoppiki const char per.	and cher for 1	
unsigned cher stream( const o		······
unsigned char strespol rom ch	ar*src1, const char *src2)	
unsigned char streson( const c	har src1. rom char (src2.)	
unsigned char strespol rom ch	ar src1, rom char src2)	*********
unsigned char strspni consi ci unsigned char strspni rom cha	stant const char sm2	
unsigned char strappi canst of	ar"src1_rom ober "src2 i	
unsioned char straunt rom cha	(*sici. rom char*sic2).	
char" sintoki const char_oirf.	const char "ptr2 i	
char, shipki const char "src. n	oni char "srt ).	
char stocht const char sic, c	her ch )	
char" sitsiri const char." piri. c	const char *ptr2 )	
char" stratri const char "src. rd	m char tare )	
nversion Functions		
unsioned cher sprintli cher bu	fler, const char *format, unsigned int val )	
unsigned char sprint/32( char	buffer. const char format, unsigned long val ]	
ini sittoil consi cher, butter, ci	ar" endPtr. unsigned char radir ). char" endPtr. unsigned char radix );	······································
iona salay const cher. ouner.	CIAL OHOP'L UNSIDE CLAFTAUX L	*****
inno afoli const char buffer )		***************************************
char" itoal int val. char" buffer.	unsigned char radix 1	
char" libal long val. char" buff	er, unsigned char radix )	
intweight Conversion Fur	ctions.	
<u>vaid uilas_hex( aher" builler, u</u>	nsigned int val. Unsigned char digits )	·····
	SourceBoost Technologies	BoostC <sup>14</sup> Manu

age

## index

¥1.20

BoostC compiler		
Introduction		
BoostC Compiler specifica	tion	9
Sase Data types		
Special Data types		
Special Language Feature	<b>0</b>	
Code Production and Opti	mization Features	
Ocbugging features		
Full MPLAB integration		
	۲	
Compliation model and to	olchain	
Preprocessor.		
Differences with C1C com	pliation model	14
Satting the MDI AB I show	age Tool Locations	41
Creating a project under 1	PLAB IDE	47
Lising (CD2		21
Command line ontions.		27
Optimization		
BoostLink command line.		
<u>-0</u>		
-SWC3 51 82 53		
-ismocontext		24
-iori2		23
_berefa		
Hbc Library		
Gode entry gaints	الشريقة بهجر والمحادثة الشاري ليتبالي ويورون الكفا فلموجز بيوم ويهرون أكا محجوريهم	
SourceBoos(IDE		
	*****	
Hundel		
Rif, #else, #endif	·····	
thilles.		
STRONG		
iwaming.	الا من المراجع - من من المراجع - من من المراجع - من المراجع 	
Pragma directives	****	34
Boranna DATA		
foraoma CLOCK FREQ		
Setting Device Configurat	Ion Options	<u></u>
Initialization of EEPROM (	Data	
<u>C language</u>	,	
Page 2	SourceBoost Technologies	BoostC™ Manua

			void video, bini (spar' bullyr, unzisared ini val, unzisared char (sois) void video, deci (char' bullyr, unzisared ini val, unzisared char (sois) unzisared (nt abuil parl const char' buller)
			unsected int alour bin (const char buffer). Unsigned int alour deci const char buffer).
			cher topper( cher ch )
			chur toicwerf chur ch.) Chur skrjai Chur ch. chur skrjai Chur ch.)
			cher (sappel carp) cher (sappel char ch) cher (sappel char ch) cher (sappel char ch) cher (sappel char ch)
			char isoraphi char ch i
			char ispinit char ch ]
			chur (isquard: chur ch.) chur (isquard: chur ch.) chur (isquard: chur ch.)
			char issicial char ch . char issicial (char ch ). volt memoting operatival tar, char ch, unsigned char lien )
			signed char mamorpul const void "ptr1 const void "ptr2 unsigned char len ]
			void" memorial void "oit, char ch, unsigned char len ).
			Miscalianeous Eurotions unsigned short (vold) xold small unsigned short seed
			1984 ( 1 )
			12C functions. 12c junt 12c start 12c metant 12c stop 12c most 12c with 10c most information book into 12c stores to and 12c (set c first).
			RS232 TURCHONE
			uan, init, khill, aeta, getab, puta, puta. Ita: mare informetion kock into satisf, driver.in and series issue, first,
			LCD functions lod salue lotisti ind, clear ind, mile lod, functionale, lod, datamode for most information lods into lod, driver in and lode, files
			Flash functions
			ahad faah, uudishad addi. tali faah, kaabadahad dala. said laay, uudishad addi.
			Char eeorom reedicher addr.
			socia secono, wite/chet addr. chet dels). ADC functione stort adc. messure/chet.ch.
			One wire bus functions
			char 00 justesniji. stori 00 justesniji. char 00 read stranchoadj.
			veid oo shad sonversion). Ataroo oorarason burd ataroo veid ba sababion).
			oter ce wait for completion) PC System Requirements
			Technical support BoostC Support Subscription
			Licensing issues Seneral Support
oostC** Manual	SourceBoost Technologies	Page 7	
			BoostC <sup>™</sup> Manual SourceBoost Technologies Page
			BoostC <sup>m</sup> Manual SourceBoost Technologies Page

#### stallation

ide

#### e BoostC compiler cannot be downloaded or installed on its own. BoostC is part the SourceBoost software package that includes the SourceBoost IDE and other nguage Suites. It is available for download from our site nguage Suites. It is p://www.sourceboost.com site

ien you buy a license, you will activation code(s) and detailed instructions on w to activate the compiler and other tools you have licensed.

install SourceBoost IDE and BoostC on your system, please follow these simple :ps:

- Execute the installer sourceboost.exe and follow on-screen directions. .
- Please pay attention to the integration dialog:

hould Boost Complete integrate into MPLAB? a triegrate Boost Complete into MPLAB locato MPLAB introduction directory in due total bolow and than press the triegrate factors. To skip MPLAB introduction press a Nord bottom at the bottom at this delaya ←	
ontel between and than pieces the theographic futtion. To skip MPLAB integration press as Nort buttom at the bettom at this delog	3
E Jone     Subat     Subat	
を 当 JGsot く 当 LANgued Network Sconner 当 Messanger 当 <u>Messanger</u> も 当 missenth forspagn も 当 Missent Hardware	١.
Collegand Hedwork Scowner     Macazye     Macazye     Macazye     microsoft instruge     microsoft Instruge	
• Microsoft frombage	
• 🔄 Microsoft Hardware	•
<ul> <li>Microsoft Office</li> </ul>	
(Back   Heal) Can	

To Integrate BoostC with MPLAB, choose the correct Microchlp installation directory, then click on "Integrate" before stepping to the next Installation wizard dialog.

 The rest of the installation process is straightforward. At the end, SourceBoost IDE is ready to be used on your system. Should any difficulty arise, please double check your system configuration and mail all details to support@sourceboost.com

				d of a function call. This speeds up code ex-	
3oostC™ Manual	SourceBoost Technologies	Page 11	BoostC <sup>ree</sup> Manual	SourceBoost Technologies	Page 9
necessary MPLAE	he installation step "MPLAB Integration" 3 integration files will be installed	to the \mplab	<ul> <li>Removal of Orph</li> </ul>	chable (or dead) code - reduces code ma an (uncalled) functions - reduces code n	nemory usage.
can always be ma	e chosen SoucceBoost Installation direc anually copied to the correct location – n <sup>-r</sup> section later in this manual.		multiple code page • Automatic Bank variables. • Efficient RAM us	s Switching for Variables - allows c age - local variables in different code sec ar analyzes the program to prevent any class	arefree use of tions can share
			Debugging feature	S	
			output for source Step inta, Step both at source lev Multiple Executi source level and a Monitoring varia	Instruction Level Debugger - linker Ge level debugging under SourceBoost Debuser. ver, Step Out and Step Back - these fu el and instruction level. on Views - see where the execution of seembly level at the same time. bibes - variables can be added to the wa to be examined and modified. There is no s stored.	inctions operate the code is at itch windows to
			Full MPLAB integra	ntion	
			Creation and Edition     Build a project wit	2;	
			Librarian		
			of regularly used,	of <b>library files</b> - this simplifies managem shared code. In time - using library files reduces compilat	
			Code Analysis		
			Target Code Us: the code space us     Target RAM Usa	SourceBoost IDE can display the function age - From the complete program, down t age can be viewed in SourceBoost IDE. ige - From the complete program, down t n be examined and reviewed in SourceBoos	o Function level
	SourceBoost Technologies B	oostC <sup>IM</sup> Manual	Page 10	SourceBoost Technologies 84	oostC™ Manual
	2001-1-20000 ( KG/H0/0 <u>-</u>			-	

**BoostC compiler** 

#### Introduction

Thank you for choosing BoostC. BoostC is our next generation C compiler that works with PIC16, PIC18 and some PIC12 processors.

This ANSI C compatible compiler supports features like source level symbolic debugging, signed data types, structures/unions and pointers.

The BoostC compiler can be used within our SourceBoost IDE (Integrated Development Environment), or it can be integrated into Microchip MPLAB,

**BoostC Compiler specification** 

#### **Base Data types**

Size	Type name	Specification
1 bìt	bit, bool	boolean
8 bit	char	signed, unsigned
16 bit	short, int	signed, unsigned
32 bit	long	signed, unsigned

#### Special Data types

- Single bit single bit data type for efficient flag storage.
   Fixed address fixed address data types allow easy access to target device registers.
- Read only code memory based constants.

#### **Special Language Features**

References as function arguments.

- Function overloading. Function templates.

#### **Code Production and Optimization Features**

ANSI 'C' compatible - Makes code highly portable. Produces optimized code for both PIC16 (14bit core) and PIC18 (16bit core)

- Produces optimized code for bour races (arrow cards, arrow cards) targets. Support for Data Structures and Unions Data structures and arrays can be comprised of base data types or other data structures. Arrays of base data types or data structures can be created. Support for pointers pointers can be used in "all the usual ways". Inline Assembly Inline assembly allows hand crafted assembly code to be used when necessary. Inline Functions Inline functions allows a section of code to be written as a function, but when a reference is made to it the inline function code is increated instead of a function call. This speeds up code execution.

#### **'LAB integration**

**stC** C compiler can be integrated into Microchips MPLAB integrated elopment environment (IDE). The MPLAB integration option should be selected ing the SourceBoost software package installation.

ase note: To use BoostC under MPLAB the MPLAB integration button must be seed during the SourceBoost package installation. This copies some files and s the required registry keys required for integration to work.

case the installation step "MPLAB Integration" failed, the files in the ourceBoost>\mplab directory can be manually copied into

IPLA8 IDE>\Third Party\MTC Suites for MPLAB 7.x. or

IPLAB IDE>\LegacyLanguageSultes for MPLAB 6.x.

the above examples, <MPLAB IDE> refers to the MPLAB Installation directory d <SourceBoost> refers to the SourceBoost IDE and compilers Installation ectory.

#### atures

- ten BoostC is integrated into MPLAB IDE it allows the following:

- Use of the MPLAB Project Manager within MPLAB IDE.
   Creation and Editing of source code from within MPLAB IDE.
   Build a project without leaving MPLAB IDE.
   Source level debugging and variable monitoring using: MPLAB simulator,
  MPLAB ICD2, MPLAB ICE2000.

#### stting the MPLAB Language Tool Locations

ite: this process only needs to be performed once.

e procedure below specifies paths assuming the default installation folder has en used for the SourceBoost software package.

1. Start MPLAB IDE

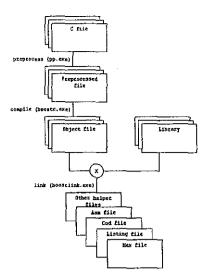
loostC<sup>m</sup> Magual

Menu Project => Set Language Tool Locations. Note: If BoostC C compiler does not appear in the Registered Tools list, then the Integration process during the SourceBoost installation was not performed or was unsuccessful.

SourceBoost Technologies

Page 15

Compilation model and toolchain



## Preprocessor

The preprocessor pp.exe is automatically invoked by the compiler.

#### Complier

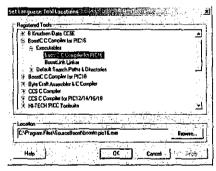
There are actually two separate compliers: one for pic16 and one for pic18 targets. When you work under SourceBoost IDE, there is no need to specify which one to use: the IDE picks the correct compiler based on the selected target.

The output of the compiler is one or more **.obj** files, that are further processed by librarian or linker, in order to get a .lib-or..hex file.

SourceBoost Technologies

Page 13

## Set BoostC C compiler for PIC16 location;



#### 4. Now set BoostLink Linker location:

🗄 8 Knudsen Dala (1286	<u>ه</u>
BonniC C Compiler for PICI6 Executables	1
BoostC C Complex for PIC16	
子会会的上述是"上帝"的"	
37 Default Search Paths & Decctories	
Ri BoostC C Complex for PIC18	
当 Byte Craft Assumbler & C Compiler 第 CCS C Compiler	
⊕ CCS C Complex ₩ CCS C Complex (or PIC12/14/16/16	
HI-TECH PICE Toolmite	-1
Colice	<u> </u>
Program Files / SourceBones/Uscontine.pic.mm	
a tolenut eve woodcoprove autoration because	- Dioritica

#### Linker

BoostLink Optimizing Linker links .obj files generated by compiler into a .hex file that is ready to send to target. It also generates some auxiliary files used for debugging and code analysis.

#### Librarian

Librarian is built into BoostLink linker executable and gets activated by -11b command line aroument. There is a dedicated box in the Option dialog inside SourceBoost IDE that changes project target to library instead of hex file.

To create a <u>target independent</u> library, include *boostc.h* instead of itex lite. To create a <u>target independent</u> library, include *boostc.h* instead of system.h into the library sources. This way no target specific information (like target dependent constants or variables mapped to target specific registers) is included into the library. Note that this is the only case in which system.h does not be included into the code.

#### **Differences with C2C compilation model**

The main difference between BoostC and our previous generation C2C complier is that the latter had a built-in linker and created an .asm file needing to be assembled using an external assembler (like MPASM), while the **BoostC** toolsuite doesn't need any external tools and directly generates the target .hex file.

Another difference is in how compilers handle read-only variables located in code memory. BoostC uses the special data type specifier 'rom', while C2C placed any variable defined as 'const' into code memory.

1de 16

4. Menu File 🔿 Save As. Locate the project folder using the Save As dialog	E Set Baset C complex for Birth Jametian
box.	5. Set BoostC C compiler for PIC18 location;
Save as I as I as	Rightend Tools
	y Boadt C Complex for PC16  Boadt C Complex for PC18  C Complex for PC18  E Executables
	Boosthink Linkar Boosthink Linkar T Data & Seach Patha & Directarie
	Byte Coll Assembler & C Cooplar     SOS Complex     Cost Complex     Cost Complex     Cost Complex tor PC(2)/14/16/19
File name: [est.c Save	NH-TECH PICC Toolada
Save as type: (C Source Files (* C*16)  Canced Hob	CCPagran Files Sauceifans Noranta pic 1 Beve Browna
Encoding [ANS]	Help OK Conzi Hora
1 Add File To Project	6. Eventually, set <b>BoostLink</b> Linker location in the PIC18 tree:
5. Add the test, c source file to the project by right clicking on Source Files in the project tree – as shown below.	Set Compusing Tand Instations
Lestman 上版版例》	Registered Tools     Security CODE     Secu
	⊂ BocutC Complex (a) PIC19
	Book C C Coupler in PICIB Book C Coupler in PICIB Production A Search Parts & Directories
Divisions Cather Plan	P Byte Call Association & CCampler  ii CCS IC Complet  CCS IC Completer (PC12/14/16/18  CCS IC Completer (PC12/14/16/18
	( * HHTECK PRCC Toolwan
	Location C'Hogen File SourceBootboostink picess Browsin
This Ye Symbols	Help Soor
	Creating a project under MPLAB IDE Before attempting to do this, please ensure that the <b>Setting the MPLAB</b>
	language tool locations" process illustrated in the above section has been successfully performed.
oostC <sup>TM</sup> Manual SourceBoost Technologies Page 19	BoostC <sup>™</sup> Manual SourceBoost Technologies Page 17
folder) to the project by right clicking on Library Files in the project tree.	built using the BoostC C compilier, compiling for a PIC16 Target. The project name is test and the project and source code will be located in folder C:\PicProgram\test         1. Menu Project ⇒ New. Enter a project name and directory. Note: this can be an existing directory containing a SourceBoost IDE project.         Image: Interference of the project of the project of the project of the project.         Image: Interference of the project of the project of the project.         Image: Interference of the project of the project of the project.         Image: Interference of the project of the project of the project.         Image: Interference of the project o
<ul> <li>Check the final project. It should look as below:</li> <li>Exiting a second s</li></ul>	Hep       OK       Consid         2. Menu Project -> Select Language Toolsuite, Select the BoostC C Compiler for PIC16.       Select Construction of the Construct
Itel introduction is a second state in the second seco	2. Menu Project → Seject Language Toolsuite, Select the BoostC C Complier for PIC16.         Statutement of the select
<ul> <li>Iterations is a second secon</li></ul>	2. Menu Project → Select Language Toolsuite, Select the BoostC C Complier for PIC16.         Statutement of the select

pstLink command line	Using ICD2	
stink Ontimizing Linker Version v vv	The are a few things to be aware of when using or planning using ICD2:	
p://www.sourceboost.com yright(C) 2004-2007 Pavel Baranov yright(C) 2004-2007 David Hobday	1. RAM usage: ICD2 uses some of the target devices RAM, leaving less room	
ensed to <license info=""></license>	for the actual application.	
ge: boostlink.pic.exe {options} files	In order to reserve the RAM required by ICD2, and prevent Boost Linker from using it, the <i>icd2.h</i> header file must be included in the source code, eg:	
tons: t name target processor	#include system.no	
on optimization level 0-1 (default n=1) n=0 - no optimization	#include <icd2.h> // allocates RAM used by ICD2</icd2.h>	
n=1 - pattern matching and bank switching optimize on v verbose mode d path directory for project output	void mainO	
d path directory for project output p name project (output) name for multiple .obj file linking 1d path directory for library search	<pre>while(3); }</pre>	
rb address ROW base (start) address to use rt address ROW top (end) address to use		
Wrs s1 s2 s3 Use software call stark. Hardware stark is allocated by	2. SFR usage: ICD2 uses some Special Function Registers. This prevents the	
<pre>spacifying stack depths s1,s2,s3 (optional) s1 = main and task routines hardware stack allocation s2 = ISR hardware stack allocation</pre>	use of some peripheral devices when using ICD2 to debug code.	
SS = PICI8 low priority ISK margware stack allocation	Important: It is down the user to ensure that the ICD2 special function registers are not accessed. On some targets these registers reside at the	
srnoshadow ISR No use of Shadow registers Srnocontext ISR No context Save/restore is added to ISR(PIC18 only)	same address as other peripheral device special function registers. Please	
cd2 Add NOP at first ROM address for correct ICD2 operation Mexela Always add extended linear address record to .hex file	check the documentation provided in the MPLAB IDE help for ICD2 resource usage in order to prevent problems.	
itches for making libraries: -lib make library file from supplied .obj and .lib files	3. Break point overrun: Due to timing skew in the target device (caused by	
p name project (library output file) name	instruction prefetch), execution will pass the instruction address where a breakpoint is set before it stops.	
	4. NOP at ROM address 0: See the BoostLink command line option -lcd2 to	
	add a NOP at ROM address 0.	
is command line option causes the code generated by the linker to start at the dress specified. Boot loaders often reside in the low area of ROM.		
ample		
Dx0800		
	•	
IostC <sup>m</sup> Manual SourceBoost Technologies Page 23	BoostC <sup>m</sup> Manual SourceBoost Technologies Page 21	
s s1 s2 s3 command line option to the linker tells it to use a software call stack in	<b>Command line options</b> To get full list of BoostC compiler and BoostLink linker command line options run	
command line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the irdware call stack of the PIC. A function call that is made on the software call	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line.	
ommand line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the rdware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line	
ommand line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the rdware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must ad when using <b>Novo RTOS</b> . Where possible the hardware stack is used for ncy. By specifying the depth of hardware stack to use for main (and Novo	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line BoostC Gotimizing C Compiler Version X.XX	
ommand line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the rdware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must ad when using <b>Novo RTOS</b> . Where possible the hardware stack is used for rcy. By specifying the depth of hardware stack to use for main (and Novo <i>s1</i> , ISR (interrupt service routine) <i>s2</i> and low priority ISR (PIC18 only) <i>s3</i> , es control over when the software call stack is used instead of the hardware	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line	
ommand line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the rdware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must of when using <b>Novo RTOS</b> . Where possible the hardware stack is used for rcy, By specifying the depth of hardware stack to use for main (and Novo <b>s1</b> , ISR (interrupt service routine) <b>s2</b> and low priority ISR (PIC18 only) <b>s3</b> , es control over when the software call stack is used instead of the hardware ack. The software call stack is a specific to region of the tree,	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line BoostC optimizing C Compiler Version x.xx http://www.sourceboost.com Convertebt(C) 2004-2007 Pavel Baranov	
ommand line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the rdware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must ad when using <b>Novo RTOS</b> . Where possible the hardware stack is used for ncy. By specifying the depth of hardware stack to use for main (and Novo st, ISR (interrupt service routine) s2 and low priority ISR (PICI8 only) s3, es control over when the software call stack is used instead of the hardware ack. The software call stack is applied to functions higher up in the call tree, is lower down the call tree still use the hardware call stack. If no hardware depths are specified, then the software stack is only used in functions that	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line BoostC Optimizing C Compiler Version x.xx http://www.sourceboost.com Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 David Hobday Licensed to <license info=""> Usage: boostc.picl6.exe [options] files</license>	
command line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the rrdware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must ad when using <b>Novo RTOS</b> . Where possible the hardware stack is used for ncy. By specifying the depth of hardware stack to use for main (and Novo st, ISR (interrupt service routine) s2 and low priority ISR (PICI8 only) s3, les control over when the software call stack is used instead of the hardware ack. The software call stack is applied to functions higher up in the call tree, is lower down the call tree still use the hardware call stack. If no hardware depths are specified, then the software stack is only used in functions that n or call functions that contain a <b>Novo RTOS</b> Sys_Yield() function.	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line BoostC Optimizing C Compiler Version x.xx http://www.sourceboost.com Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 David Hobday Licensed to <license info=""> Usage: boostc.pic16.exe [options] files Options: - t name target processor (default name=PIC16F648A)</license>	
command line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the rrdware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must ad when using <b>Novo RTOS</b> . Where possible the hardware stack is used for ncy. By specifying the depth of hardware stack to use for main (and Novo s1, ISR (interrupt service routine) s2 and low priority ISR (PICI8 only) s3, les control over when the software call stack is used instead of the hardware ack. The software call stack is applied to functions higher up in the call tree, is lower down the call tree still use the hardware call stack. If no hardware depths are specified, then the software stack is only used in functions that	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line BoostC Optimizing C Compiler Version x.xx http://www.sourceboost.com Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 David Hobday Licensed to <li>cense info&gt; Usage: boostc.pic16.exe [options] files Options: - t name target processor (default name=PIC16F645A) - on optimization level (default n=1) n=0 - optimization turned off</li>	
command line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the indware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must ed when using <b>Novo RTOS</b> . Where possible the hardware stack is used for nex. By specifying the depth of hardware stack to use for main (and Novo <b>s1</b> , ISR (interrupt service routine) <b>s2</b> and low priority ISR (PIC18 only) <b>s3</b> , see control over when the software call stack is used instead of the hardware ack. The software call stack is applied to functions higher up in the call tree, is lower down the call tree still use the hardware call stack. If no hardware depths are specified, then the software stack is only used in functions that n or call functions that contain a <b>Novo RTOS</b> Sys_Yield() function. <b>pie:</b> <b>6</b> 2 routine will use hardware call stack up to a depth of 6 and then start using	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line BoostC optimizing C Compiler Version x.xx http://www.sourceboost.com Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 David Hobday Licensed to <li>cense info&gt; Usage: boostc.picl6.exe [options] files Options: -t name -t name -t</li>	
ommand line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the rdware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must ad when using <b>Novo RTOS</b> . Where possible the hardware stack is used for ncy. By specifying the depth of hardware stack to use for main (and Novo <i>s1</i> , ISR (interrupt service routine) <i>s2</i> and low priority ISR (PICI8 only) <i>s3</i> , es control over when the software call stack is used instead of the hardware ack. The software call stack is applied to functions higher up in the call tree, is lower down the call tree still use the hardware call stack. If no hardware depths are specified, then the software stack is only used in functions that n or call functions that contain a <b>Novo RTOS</b> Sys_Yield() function. <b>pie:</b> <i>6</i> 2 outine will use hardware call stack up to a depth of 6 and then start using are call stack. Interrupt routine will use hardware call stack up to a depth of 2	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line BoostC Optimizing C Compiler Version x.xx http://www.sourceboost.com Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 David Hobday Licensed to <li>cense info&gt; Usage: boostc.picl6.exe [options] files Options: - t name t arget processor (default name=PIC16F648A) -On optimization level (default n=1) n=0 - optimization turned onf n=1 - optimization turned on n=p - 32 bit long promotion turned on -Wm warning-level (default n=1)</li>	
ommand line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the rdware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must de when using <b>Novo RTOS</b> . Where possible the hardware stack is used for try. By specifying the depth of hardware stack to use for main (and Novo st]. ISR (interrupt service routine) s2 and low priority ISR (PIC18 only) s3, es control over when the software call stack is used instead of the hardware ack. The software call stack is applied to functions higher up in the call tree, is lower down the call tree still use the hardware call stack. If no hardware depths are specified, then the software stack is only used in functions that nor call functions that contain a <b>Novo RTOS</b> Sys_Yield() function. <b>ple:</b> 6 2 outhe will use hardware call stack up to a depth of 6 and then start using are call stack. Interrupt routine will use hardware call stack up to a depth of 2 art using software call stack. An ISR uses hardware call stack depth of 1 to he address of the point where the code was interrupted, so in this example it	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line ReostC optimizing C Compiler Version x.xx http://www.sourceboost.com Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 David Hobday Licensed to <li>license info&gt; Usage: boostc.picl6.exe [options] files Options: -t name target processor (default name=PIC16F648A) -On optimization level (default n=1) n=0 - optimization turned ofn n=a - aggressive outlWization turned on n=a - some warnings n=1 - some warnings n=2 - some warnings</li>	
ommand line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the rdware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must d when using <b>Novo RTOS</b> . Where possible the hardware stack is used for ncy. By specifying the depth of hardware stack to use for main (and Novo <b>SI</b> , ISR (Interrupt service routine) <b>s2</b> and low priority ISR (PIC18 only) <b>s3</b> , es control over when the software call stack is used instead of the hardware ack. The software call stack is applied to functions higher up in the call tree, is lower down the call tree still use the hardware call stack. If no hardware depths are specified, then the software stack is only used in functions that no ro call functions that contain a <b>Novo RTOS</b> Sys_Yield() function. <b>ple:</b> <b>6</b> 2 routine will use hardware call stack up to a depth of 6 and then start using re call stack. Interrupt routine will use hardware call stack up to a depth of 1 art using software call stack. Na Fordware call stack depth of 1 to the address of the point where the code was interrupted, so in this example it hardware call stack depth 1 for subsequent calls within the ISR.	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line ReostC optimizing C Compiler Version x.xx http://www.sourceboost.com Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 David Hobday Licensed to <li>cense info&gt; Usage: boostc.picl6.exe [options] files Options: -t name target processor (default name=PIC16F648A) -On optimization level (default n=1) n=0 - optimization turned off n=a - aggressive optimization turned on n=a - aggressive optimization turned on -wm warning level (default n=1) n=0 - no warnings n=1 - optimized n=1 - optimized n=0 - no warnings n=1 - some warnings n=2 - sine warnings -werr treat warnings serves (default off) -i debug initine code (default off)</li>	
ommand line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the rdware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must d when using <b>Novo RTOS</b> . Where possible the hardware stack is used for try, By specifying the depth of hardware stack to use for main (and Novo st], ISR (interrupt service routine) s2 and low priority ISR (PIC18 only) s3, es control over when the software call stack is used instead of the hardware ack. The software call stack is applied to functions higher up in the call tree, is lower down the call tree still use the hardware call stack. If no hardware depths are specified, then the software stack is only used in functions that in or call functions that contain a <b>Novo RTOS</b> Sys_Yield() function. <b>ple:</b> 6 2 outhe will use hardware call stack up to a depth of 6 and then start using are call stack. Interrupt routine will use hardware call stack up to a depth of 2 art using software call stack. An ISR uses hardware call stack depth of 1 to he address of the point where the code was interrupted, so in this example it aves a hardware call stack depth 1 for subsequent calls within the ISR. <b>wshadow</b>	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line BoostC optimizing C Compiler Version x.xx http://www.sourceboost.com Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 David Hobday Licensed to <li>2004-2007 David Hobday Licensed to <li>2004-2007 Cavel Baranov Copyright(C) 2004-2007 David Hobday Licensed to <li>2004-2007 Cavel Baranov Copyright(C) 2004-2007 Cavel</li></li></li></li></li></li></li></li></li>	
ommand line option to the linker tells it to use a software call stack in In to the hardware call stack. This allows subroutine calls deeper than the rdware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must d when using <b>Novo RTOS</b> . Where possible the hardware stack is used for tcy. By specifying the depth of hardware stack to use for main (and Novo <b>s1</b> , ISR (interrupt service routine) <b>s2</b> and low priority ISR (PIC18 only) <b>s3</b> , es control over when the software call stack is used instead of the hardware ack. The software call stack is applied to functions higher up in the call tree, s lower down the call tree still use the hardware call stack. If no hardware depths are specified, then the software stack is only used in functions that n or call functions that contain a <b>Novo RTOS</b> Sys_Yield() function. <b>ple:</b> <b>6</b> 2 outline will use hardware call stack up to a depth of 6 and then start using re call stack. Interrupt routine will use hardware call stack up to a depth of 1 he address of the point where the code was interrupted, so in this example it taves a hardware call stack depth 1 for subsequent calls within the ISR. <b>stakdow</b> ommand line switch tells the linker not to use the PIC18 shadow registers for pits service routine (ISR) context saving. This option is required as a work	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line BoostC optimizing C Compiler Version x.xx http://www.sourceboost.com Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 David Hobday Licensed to <li>2004-2007 David Hobday Licensed to <li>2004-2007 Cavel Baranov Copyright(C) 2004-2007 David Hobday Licensed to <li>2004-2007 Cavel Baranov Copyright(C) 2004-2007 Cavel</li></li></li></li></li></li></li></li></li>	
ommand line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the rdware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must d when using <b>Novo RTOS</b> . Where possible the hardware stack is used for try, By specifying the depth of hardware stack to use for main (and Novo <b>s1</b> , ISR (interrupt service routine) <b>s2</b> and low priority ISR (PIC18 only) <b>s3</b> , es control over when the software call stack is used instead of the hardware ack. The software call stack is applied to functions higher up in the call tree, is lower down the call tree still use the hardware call stack. If no hardware depths are specified, then the software stack is only used in functions that no or call functions that contain a <b>Novo RTOS</b> Sys_Yield() function. <b>ple:</b> <b>6</b> 2 outline will use hardware call stack up to a depth of 6 and then start using re call stack. Interrupt routine will use hardware call stack up to a depth of 1 he address of the point where the code was interrupted, so in this example it taves a hardware call stack depth 1 for subsequent calls within the ISR. <b>stardware</b> ommand line switch tells the linker not to use the PIC18 shadow registers for pit starks routine (ISR) context saving. This option is required as a work	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line BoostC optimizing C Compiler Version x.xx http://www.sourceboost.com Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 David Hobday Licensed to <li>cense info&gt; Usage: boostc.picl6.exe [options] files Options: -t name target processor (default name=PIC16F648A) -on optimization level (default n=1) n=0 - optimization turned off n=1 - optimization turned off n=3 - aggressive optimization turned on n=4 - aggressive optimization turned on n=7 - 30 ti long promotion turned on n=7 - 30 ti long promotion turned on -Wm warning: N=vel (default n=1) n=0 - no warnings n=1 - optimization of uninitialized static variables -d name define 'name' -m generate dependencies file (default off) -v verbose mode turned on (default off) -i debog inlitialization of uninitialized static variables -d name define 'name' -m generate dependencies file (default off) -i debog uninitialization of (default off) -i pathlipath2 additional include directories</li>	
ommand line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the rdware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must be when using <b>Novo RTOS</b> . Where possible the hardware stack is used for nor, by specifying the depth of hardware stack to use for main (and Novo <i>s1</i> , ISR (interrupt service routine) <i>s2</i> and low priority ISR (PIC18 only) <i>s3</i> , es control over when the software call stack is used instead of the hardware ack. The software call stack is used instead of the hardware depths are specified, then the software stack is only used in functions that no ro call functions that contain a <b>Novo RTOS</b> Sys_Yleid() function. <b>ple:</b> 6 2 routine will use hardware call stack up to a depth of 6 and then start using are call stack. Interrupt routine will use hardware call stack up to a depth of 1 to he address of the point where the code was interrupted, so in this example it eaves a hardware call stack depth 1 for subsequent calls within the ISR. <b>Soshadow</b> ommand line switch tells the linker not to use the PIC18 shadow registers for uptice routine (ISR) context saving. This option is required as a work d for silicon bugs in some PIC18's.	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line RoostC optimizing C Compiler Version x.xx http://www.sourceboost.com Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 David Hobday Licensed to <license info=""> Usage: boostc.picl6.exe [options] files Options: -t name target processor (default name=PIC16F648A) -On optimization level (default n=1) n=0 - optimization turned off n=1 - optimization turned off n=4 - aggressive optimization turned on n=6 - aggressive optimization turned on n=7 - 3bit 10ng Promotion turned on n=7 - 3bit 10ng Promotion turned on n=7 - 3bit 10ng Promotion turned on n=7 - some warnings n=1 - optimization off -Verr treat warnings n=2 - all warnings -Verr treat warnings -Verr treat warnings -Verr treat warnings -Verr treat warnings -Verr treat warnings -Ver treat warnings -Verr treat warnings -Ver treat warni</license>	
ommand line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the rdware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must d when using <b>Novo RTOS</b> . Where possible the hardware stack is used for try. By specifying the depth of hardware stack to use for main (and Novo st]. ISR (interrupt service routine) s2 and low priority ISR (PIC18 only) s3, es control over when the software call stack is used instead of the hardware ack. The software call stack is applied to functions higher up in the call tree, s lower down the call tree still use the hardware call stack. If no hardware depths are specified, then the software stack is only used in functions that in or call functions that contain a <b>Novo RTOS</b> Sys_Yield() function. <b>pie:</b> 6 2 bouthe will use hardware call stack up to a depth of 6 and then start using are call stack. Interrupt routine will use hardware call stack up to a depth of 2 art using software call stack an ISR uses hardware call stack depth of 1 to the address of the point where the code was interrupted, so in this example it hardware call stack depth 1 for subsequent calls within the ISR. <b>Nshadow</b> ormmand line switch tells the linker not to use the PIC18 shadow registers for upt service routine (ISR) context saving. This option is required as a work d for silicon bugs in some PIC18's. <b>Notonext</b> uption only works with PIC18's. When use this prevents the linker adding	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line BoostC optimizing C Compiler version x.xx http://www.sourceboost.com Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 David Hobday Licensed to <li>clease info&gt; Usage: boostc.pic16.exe [options] files options: -t name target processor (default name=PIC166F048A) -On optimization level (default n=1) n=0 - optimization turned off n=1 - optimization turned off n=4 - aggressive optimization turned on n=6 - aggressive optimization turned on n=6 - aggressive optimization turned on -Wm warning: Hevel (default n=1) moder - No warnings m=1 - some warnings m=1 - some warnings m=2 - all warnings -werr treat warnings as errors (default off) -i debug inline code (default off) -i debug inline code (default off) -i getfine mage-mentics file (default off) -i getfine addet unced on (default off) -i getfine addet unced on (default off) -i getfine mage-mentics file (default off) -i getfine addet unced on (default off) -i getfine a</li>	
ommand line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the rdware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must d when using <b>Novo RTOS</b> . Where possible the hardware stack is used for rty, By specifying the depth of hardware stack to use for main (and Novo <i>s1</i> , ISR (interrupt service routine) <i>s2</i> and low priority ISR (PIC18 only) <i>s3</i> , es control over when the software call stack is used instead of the hardware ack. The software call stack is applied to functions higher up in the call tree, is lower down the call tree still use the hardware call stack. If no hardware depths are specified, then the software stack is only used in functions that n or call functions that contain a <b>Novo RTOS</b> Sys_Yleid() function. <b>ple:</b> 6 2 outine will use hardware call stack up to a depth of 6 and then start using tre call stack. Interrupt routine will use hardware call stack up to a depth of 2 art using software call stack Mar ISR uses hardware call stack depth of 1 to he address of the point where the code was Interrupted, so in this example it saves a hardware call stack depth 1 for subsequent calls within the ISR. <b>schadow</b> ommand line switch tells the linker not to use the PIC18 shadow registers for upt service routine (ISR) context saving. This option is required as a work d for silicon bugs in some PIC18's. <b>scontext</b> partice in some NIC18's. When use this prevents the linker adding code for context saving. This allow the programmer to generate their own rai ISR context saving code, or have none at all.	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line RoostC optimizing C Compiler version x.xx http://www.sourceboost.com Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 David Hobday Licensed to <li>license info&gt; Usage: boostc.picl6.exe [options] files Options: -t name target processor (default name=PICL6F648A) -on optimization level (default n=1) n=0 - optimization turned off n=1 - optimization turned off n=4 - aggressive optimization turned on n=6 - aggressive optimization turned on n=7 - 3bit 1 long promotion turned on n=7 - 3bit 1 long promotion turned on -wm warning=level (default n=1) -werr treat warnings -werr treat warnings -werr treat warnings -werr treat warnings as errors (default off) -i debug initialization of uninitialized static variables -d name define 'name' -m generate dependencies file (default off) -i pathl:path2 additional include directories Dptimization Iscontrolied by -0 command line option and #pragma. Optimizet fags:</li>	
ommand line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the rdware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must d when using <b>Novo RTOS</b> . Where possible the hardware stack is used for try. By specifying the depth of hardware stack to use for main (and Novo <b>SI</b> , ISR (Interrupt service routine) <b>s2</b> and low priority ISR (PIC18 only) <b>s3</b> , es control over when the software call stack is used instead of the hardware ack. The software call stack is applied to functions higher up in the call tree, is lower down the call tree still use the hardware call stack. If no hardware depths are specified, then the software stack is only used in functions that no ro call functions that contain a <b>Novo RTOS</b> Sys_Yield() function. <b>ple:</b> <b>6</b> 2 Touthe will use hardware call stack up to a depth of 6 and then start using re call stack. Interrupt routine will use hardware call stack dup to a depth of 1 art using software call stack an JSR uses hardware call stack depth of 1 to the address of the point where the code was interrupted, so in this example it taves a hardware call stack depth 1 for subsequent calls within the ISR. <b>estadow</b> ommand line switch tells the linker not to use the PIC18 shadow registers for pit service routine (ISR) context saving. This option is required as a work d for silicon bugs in some PIC18's. <b>econtext</b> uption only works with PIC18's. When use this prevents the linker adding cade for context saving, on have none at all. <b>ple:</b>	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line BoostC optimizing C Compiler version x.xx http://www.sourceboost.com Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 David Hobday Licensed to <li>clease info&gt; Usage: boostc.pic16.exe [options] files options: -t name target processor (default name=PIC166F048A) -On optimization level (default n=1) n=0 - optimization turned off n=1 - optimization turned off n=4 - aggressive optimization turned on n=6 - aggressive optimization turned on n=6 - aggressive optimization turned on -Wm warning: Hevel (default n=1) moder - No warnings m=1 - some warnings m=1 - some warnings m=2 - all warnings -werr treat warnings as errors (default off) -i debug inline code (default off) -i debug inline code (default off) -i getfine mage-mentics file (default off) -i getfine addet unced on (default off) -i getfine addet unced on (default off) -i getfine mage-mentics file (default off) -i getfine addet unced on (default off) -i getfine a</li>	
ommand line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the rdware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must d when using <b>Novo RTOS</b> . Where possible the hardware stack is used for rty, By specifying the depth of hardware stack to use for main (and Novo <i>s1</i> , ISR (interrupt service routine) <i>s2</i> and low priority ISR (PIC18 only) <i>s3</i> , es control over when the software call stack is used instead of the hardware ack. The software call stack is applied to functions higher up in the call tree, is lower down the call tree still use the hardware call stack. If no hardware depths are specified, then the software stack is only used in functions that no or call functions that contain a <b>Novo RTOS</b> Sys_Yield() function. <b>ple:</b> 6 2 outine will use hardware call stack up to a depth of 6 and then start using ire call stack. Interrupt routine will use hardware call stack up to a depth of 2 art using software call stack depth 1 for subsequent calls within the ISR. <b>stackow</b> ommand line switch tells the linker not to use the PIC18 shadow registers for pit service routine (ISR) context saving. This option is required as a work d for silicon bugs in some PIC18's. <b>scontext</b> pits only works with PIC18's. When use this prevents the linker adding code for context saving. This allow the programmer to generate their own rad ISR context saving code, or have none at all. <b>ipite:</b> ttext saving example sues that. the ISR code will, only modify w. and and bsr	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line BoostC optimizing C Compiler version x.xx http://www.sourceboost.com Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 David Hobday Licensed to <li>license info&gt; Usage: boostc.picl6.exe [options] files Options: -t name target processor (default name=PIC166F648A) -on optimization level (default n=1) n=0 - optimization turned off n=1 - optimization turned off n=2 - adgressive optimization turned on n=4 - adgressive optimization turned on n=6 - no warnings m=1 - optimization furned off n=2 - adgressive optimization turned on -wm warning:level (default n=1) n=0 - no warnings m=1 - some warnings m=1 - some warnings m=1 - some warnings -werr treat warnings -werr treat warnings as errors (default off) -i debug inline code (default off) -su disable initialization of (default off) -su disable initialization of (default off) -i getfile mame -m generate dependencies file (default off) -i pathlipath2 additional include directories Dptimize flags: -OO no or very minimal optimization -01 regular optimization (this option is recommended for most applications) -Oa aggressive optimization (produces shorter code and optimizes out some</li>	
ommand line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the rdware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must de when using <b>Novo RTOS</b> . Where possible the hardware stack is used for new, By specifying the depth of hardware stack to use for main (and Novo <b>31</b> , ISR (interrupt service routine) <b>s2</b> and low priority ISR (PIC18 only) <b>s3</b> , see control over when the software call stack is used instead of the hardware ack. The software call stack is auglied to functions higher up in the call tree, is lower down the call tree still use the hardware call stack. If no hardware depths are specified, then the software stack is only used in functions that no ro call functions that contain a <b>Novo RTOS</b> Sys_Yield() function. <b>ple:</b> <b>6</b> 2 routine will use hardware call stack up to a depth of 6 and then start using are call stack. Interrupt routine will use hardware call stack dup to a depth of 1 to he address of the point where the code was interrupted, so in this example it eaves a hardware call stack and to use the PIC18 shadow registers for upt service routine (ISR) context saving. This option is required as a work d for silicon bugs in some PIC18's. <b>xoontext</b> uption only works with PIC18's. When use this prevents the linker adding code for context saving code, or have none at all. <b>upte:</b> ntext saving example starts code will only modify w and and bsr eater coptext, saving further, at fixed, address	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line BoostC optimizing C compiler version x.xx http://www.sourceboost.com Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 David Hobday Licensed to <li>clease info&gt; Usage: boostc.picl6.exe [options] files Options: -t name target processor (default name=PXC16F648A) -On optimization level (default n=1) n=0 - optimization turned off n=1 - optimization turned off n=4 - optimization turned off n=4 - optimization turned on n=6 - source warnings m=0 - no warnings n=1 - source warnings n=2 - source warnings n=4 - source warnings n=5 - source warnings n=4 - source warnings n=4 - source warnings n=4 - source warnings n=4 - source warnings n=5 - source warnings n=4 - source warnings n=5 - source warnings n=6 - no warnings n=7 - source warnings -werr tract all warnings -werr tract all warnings -werr tract all warnings -werr tract all warnings -were define 'name' - source default off) - i debug inline code (default off) - j pathl;path2 additional include directories <b>Optimization</b> Code optimization is controlled by -0 command line option and #pragma. Optimize flags: - O0 no or very minimal optimization - 01 regular optimization (produces shorter code and optimizes out some variables - this can make debugging more difficult) - Op promotes results of some 16 bit operations to 32 bits (can result in</li>	
ommand line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the rdware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must de when using <b>Novo RTOS</b> . Where possible the hardware stack is used for new, By specifying the depth of hardware stack to use for main (and Novo <b>31</b> , ISR (interrupt service routine) <b>s2</b> and low priority ISR (PIC18 only) <b>s3</b> , see control over when the software call stack is used instead of the hardware ack. The software call stack is auglied to functions higher up in the call tree, is lower down the call tree still use the hardware call stack. If no hardware depths are specified, then the software stack is only used in functions that no ro call functions that contain a <b>Novo RTOS</b> Sys_Yield() function. <b>ple:</b> <b>6</b> 2 routine will use hardware call stack up to a depth of 6 and then start using are call stack. Interrupt routine will use hardware call stack dup to a depth of 1 to he address of the point where the code was interrupted, so in this example it eaves a hardware call stack and to use the PIC18 shadow registers for upt service routine (ISR) context saving. This option is required as a work d for silicon bugs in some PIC18's. <b>xoontext</b> uption only works with PIC18's. When use this prevents the linker adding code for context saving code, or have none at all. <b>upte:</b> ntext saving example starts code will only modify w and and bsr eater coptext, saving further, at fixed, address	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line BoostC optimizing C Compiler Version x.xx http://www.sourceboost.com Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 David Hobday Licensed to <li>License info&gt; Usage: boostc.picl6.exe [options] files Options: -t name target processor (default n=1) nat - optimization level (default n=1) nat - optimization turned of nat - optimization turned on n=0 - 32 bit long promotion turned on n=0 - 50 warnings n=1 - some warnings n=2 - solf even (default n=1) n=0 - no warnings n=2 - all warnings -Werr treat warning servors (default off) -i debug inline code (default off) -i debug inline code (default off) -i debug inline code (default off) -i generate dependencies file (default off) -i generate dependencies file (default off) -v v verbose mode turned on (default off) -i pathlipath2 additional include directories Dptimization is controlled by -0 command line option and #pragma. Optimize flags: -00 no or very minimal optimization -01 regular optimization (this option is recommended for most applications) -0a aggressive optimization (this option is recommended for most applications) -0a aggressive optimization (this option is recommended for most applications) -0a aggressive optimization (this option is recommended for most applications) -0a aggressive optimization (this option is recommended for most applications)</li>	
ommand line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the rdware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must de when using <b>Novo RTOS</b> . Where possible the hardware stack is used for new, By specifying the depth of hardware stack to use for main (and Novo <b>31</b> , ISR (interrupt service routine) <b>s2</b> and low priority ISR (PIC18 only) <b>s3</b> , see control over when the software call stack is used instead of the hardware ack. The software call stack is auglied to functions higher up in the call tree, is lower down the call tree still use the hardware call stack. If no hardware depths are specified, then the software stack is only used in functions that no ro call functions that contain a <b>Novo RTOS</b> Sys_Yield() function. <b>ple:</b> <b>6</b> 2 routine will use hardware call stack up to a depth of 6 and then start using are call stack. Interrupt routine will use hardware call stack dup to a depth of 1 to he address of the point where the code was interrupted, so in this example it eaves a hardware call stack and to use the PIC18 shadow registers for upt service routine (ISR) context saving. This option is required as a work d for silicon bugs in some PIC18's. <b>xoontext</b> uption only works with PIC18's. When use this prevents the linker adding code for context saving code, or have none at all. <b>upte:</b> ntext saving example starts code will only modify w and and bsr eater coptext, saving further, at fixed, address	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line BoostC optimizing C compiler version x.xx http://www.sourceboost.com Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 David Hobday Licensed to <li>clease info&gt; Usage: boostc.picl6.exe [options] files Options: -t name target processor (default name=PXC16F648A) -On optimization level (default n=1) n=0 - optimization turned off n=1 - optimization turned off n=4 - optimization turned off n=4 - optimization turned off n=4 - optimization turned on n=6 - source warnings m=0 - no warnings n=0 - source warnings m=1 - source warnings n=0 - source warnings n=1 - optimization of uninitialized static variables - d name define 'name' - were treat all vardings errors (default off) - i debug inline code (default off) - j debug inline code (default off) - on on or very minimal include directories Dotimize flags: - On no or very minimal optimization - On roor very minimal optimization - OI regular optimization (produces shorter code and optimizes out some variables - this can make debugging more difficult) - Op promotes results of some 16 bit operations to 32 bits (can result in</li>	
command line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the indware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must de when using <b>Novo RTOS</b> . Where possible the hardware stack is used for new, By specifying the depth of hardware stack to use for main (and Novo <b>s1</b> , ISR (interrupt service routine) <b>s2</b> and low priority ISR (PIC18 only) <b>s3</b> , see control over when the software call stack is used instead of the hardware ack. The software call stack is used instead of the hardware depths are specified, then the software stack is only used in functions that n or call functions that contain a <b>Novo RTOS</b> Sys_Yleid() function. <b>gle:</b> <b>6</b> 2 routine will use hardware call stack up to a depth of 6 and then start using are call stack. Interrupt routine will use hardware call stack up to a depth of 1 to the address of the point where the code was interrupted, so in this example it eaves a hardware call stack depth 1 for subsequent calls within the ISR. <b>osthadow</b> ommand line switch tells the linker not to use the PIC18 shadow registers for upt service routine (ISR) context saving. This option is required as a work d for silicon bugs in some PIC18's. <b>Decontext</b> option only works with PIC18's. When use this prevents the linker adding code for context saving code, or have none at all. <b>uple:</b> ntext saving example starts taying example start saving example	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line BoostC optimizing C compiler version x.xx http://www.sourceboost.com Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 David Hobday Licensed to <li>License info&gt; Usage: boostc.picl6.exe [options] files Options: -t name target processor (default name=PXC16F648A) -On optimization level (default n=1) n=0 - optimization turned off n=1 - optimization turned off n=4 - optimization turned off n=4 - optimization turned on n=6 - source warnings m=0 - no warnings n=0 - source warnings m=1 - optimization of uninitialized static variables -d name define 'name' -werr treat all varonings errors (default off) -i debug inline code (default off) -on we define 'name' - m generate dependencies file (default off) - v verbose mode turned on (default off) - j pathl;path2 additional include directories Dptimize flags: - On no or very minimal optimization - 01 regular optimization (produces shorter code and optimizes out some variables - this can make debugging more difficult) - Op promotes results of some 16 bit operations to 32 bits (can result in</li>	
ommand line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the rdware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must de when using <b>Novo RTOS</b> . Where possible the hardware stack is used for new, By specifying the depth of hardware stack to use for main (and Novo <b>31</b> , ISR (interrupt service routine) <b>s2</b> and low priority ISR (PIC18 only) <b>s3</b> , see control over when the software call stack is used instead of the hardware ack. The software call stack is auglied to functions higher up in the call tree, is lower down the call tree still use the hardware call stack. If no hardware depths are specified, then the software stack is only used in functions that no ro call functions that contain a <b>Novo RTOS</b> Sys_Yield() function. <b>ple:</b> <b>6</b> 2 routine will use hardware call stack up to a depth of 6 and then start using are call stack. Interrupt routine will use hardware call stack dup to a depth of 1 to he address of the point where the code was interrupted, so in this example it eaves a hardware call stack and to use the PIC18 shadow registers for upt service routine (ISR) context saving. This option is required as a work d for silicon bugs in some PIC18's. <b>xoontext</b> uption only works with PIC18's. When use this prevents the linker adding code for context saving code, or have none at all. <b>upte:</b> ntext saving example starts code will only modify w and and bsr eater coptext, saving further, at fixed, address	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line BoostC optimizing C compiler version x.xx http://www.sourceboost.com Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 David Hobday Licensed to <li>clease info&gt; Usage: boostc.picl6.exe [options] files Options: -t name target processor (default name=PXC16F648A) -On optimization level (default n=1) n=0 - optimization turned off n=1 - optimization turned off n=4 - optimization turned off n=4 - optimization turned on n=6 - source warnings m=0 - no warnings n=1 - source warnings n=2 - source warnings n=4 - source warnings n=5 - source warnings n=4 - source warnings n=4 - source warnings n=4 - source warnings n=4 - source warnings n=5 - source warnings n=4 - source warnings n=5 - source warnings n=6 - no warnings n=7 - source warnings -werr tract all warnings -werr tract all warnings -werr tract all warnings -werr tract all warnings -were define 'name' - source default off) - i debug inline code (default off) - j pathl;path2 additional include directories <b>Optimization</b> Code optimization is controlled by -0 command line option and #pragma. Optimize flags: - O0 no or very minimal optimization - 01 regular optimization (produces shorter code and optimizes out some variables - this can make debugging more difficult) - Op promotes results of some 16 bit operations to 32 bits (can result in</li>	
command line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the indware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must de when using <b>Novo RTOS</b> . Where possible the hardware stack is used for new, By specifying the depth of hardware stack to use for main (and Novo <b>s1</b> , ISR (interrupt service routine) <b>s2</b> and low priority ISR (PIC18 only) <b>s3</b> , see control over when the software call stack is used instead of the hardware ack. The software call stack is used instead of the hardware depths are specified, then the software stack is only used in functions that n or call functions that contain a <b>Novo RTOS</b> Sys_Yleid() function. <b>gle:</b> <b>6</b> 2 routine will use hardware call stack up to a depth of 6 and then start using are call stack. Interrupt routine will use hardware call stack up to a depth of 1 to the address of the point where the code was interrupted, so in this example it eaves a hardware call stack depth 1 for subsequent calls within the ISR. <b>osthadow</b> ommand line switch tells the linker not to use the PIC18 shadow registers for upt service routine (ISR) context saving. This option is required as a work d for silicon bugs in some PIC18's. <b>Decontext</b> option only works with PIC18's. When use this prevents the linker adding code for context saving code, or have none at all. <b>uple:</b> ntext saving example starts taying example start saving example	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line BoostC optimizing C compiler Version x.xx http://www.sourceboost.com Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 David Hobday Licensed to <license info=""> Usage: boostc.picl6.exe [options] files Options: -t name target processor (default name=PLC16F648A) -On optimization level (default n=1) n=0 - optimization turned off n=1 - optimization turned off n=1 - optimization turned off n=0 - no warnings m=0 - source warnings m=0 - source warnings m=0 - source warnings m=1 - source warnings m=1 - source warnings m=1 - source warnings m=2 - source warnings m=1 - intrialization of uninitialized static variables -d name define 'name' -were treat all warnings errors (default off) -i debug inling code (default off) -on no or very minimal include directories Doptimization scontrolied by -0 command line option and #pragma. Optimize flags: -O0 no or very minimal optimization -01 regular optimization (produces shorter code and optimizes out some variables - this can make debugging more difficult) -Op promotes results of some 16 bit operations to 32 bits (can result in</license>	
command line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the indware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must de when using <b>Novo RTOS</b> . Where possible the hardware stack is used for nancy. By specifying the depth of hardware stack to use for main (and Novo st. ISR (interrupt service routine) s2 and low priority ISR (PIC18 only) s3, les control over when the software call stack is used instead of the hardware ack. The software call stack is applied to functions higher up in the call tree, is lower down the call tree still use the hardware sail stack. If no hardware depths are specified, then the software stack is only used in functions that on or call functions that contain a <b>Novo RTOS</b> Sys_Yield() function. <b>ple:</b> 6 2 routine will use hardware call stack up to a depth of 6 and then start using are call stack. Interrupt routine will use hardware call stack up to a depth of 1 to the address of the point where the code was interrupted, so in this example it eaves a hardware call stack depth 1 for subsequent calls within the ISR. <b>ostandow</b> ommand line switch tells the linker not to use the PIC18 shadow registers for up service routine (ISR) context saving. This option is required as a work d for silicon bugs in some PIC18's. <b>Nocontext</b> upts envice noties (ISR) context saving. This option is required as a work d for silicon bugs in some PIC18's. When use this prevents the linker adding code for context saving. This allow the programmer to generate their own hal ISR context saving be sumes that the ISR code will only modify w and and bsr catter saving example sumes that the ISR code will only modify w and and bsr catter cortext saving buffer at fixed address context I 2 leadodogi; Interrupt O	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line BoostC optimizing C compiler Version x.xx http://www.sourceboost.com Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 David Hobday Licensed to <license info=""> Usage: boostc.picl6.exe [options] files Options: -t name target processor (default name=PLC16F648A) -On optimization level (default n=1) n=0 - optimization turned off n=1 - optimization turned off n=1 - optimization turned off n=0 - no warnings m=0 - source warnings m=0 - source warnings m=0 - source warnings m=1 - source warnings m=1 - source warnings m=1 - source warnings m=2 - source warnings m=1 - intrialization of uninitialized static variables -d name define 'name' -were treat all warnings errors (default off) -i debug inling code (default off) -on no or very minimal include directories Doptimization scontrolied by -0 command line option and #pragma. Optimize flags: -O0 no or very minimal optimization -01 regular optimization (produces shorter code and optimizes out some variables - this can make debugging more difficult) -Op promotes results of some 16 bit operations to 32 bits (can result in</license>	
command line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the indware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must de when using <b>Novo RTOS</b> . Where possible the hardware stack is used for new, By specifying the depth of hardware stack to use for main (and Novo <b>s1</b> , ISR (interrupt service routine) <b>s2</b> and low priority ISR (PIC18 only) <b>s3</b> , see control over when the software call stack is used instead of the hardware ack. The software call stack is used instead of the hardware depths are specified, then the software stack is only used in functions that n or call functions that contain a <b>Novo RTOS</b> Sys_Yleid() function. <b>gle:</b> <b>6</b> 2 routine will use hardware call stack up to a depth of 6 and then start using are call stack. Interrupt routine will use hardware call stack up to a depth of 1 to the address of the point where the code was interrupted, so in this example it eaves a hardware call stack depth 1 for subsequent calls within the ISR. <b>osthadow</b> ommand line switch tells the linker not to use the PIC18 shadow registers for upt service routine (ISR) context saving. This option is required as a work d for silicon bugs in some PIC18's. <b>Decontext</b> option only works with PIC18's. When use this prevents the linker adding code for context saving code, or have none at all. <b>uple:</b> ntext saving example starts taying example start saving example	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line BoostC optimizing C compiler version x.xx http://www.sourceboost.com Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 David Hobday Licensed to <li>License info&gt; Usage: boostc.picl6.exe [options] files Options: -t name target processor (default name=PXC16F648A) -On optimization level (default n=1) n=0 - optimization turned off n=1 - optimization turned off n=4 - optimization turned off n=4 - optimization turned on n=6 - source warnings m=0 - no warnings n=0 - source warnings m=1 - optimization of uninitialized static variables -d name define 'name' -werr treat all varonings errors (default off) -i debug inline code (default off) -on we define 'name' - m generate dependencies file (default off) - v verbose mode turned on (default off) - j pathl;path2 additional include directories Dptimize flags: - On no or very minimal optimization - 01 regular optimization (produces shorter code and optimizes out some variables - this can make debugging more difficult) - Op promotes results of some 16 bit operations to 32 bits (can result in</li>	
command line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the riware call stack of the PIC. A function call that its made on the software call uses an extra byte of RAM to hold the return point number. This option must call when using Novo RTOS. Where possible the hardware stack is used for nor, By specifying the depth of hardware stack to use for main (and Novo st, ISR (interrupt service routine) s2 and low priority ISR (PIC18 only) s3, ese control over when the software call stack is used instead of the hardware depths are specified, then the software stack is only used in functions that nor call functions that contain a Novo RTOS Sys_Yfeld() function. pie: 6 2 routine will use hardware call stack up to a depth of 6 and then start using are call stack. Interrupt routine will use hardware call stack up to a depth of 2 tart using software call stack. An ISR uses hardware call stack depth of 1 to the address of the point where the code was interrupted, so in this example it eaves a hardware call stack. An ISR uses hardware call stack depth of 1 to the address of the point where the code was interrupted, so in this example it eaves a hardware call stack. An ISR uses hardware call stack depth of 1 to the address of the point where the code was interrupted, so in this example it eaves a hardware call stack depth 1 for subsequent calls within the ISR. <b>osthadow</b> ornmand line switch tells the linker not to use the PIC18 shadow registers for upt service routine (ISR) context saving. This option is required as a work d for sillion bugs in some PIC18's. <b>vecontext</b> potion only works with PIC18's. When use this prevents the linker adding cade for context saving code, or have none at all. <b>up:</b> ntext saving example suses that the ISR code will only modify. w and and bsr eare context saving buffer at fixed address context! 2 leonod0; Interrupt O as movef _bsr, _context	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC optimizing C Compiler Version X.XX http://www.Sourceboost.com Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 Pavel Baranov Usage: boostc.picl6.exe [options] files Options: - t name target processor (default name=PIC16F648A) - on optimization turned on men - optimization turned on men - optimization turned on - men warnings me1 - some warnings me1 - some warnings me1 - some warnings - verr treat warnings are errors (default off) - i debug inline code (default off) - i debug inline code (default off) - i debug inline code (default off) - i pathi;path2 additional include directories Dotimization Code optimization is controlled by -O command line option and #pragma. Optimized fags: - On no or very minimal optimization - or variables - this can make debugging more difficulti) - on promotes results of some 16 ht operations to 32 bits (can result in more efficient code is some cases).	
ommand line option to the linker tells it to use a software call stack in on to the hardware call stack. This allows subroutine calls deeper than the rdware call stack of the PIC. A function call that is made on the software call uses an extra byte of RAM to hold the return point number. This option must d when using <b>Novo RTOS</b> . Where possible the hardware stack is used for try, By specifying the depth of hardware stack to use for main (and Novo <b>s1</b> , ISR (interrupt service routine) <b>s2</b> and low priority ISR (PIC18 only) <b>s3</b> , es control over when the software call stack is used instead of the hardware ack. The software call stack is applied to functions higher up in the call tree, is lower down the call tree still use the hardware call stack. If no hardware depths are specified, then the software stack is only used in functions that no ro call functions that contain a <b>Novo RTOS</b> Sys_Yield() function. <b>ple:</b> 6 2 outline will use hardware call stack up to a depth of 6 and then start using rre call stack. Interrupt routine will use hardware call stack dup to a depth of 1 he address of the point where the code was interrupted, so in this example it taves a hardware call stack depth 1 for subsequent calls within the ISR. <b>schadow</b> ommand line switch tells the linker not to use the PIC18 shadow registers for pit service routine (ISR) context saving. This option is required as a work d for silicon bugs in some PIC18's. <b>xcontext</b> uption only works with PIC18's. When use this prevents the linker adding code for context saving code, or have none at all. <b>ple:</b> taxes saving example suses that the ISR code will only workfy w and and bsr taxes the ISR code will only workfy w and and bsr taxes the ISR code will only workfy w and and bsr taxes topicst, saving buffer at fixed, address	To get full list of BoostC compiler and BoostLink linker command line options run compiler or linker from command line. BoostC command line BoostC optimizing C compiler Version x.xx http://www.sourceboost.com Copyright(C) 2004-2007 Pavel Baranov Copyright(C) 2004-2007 David Hobday Licensed to <license info=""> Usage: boostc.picl6.exe [options] files Options: -t name target processor (default name=PLC16F648A) -On optimization level (default n=1) n=0 - optimization turned off n=1 - optimization turned off n=1 - optimization turned off n=0 - no warnings m=0 - source warnings m=0 - source warnings m=0 - source warnings m=1 - source warnings m=1 - source warnings m=1 - source warnings m=2 - source warnings m=1 - intrialization of uninitialized static variables -d name define 'name' -were treat all warnings errors (default off) -i debug inling code (default off) -on no or very minimal include directories Doptimization scontrolied by -0 command line option and #pragma. Optimize flags: -O0 no or very minimal optimization -01 regular optimization (produces shorter code and optimizes out some variables - this can make debugging more difficult) -Op promotes results of some 16 bit operations to 32 bits (can result in</license>	

#### iclude -icd2 Use this command line switch to add a NOP instruction at the first ROM address used (usually address). This is required on some devices for correct operation of Microchip ICD2 (In Circuit Debugger). #include <filename.h> ntax: ur *#include* "filename b" -hexeia Always add extended linear address record to .hex file. Without this switch an extended linear address record is only added to the .hex file if required by addresses included in the .hex file. ments: filename is any valid PC filename. It may include standard drive and path information. In the event no path is given, the following applies: a) If filename appears between "", the directory of the projects is libc Library searched first. When a project is being linked, SourceBoost IDE adds *libc.pic16.lib* or *libc.pic18.lib* to the linker command line, if it can find this library in its default b) If the delimiters <> are used, only the IDE include path list is searched for filename location. Iocation, The *libc* library contains necessary code for multiplication, division and dynamic memory allocation. It also includes code for string operations. If the file is not found, an error will be issued and compliation shall stop. Text from the include file ${\bf filename.h}$ is inserted at the point of the source where this directive appears, at compile time. mose: Code entry points #include <system.to camples: Entry points depend on the code address range using by the BoostLink linker. By default, the linker uses all available code space, but it's also possible to specify code start and end addresses that linker should use through linker command line options. For PIC16: Reset (main) entry point <code start> + 0x00 Interrupt entry point <code start> + 0x04 For PIC18: Reset (main) entry point <code start> + 0x00 Interrupt entry point <code start> + 0x08 Low priority ISR entry point <code start> + 0x18 SourceBoost IDE The SourceBoost IDE is thoroughly covered in a separate manual. . loostC™ Manual BoostC™ Manual SourceBoost Technologies Page 27 SourceBoost Technologies Page 25 fîne Preprocessor The pp.exe preprocessor is automatically invoked by the compiler. It executes a series of parametrized text substitutions and replacement (macro processing), besides evaluating special directives. #define id statement tax: All preprocessor directives start with a '#'. Non standard directives are always #define 1d(a, b...) statement contained in statements with a leading ANSI keyword *#pragma*, so to avoid potential conflicts when porting code to other compilers and/or with advanced source analysis tools (lint, static checkers, code formatters, flow analyzers and so id is any valid preprocessor identifier. nents: statement is any valid text. on). ${\bf a},\,{\bf b}$ and so on are local preprocessor identifiers, that in the given form model a function's formal parameters, separated by commas, Directives The following directives are supported by pp: Both forms produce a basic string replacement of **id** with the given **text**. Replacement will take place from the point where the #*define* pose: #include #define statement appears in the program, and below. #undef The second form represents a preprocessor pseudo-function. The local identifiers are positionally matched up with the original text, and are replaced with the text passed to the macro wherever it is #if #else used. #endif #define LEN 16 #define LOWNIBBLE(X) ((X) & GXOF) moles: #ifdef ≭ifndef a 69: #ermr le = a + LEN;// becomes le = a + 16; #warning b = LOWNIBBLE(a); // same as b = a & 0x0F; These directives are individually explained in the following pages.

3e 28

Page 26

SourceBoost Technologies

Jef itax: ments: rpose; amples:	<pre>#ifdef 1d code #endif id is any valid preprocessor identifier. code is one or more lines of valid C source code. When the preprocessor encounters this directive, it evaluates whether the identifier Id Is in its symbol table (eg previously specified within a #define statement). In case Id is defined, the lines of code between #iddef and #endif (or an optional #elee, if present) will be processed. In the opposite case, code statements between #iddef and #endif will be ignored by the compiler. NOTE: Id can not be a C variable ! Only preprocessor identifiers created via #define can be used. #define DEBUG printfC Reached test point #1"); #endif</pre>	#undef Syntax: Elementz Purpose: Example	Starting with the line where this directive appears, id will no more have meaning for the preprocessor, i.e. a subsequent <i>#ifdef id</i> shall evaluate to logical FALSE. Please note that id can then be reused and assigned a different value.
daf	fifndef 1d code fendif	BoostC" #If, #else, 4 Syntax:	fendif #1f expr code fe1se code
pose:	Id is any valid preprocessor identifier. code is one or more lines of valid C source code. When the preprocessor encounters this directive, it evaluates whether the identifier Id is in its symbol table (eg previously specified within a #define statement). In case Id is not defined, the lines of code between #ifndef and #endif (or an optional #else, if present) will be processed.	Elements: Purpose:	<pre>#endif expr is any valid expression using constants, standard operators and preprocessor identifiers. code is one or more valid C source code line. The preprocessor evaluates the constant expression expr and, if it is non-zero, will process the lines up to the optional #else or the #endif C is burdle burdle active is approach.</pre>
	<pre>#endif (or an opuonal #Energ, if present) will be processed. In the opposite case, code statements between #ifindef and #endif will be ignored by the compiler. NOTE: id can not be a C variable i Only preprocessor identifiers created via #define can be used. #ifindef DEBUG printf("Debug disabled !"); #else printf("Reached test point #1"); #endif</pre>	Examples:	<pre>#endif. Otherwise the optional #else branch code will be processed, if present. The latter two preprocessor directives are also used with specialized forms of the #if directive (see #ifdef, #ifndef). NOTE: expr cannot contain C variables 1 Only constant expressions and operators can be used. // conditionally initialize a RAM variable fif statTDELAY &gt; 20     slow = 0;     fendif</pre>
ge <b>32</b>	SourceBoost Technologies BoostC <sup>m</sup> Manual	Page <b>30</b>	SourceBoost Technologies BoostC <sup>rei</sup> Manual

ļ

				· · · · · · ·
ntax:	#pragma DATA addr, dl, dZ, or	Syntax:	<i>≇error</i> text	
ements:	#pragma DATA addr, "abcdefg1", "abcdefg2", addr is any valid code memory address.	Elements		
	d1, d2 are 8-bit integer constants.	Purpose:	When the preprocessor encounters this direc compliation and issues and error. The user su	tive, it stop: Ipplied <b>text</b> is
	"abcdefgX" is a character string, the ASCII values of the charcters will be stored as 8 bit value.		printed as an informational message. This directive is useful when coupled with the expre	ession checking
rpose:	User data can be placed at a specific location using this construct. In particular, this can be used to specify target configuration word or to set some calibration/configuration data into on-chip eeprom.		features of the preprocessor, to validate the configuration choices and defines made elsewhere and include files (or on the command line).	coherence a
amples:	<pre>#pragma: DATA 0x200, 0xA, 0xB, "test" //Set PICLE configuration word</pre>	Examples	Ferror "MUST define a default value for spee	d !"
	//Set prici6 configuration word #pragma DATA 0x2007, HS_DSC & _MOT_OFF & _LVP_OFF //Put some data into eeprom #pragma DATA 0x2100, "0x12, 0x34, 0x356, 0x78, "ABCD"		<b>∦endif</b> £ (11) (en 131 - 21)en en anter (11) I	
	#pragma DATA 0x2100, 0x12, 0x34, 0x56, 0x78, "ABCD"			
ostC <sup>™</sup> M	anual SourceBoost Technologies Page 35	BoostC <sup>74</sup>	Manual SourceBoost Technologies	Page 33
na CLOG	K FREQ	#waming		
		#warning	Avarning text	
×	al for an	Syntax:	An and show a start start was a second start of the start start of the start	
x: nts:	#pragma_CLOCK_FREQ_Frequency_in_kz Frequency_in_Hz is the processor's clock speed.		#warning text text is any valid text. When the preprocessor encounters this directive, it	
x: nts:	#pragma CLOCK_FREQ Frequency_in_Hz Frequency_in_Hz is the processor's clock speed. The CLOCK_FREQ directive tells the compiler under what clock frequency the code is expected to run.	Syntax: Elements:	#warning text text is any valid text. When the preprocessor encounters this directive, it compiler to issue a warning. The user supplied text is an informational message.	printed as
k; nts: se;	#pragma CLOCK_FREQ Frequency_in_tz Frequency_in_Hz is the processor's clock speed. The CLOCK_FREQ directive tells the compiler under what clock frequency the code is expected to run. Note: delay code generated by the linker is based on this figure.	Syntax: Elements:	Awarning text text is any valid text. When the preprocessor encounters this directive, it compiler to issue a warning. The user supplied text is an informational message. This directive is useful when coupled with the expressi- features of the preprocessor, to validate the co	printed as on Checking herence of
x; nts: se;	#pragma CLOCK_FREQ Frequency_in_Hz Frequency_in_Hz is the processor's clock speed. The CLOCK_FREQ directive tells the compiler under what clock frequency the code is expected to run.	Syntax: Elements:	#warning text text is any valid text. When the preprocessor encounters this directive, it compiler to issue a warning. The user supplied text is an informational message. This directive is useful when coupled with the expression	printed as on Checking herence of
<; nts: 5e;	#pragma CLOCK_FREQ Frequency_in_tz Frequency_in_Hz is the processor's clock speed. The CLOCK_FREQ directive tells the compiler under what clock frequency the code is expected to run. Note: delay code generated by the linker is based on this figure.	Syntax: Elements:	#warning text text is any valid text. When the preprocessor encounters this directive, it compiler to issue a warning. The user supplied text is an informational message. This directive is useful when coupled with the expressi features of the preprocessor, to validate the co configuration choices and defines made elsewhere in i	printed as on checking herence of the sources
nma CLOC x: sets: se: ples:	#pragma CLOCK_FREQ Frequency_in_tz Frequency_in_Hz is the processor's clock speed. The CLOCK_FREQ directive tells the compiler under what clock frequency the code is expected to run. Note: delay code generated by the linker is based on this figure.	Syntax: Elements: Purpose: Examples: Pragma dii	Awarning text text is any valid text. When the preprocessor encounters this directive, it compiler to issue a warning. The user supplied text is an informational message. This directive is useful when coupled with the expressi- features of the preprocessor, to validate the co- configuration choices and defines made elsewhere in it and include files (or on the command line). fifndef NODEADOR warning "ADDR not defined; will enter dynamic a fendit	printed as on Checking herence of the sources mode
x: :nts: se:	#pragma CLOCK_FREQ Frequency_in_tz Frequency_in_Hz is the processor's clock speed. The CLOCK_FREQ directive tells the compiler under what clock frequency the code is expected to run. Note: delay code generated by the linker is based on this figure.	Syntax: Elements: Purpose: <b>Examples:</b> Pragma dit Specific Boost to avoid pote	<pre>#warning text text is any valid text. When the preprocessor encounters this directive, it compiler to issue a warning. The user supplied text is an informational message. This directive is useful when coupled with the expressi features of the preprocessor, to validate the co configuration choices and defines made elsewhere in is and include files (or on the command line). fifndef NODEADOR     #warning 'ADOR not defined, will enter dynamic. #centives C preprocessor directives all follow the ANSI keyword # that conflicts when porting code to other compilers a </pre>	printed as on checking herence of the sources wode." <i>pragma</i> , so and/or with
x; nts: se;	#pragma CLOCK_FREQ Frequency_in_tz Frequency_in_Hz is the processor's clock speed. The CLOCK_FREQ directive tells the compiler under what clock frequency the code is expected to run. Note: delay code generated by the linker is based on this figure.	Syntax: Elements: Purpose: Examples: Pragma diu Specific Boost to avoid pote advanced so analyzers and	Awarning text text is any valid text. When the preprocessor encounters this directive, it compiler to issue a warning. The user supplied text is an informational message. This directive is useful when coupled with the expressi- features of the preprocessor, to validate the co- configuration choices and defines made elsewhere in t and include files (or on the command line). #ifndef MODEADOR 	printed as on checking herence of the sources wode." pragma, so and/or with
x; nts: se;	#pragma CLOCK_FREQ Frequency_in_tz Frequency_in_Hz is the processor's clock speed. The CLOCK_FREQ directive tells the compiler under what clock frequency the code is expected to run. Note: delay code generated by the linker is based on this figure.	Syntax: Elements: Purpose: Examples: Pragma diu Specific Boost to avoid pote advanced so analyzers and	<pre>#warning text text is any valid text. When the preprocessor encounters this directive, it compiler to issue a warning. The user supplied text is an informational message. This directive is useful when coupled with the expressi features of the preprocessor, to validate the co configuration choices and defines made elsewhere in t and include files (or on the command line). fifndef NODEDOR warning "ADOR not defined; will enter dynamic a rectives C preprocessor directives all follow the ANSI keyword # ntial conflicts when porting code to other compilers a irce analysis tools (lint, static checkers, code forma so on). directives are supported by pp:</pre>	printed as on checking herence of the sources wode." pragma, so and/or with
<; nts: 5e;	#pragma CLOCK_FREQ Frequency_in_tz Frequency_in_Hz is the processor's clock speed. The CLOCK_FREQ directive tells the compiler under what clock frequency the code is expected to run. Note: delay code generated by the linker is based on this figure.	Syntax: Elements: Purpose: Examples: Examples: Specific Boost to avoid pote advanced sou analyzers and The following #pragma DA #pragma CL	<pre>#warning text text is any valid text. When the preprocessor encounters this directive, it compiler to issue a warning. The user supplied text is an informational message. This directive is useful when coupled with the expressi features of the preprocessor, to validate the co configuration choices and defines made elsewhere in it and include files (or on the command line). fifndef NODEADOR   #warning "ADDR not defined, will enter dynamic =   #endif ectives C preprocessor directives all follow the ANSI keyword #   ntial conflicts when porting code to other compilers a   irce analysis tools (lint, static checkers, code formation on an elsewhere in the   considered by pp: TA DCK_FREQ</pre>	printed as on checking herence of the sources wode." <i>pragma</i> , so and/or with
x; nts: se;	#pragma CLOCK_FREQ Frequency_in_tz Frequency_in_Hz is the processor's clock speed. The CLOCK_FREQ directive tells the compiler under what clock frequency the code is expected to run. Note: delay code generated by the linker is based on this figure.	Syntax: Elements: Purpose: Examples: Pragma din Specific Boost to avoid pote advanced sou analyzers and The following. #pragma DA #pragma OP	<pre>#warning text text is any valid text. When the preprocessor encounters this directive, it compiler to issue a warning. The user supplied text is an informational message. This directive is useful when coupled with the expressi features of the preprocessor, to validate the co configuration choices and defines made elsewhere in it and include files (or on the command line). fifndef NODEADOR   #warning "ADDR not defined, will enter dynamic =   #endif ectives C preprocessor directives all follow the ANSI keyword #   ntial conflicts when porting code to other compilers a   irce analysis tools (lint, static checkers, code formation on an elsewhere in the   considered by pp: TA DCK_FREQ</pre>	printed as on checking herence of the sources wode." <i>pragma</i> , so and/or with
x; nts: se;	#pragma CLOCK_FREQ Frequency_in_tz Frequency_in_Hz is the processor's clock speed. The CLOCK_FREQ directive tells the compiler under what clock frequency the code is expected to run. Note: delay code generated by the linker is based on this figure.	Syntax: Elements: Purpose: Examples: Pragma din Specific Boost to avoid pote advanced sou analyzers and The following. #pragma DA #pragma OP	<pre>#warning text text is any valid text. When the preprocessor encounters this directive, it compiler to issue a warning. The user supplied text is an informational message. This directive is useful when coupled with the expressi features of the preprocessor, to validate the co configuration choices and defines made elsewhere in is and include files (or on the command line).  #ifindef NODEADOR #warning 'ADDR not defined, will enter dynamic # #endif C preprocessor directives all follow the ANSI keyword # nitial conflicts when porting code to other compilers a irce analysis tools (lint, static checkers, code formation on). Birectives are supported by pp: TA DCK_FREQ TIMIZE</pre>	printed as on checking herence of the sources wode." <i>pragma</i> , so and/or with
x; nts: se;	#pragma CLOCK_FREQ Frequency_in_tz Frequency_in_Hz is the processor's clock speed. The CLOCK_FREQ directive tells the compiler under what clock frequency the code is expected to run. Note: delay code generated by the linker is based on this figure.	Syntax: Elements: Purpose: Examples: Pragma din Specific Boost to avoid pote advanced sou analyzers and The following. #pragma DA #pragma OP	<pre>#warning text text is any valid text. When the preprocessor encounters this directive, it compiler to issue a warning. The user supplied text is an informational message. This directive is useful when coupled with the expressi features of the preprocessor, to validate the co configuration choices and defines made elsewhere in is and include files (or on the command line).  #ifindef NODEADOR #warning 'ADDR not defined, will enter dynamic # #endif C preprocessor directives all follow the ANSI keyword # nitial conflicts when porting code to other compilers a irce analysis tools (lint, static checkers, code formation on). Birectives are supported by pp: TA DCK_FREQ TIMIZE</pre>	printed as on checking herence of the sources wode." <i>pragma</i> , so and/or with
x: ints: se:	#pragma CLOCK_FREQ Frequency_in_tz Frequency_in_Hz is the processor's clock speed. The CLOCK_FREQ directive tells the compiler under what clock frequency the code is expected to run. Note: delay code generated by the linker is based on this figure.	Syntax: Elements: Purpose: Examples: Pragma din Specific Boost to avoid pote advanced sou analyzers and The following. #pragma DA #pragma OP	<pre>#warning text text is any valid text. When the preprocessor encounters this directive, it compiler to issue a warning. The user supplied text is an informational message. This directive is useful when coupled with the expressi features of the preprocessor, to validate the co configuration choices and defines made elsewhere in is and include files (or on the command line).  #ifindef NODEADOR #warning 'ADDR not defined, will enter dynamic # #endif C preprocessor directives all follow the ANSI keyword # nitial conflicts when porting code to other compilers a irce analysis tools (lint, static checkers, code formation on). Birectives are supported by pp: TA DCK_FREQ TIMIZE</pre>	printed as on checking herence of the sources wode." <i>pragma</i> , so and/or with
x: :nts: se:	#pragma CLOCK_FREQ Frequency_in_tz Frequency_in_Hz is the processor's clock speed. The CLOCK_FREQ directive tells the compiler under what clock frequency the code is expected to run. Note: delay code generated by the linker is based on this figure.	Syntax: Elements: Purpose: Examples: Pragma din Specific Boost to avoid pote advanced sou analyzers and The following. #pragma DA #pragma OP	<pre>#warning text text is any valid text. When the preprocessor encounters this directive, it compiler to issue a warning. The user supplied text is an informational message. This directive is useful when coupled with the expressi features of the preprocessor, to validate the co configuration choices and defines made elsewhere in is and include files (or on the command line).  #ifindef NODEADOR #warning 'ADDR not defined, will enter dynamic # #endif C preprocessor directives all follow the ANSI keyword # nitial conflicts when porting code to other compilers a irce analysis tools (lint, static checkers, code formation on). Birectives are supported by pp: TA DCK_FREQ TIMIZE</pre>	printed as on checking herence of the sources wode." pragma, so and/or with
x: :nts: se:	#pragma CLOCK_FREQ Frequency_in_tz Frequency_in_Hz is the processor's clock speed. The CLOCK_FREQ directive tells the compiler under what clock frequency the code is expected to run. Note: delay code generated by the linker is based on this figure.	Syntax: Elements: Purpose: Examples: Pragma din Specific Boost to avoid pote advanced sou analyzers and The following. #pragma DA #pragma OP	<pre>#warning text text is any valid text. When the preprocessor encounters this directive, it compiler to issue a warning. The user supplied text is an informational message. This directive is useful when coupled with the expressi features of the preprocessor, to validate the co configuration choices and defines made elsewhere in is and include files (or on the command line).  #ifindef NODEADOR #warning 'ADDR not defined, will enter dynamic # #endif C preprocessor directives all follow the ANSI keyword # nitial conflicts when porting code to other compilers a irce analysis tools (lint, static checkers, code formation on). Birectives are supported by pp: TA DCK_FREQ TIMIZE</pre>	printed as on checking herence of the sources wode." pragma, so and/or with
x: :nts: se:	#pragma CLOCK_FREQ Frequency_in_tz Frequency_in_Hz is the processor's clock speed. The CLOCK_FREQ directive tells the compiler under what clock frequency the code is expected to run. Note: delay code generated by the linker is based on this figure.	Syntax: Elements: Purpose: Examples: Pragma din Specific Boost to avoid pote advanced sou analyzers and The following. #pragma DA #pragma OP	<pre>#warning text text is any valid text. When the preprocessor encounters this directive, it compiler to issue a warning. The user supplied text is an informational message. This directive is useful when coupled with the expressi features of the preprocessor, to validate the co configuration choices and defines made elsewhere in is and include files (or on the command line).  #ifindef NODEADOR #warning 'ADDR not defined, will enter dynamic # #endif C preprocessor directives all follow the ANSI keyword # nitial conflicts when porting code to other compilers a irce analysis tools (lint, static checkers, code formation on). Birectives are supported by pp: TA DCK_FREQ TIMIZE</pre>	printed as on checking herence of the sources wode." pragma, so and/or with
x; nts: se;	#pragma CLOCK_FREQ Frequency_in_tz Frequency_in_Hz is the processor's clock speed. The CLOCK_FREQ directive tells the compiler under what clock frequency the code is expected to run. Note: delay code generated by the linker is based on this figure.	Syntax: Elements: Purpose: Examples: Pragma din Specific Boost to avoid pote advanced sou analyzers and The following. #pragma DA #pragma OP	<pre>#warning text text is any valid text. When the preprocessor encounters this directive, it compiler to issue a warning. The user supplied text is an informational message. This directive is useful when coupled with the expressi features of the preprocessor, to validate the co configuration choices and defines made elsewhere in is and include files (or on the command line).  #ifindef NODEADOR #warning 'ADDR not defined, will enter dynamic # #endif C preprocessor directives all follow the ANSI keyword # nitial conflicts when porting code to other compilers a irce analysis tools (lint, static checkers, code formation on). Birectives are supported by pp: TA DCK_FREQ TIMIZE</pre>	printed as on checking herence of the sources wode." pragma, so and/or with
x: ints: se:	#pragma CLOCK_FREQ Frequency_in_tz Frequency_in_Hz is the processor's clock speed. The CLOCK_FREQ directive tells the compiler under what clock frequency the code is expected to run. Note: delay code generated by the linker is based on this figure.	Syntax: Elements: Purpose: Examples: Pragma din Specific Boost to avoid pote advanced sou analyzers and The following. #pragma DA #pragma OP	<pre>#warning text text is any valid text. When the preprocessor encounters this directive, it compiler to issue a warning. The user supplied text is an informational message. This directive is useful when coupled with the expressi features of the preprocessor, to validate the co configuration choices and defines made elsewhere in is and include files (or on the command line).  #ifindef NODEADOR #warning 'ADDR not defined, will enter dynamic # #endif C preprocessor directives all follow the ANSI keyword # nitial conflicts when porting code to other compilers a irce analysis tools (lint, static checkers, code formation on). Birectives are supported by pp: TA DCK_FREQ TIMIZE</pre>	printed as on checking herence of the sources wode." <i>pragma</i> , so and/or with

L

itialization of EEPROM Data	#pragma OPTIMIZE
s often desirable to program the PIC on board EEPROM with initial data as part he programming process. This initial data can be included in the source code.	
ROM initialization data is set using the pragma directive: <i>#pragma DATA</i> . <b>ample:</b>	Syntax: #pragma OPTIMIZE "Flags"
Initializes EEPROM with data: OC 22 38 48 45 4C 4C 4F 00 FE 99	Elements: Flags are the optimization flags also used on the command line,
ragma DATA _EEPROM, 12, 34, 56, "HELLO", 0xFE, 0b10011001	Purpose:       This directive sets new optimization, at function level. It must be used in the global scope and applies to the function that follows this pragma.         The pragma argument should be enclosed into quotes and is same as argument of the -O compiler command line options.         Empty quotes reset the optimization level previously set by this pragma.         This is the current list of valid optimization flags:         0       no or very minimal optimization         1       regular optimization (recommended)         a       aggressive optimization         promotes results of some 16 bit operations to 32 bits
	Examples: //Use aggressive optimization for function 'foo' #pragea ortHIZE "a" yoid foo() { }
PostC <sup>m</sup> Manual SourceBoost Technologies Page <b>39</b>	BoostC <sup>m</sup> Manual SourceBoost Technologies Page 37
s a quick reference of BoostC and its peculiarities due to the specific PIC t platform.	Setting Device Configuration Options In order for a program to be able to run on a target device the device configuration options need to be correctly set. For example having the wrong oscillator configuration setting may mean that the device has no dock, making it impossible for any code to be executed. Configuration data is set using the pragma directive: #pragma DATA. Configuration options typically control:
section of the manual contains a condensed list of BoostC C compiler features. In no way intended to replace a complete C language manual or ANSI/ISO fication. It is targeted, instead, at the already expert C programmer that a guick reference of BoostC and its peculiarities due to the specific PIC t platform. <b>Pram structure</b> r source file should include the general system header file, that in turn	In order for a program to be able to run on a target device the device configuration options need to be correctly set. For example having the wrong oscillator configuration setting may mean that the device has no clock, making it impossible for any code to be executed. Configuration data is set using the pragma directive: <i>#pragma DATA</i> . Configuration options typically control: • Oscillator configuration
section of the manual contains a condensed list of BoostC C compiler features. In no way intended to replace a complete C language manual or ANSI/ISO fication. It is targeted, instead, at the already expert C programmer that a guick reference of BoostC and its peculiarities due to the specific PIC t platform. Gram structure / source file should include the general system header file, that in turn les target specific header (containing register mapped variables specific for	In order for a program to be able to run on a target device the device configuration options need to be correctly set. For example having the wrong oscillator configuration setting may mean that the device has no clock, making it impossible for any code to be executed. Configuration data is set using the pragma directive: #pragma DATA. Configuration options typically control: • Oscillator configuration • Brown out reset
section of the manual contains a condensed list of BoostC C compiler features. In no way intended to replace a complete C language manual or ANSI/ISO fication. It is targeted, instead, at the already expert C programmer that is a guick reference of BoostC and its peculiarities due to the specific PIC t platform. <b>Pram structure</b> r source file should include the general system header file, that in turn tes target specific header (containing register mapped variables specific for arget), some internal functions prototypes:	In order for a program to be able to run on a target device the device configuration options need to be correctly set. For example having the wrong oscillator configuration setting may mean that the device has no clock, making it impossible for any code to be executed. Configuration data is set using the pragma directive: <i>#pragma DATA</i> . Configuration options typically control: • Oscillator configuration
section of the manual contains a condensed list of BoostC C compiler features. In no way intended to replace a complete C language manual or ANSI/ISO fication. It is targeted, instead, at the already expert C programmer that is a guick reference of BoostC and its peculiarities due to the specific PIC t platform. <b>Pram structure</b> r source file should include the general system header file, that in turn tes target specific header (containing register mapped variables specific for arget), some internal functions prototypes:	In order for a program to be able to run on a target device the device configuration options need to be correctly set. For example having the wrong oscillator configuration setting may mean that the device has no clock, making it impossible for any code to be executed. Configuration data is set using the pragma directive: #pragma DATA. Configuration options typically control: • Oscillator configuration • Brown out reset • Power up reset timer • Watchdog configuration • Peripheral configurations
section of the manual contains a condensed list of BoostC C compiler features. In no way intended to replace a complete C language manual or ANSI/ISO fication. It is targeted, instead, at the already expert C programmer that is a quick reference of BoostC and its peculiantities due to the specific PIC t platform. <b>Commentation</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b> <b>transform</b>	In order for a program to be able to run on a target device the device configuration options need to be correctly set. For example having the wrong oscillator configuration setting may mean that the device has no clock, making it impossible for any code to be executed. Configuration data is set using the pragma directive: #pragma DATA. Configuration options typically control: • Oscillator configuration • Brown out reset • Power up reset timer • Watchdog configuration • Peripheral configurations • Pin configurations
section of the manual contains a condensed list of BoostC C compiler features. In no way intended to replace a complete C language manual or ANSI/ISO fication. It is targeted, instead, at the already expert C programmer that a guick reference of BoostC and its peculiarities due to the specific PIC t platform. <b>gram structure</b> / source file should include the general system header file, that in turn des target specific header (containing register mapped variables specific for arget), some internal functions prototypes: nanipulation function prototypes: unde csystem.he	In order for a program to be able to run on a target device the device configuration options need to be correctly set. For example having the wrong oscillator configuration setting may mean that the device has no clock, making it impossible for any code to be executed. Configuration data is set using the pragma directive: #pragma DATA. Configuration options typically control: • Oscillator configuration • Brown out reset • Power up reset timer • Watchdog configuration • Peripheral configurations
section of the manual contains a condensed list of BoostC C compiler features. In no way intended to replace a complete C language manual or ANSI/ISO fication. It is targeted, instead, at the already expert C programmer that a quick reference of BoostC and its peculiarities due to the specific PIC t platform. <b>gram structure</b> <i>i</i> source file should include the general system header file, that in turn fes target specific header (containing register mapped variables specific for argect), some internal functions prototypes needed for code generation and g manipulation function prototypes: lude esystem.h>	In order for a program to be able to run on a target device the device configuration options need to be correctly set. For example having the wrong oscillator configuration setting may mean that the device has no clock, making it impossible for any code to be executed. Configuration data is set using the pragma directive: #pragma DATA. Configuration options typically control: • Oscillator configuration • Brown out reset • Power up reset timer • Watchdog configuration • Peripheral configurations • Pin configurations • Low voltage programming
section of the manual contains a condensed list of BoostC C compiler features. In no way intended to replace a complete C language manual or ANSI/ISO fication. It is targeted, instead, at the already expert C programmer that a quick reference of BoostC and its peculiarities due to the specific PIC t platform. gram structure / source file should include the general system header file, that in turn les target specific header (containing register mapped variables specific for target), some internal functions prototypes needed for code generation and manipulation function prototypes: lude csystem.b a types a data types Size Type 1 bit bit, bool	In order for a program to be able to run on a target device the device configuration options need to be correctly set. For example having the wrong oscillator configuration setting may mean that the device has no dock, making it impossible for any code to be executed. Configuration data is set using the pragma directive: #pragma DATA. Configuration options typically control: • Oscillator configuration • Brown out reset • Power up reset timer • Watchdog configuration • Peripheral configurations • Pin configurations • Low voltage programming • Memory protection • Table read protection • Stack overflow handling
section of the manual contains a condensed list of BoostC C compiler features. In no way intended to replace a complete C language manual or ANSI/ISO fication. It is targeted, instead, at the already expert C programmer that a quick reference of BoostC and its peculiarities due to the specific PIC t platform. gram structure / source file should include the general system header file, that in turn les target specific header (containing register mapped variables specific for argech, some internal functions prototypes needed for code generation and manipulation function prototypes: lude csystem.b a types size Type 1 bit bit, bool 8 bits char, unsigned char, signed char	In order for a program to be able to run on a target device the device configuration options need to be correctly set. For example having the wrong oscillator configuration setting may mean that the device has no clock, making it impossible for any code to be executed. Configuration data is set using the pragma directive: #pragma DATA. Configuration options typically control: • Oscillator configuration • Brown out reset • Power up reset timer • Watchdog configuration • Peripheral configuration • Peripheral configuration • Low voltage programming • Memory protection • Table read protection • Stack overflow handling The exact configuration options available depend on exactly which device is being used. The PIC18 devices have many more configuration options that the
section of the manual contains a condensed list of BoostC C compiler features. In no way intended to replace a complete C language manual or ANSI/ISO fication. It is targeted, instead, at the already expert C programmer that is a guick reference of BoostC and its peculiantities due to the specific PIC t platform. gram structure / source file should include the general system header file, that in turn les target specific header (containing register mapped variables specific for target), some internal functions prototypes needed for code generation and manipulation function prototypes: ude csystem.hb a types data types Size Type 1 bit bit, bool 8 bits char, unsigned char, signed char 6 bits short, unsigned short, signed short	In order for a program to be able to run on a target device the device configuration options need to be correctly set. For example having the wrong oscillator configuration setting may mean that the device has no dock, making it impossible for any code to be executed. Configuration data is set using the pragma directive: #pragma DATA. Configuration options typically control: • Oscillator configuration • Brown out reset • Power up reset timer • Watchdog configuration • Peripheral configurations • In configurations • Low voltage programming • Memory protection • Table read protection • Stack overflow handling The exact configuration options available depend on exactly which device is being used. The FIC18 devices have many more configuration options that the PIC16/PIC12 devices.
section of the manual contains a condensed list of BoostC C compiler features. In no way intended to replace a complete C language manual or ANSI/ISO fication. It is targeted, instead, at the already expert C programmer that is a guick reference of BoostC and its peculiantites due to the specific PIC t platform. pram structure r source file should include the general system header file, that in turn les target specific header (containing register mapped variables specific for arget), some internal functions prototypes needed for code generation and imanipulation function prototypes: ude csystem.hb 1 types data types Size Type 1 bit bit, bool 8 bits char, unsigned char, signed char 6 bits short, unsigned int, signed short 6 bits int, unsigned int, signed int	In order for a program to be able to run on a target device the device configuration options need to be correctly set. For example having the wrong oscillator configuration setting may mean that the device has no dock, making it impossible for any code to be executed. Configuration data is set using the pragma directive: #pragma DATA. Configuration options typically control: • Oscillator configuration • Brown out reset • Power up reset timer • Watchdog configuration • Peripheral configurations • Pin configurations • Pin configurations • Low vokage programming • Memory protection • Stack overflow handling The exact configuration options available depend on exactly which device is being used. The PIC18 devices have many more configuration options that the PIC16/PIC12 devices.
section of the manual contains a condensed list of BoostC C compiler features. In no way intended to replace a complete C language manual or ANSI/ISO fication. It is targeted, instead, at the already expert C programmer that is a guick reference of BoostC and its peculiantites due to the specific PIC t platform. pram structure r source file should include the general system header file, that in turn les target specific header (containing register mapped variables specific for arget), some internal functions prototypes needed for code generation and manipulation function prototypes: ude csystem.b 1 types data types Size Type 1 bit bit, bool 8 bits char, unsigned char, signed char 6 bits short, unsigned int, signed int 2 bits long, unsigned int, signed long Mifference between bit and bool data types is in the way how an expression	In order for a program to be able to run on a target device the device configuration options need to be correctly set. For example having the wrong oscillator configuration setting may mean that the device has no dock, making it impossible for any code to be executed. Configuration data is set using the pragma directive: #pragma DATA. Configuration options typically control: • Oscillator configuration • Brown out reset • Power up reset timer • Watchdog configuration • Peripheral configurations • Pin configurations • Pin configurations • Din configurations • Low votage programming • Memory protection • Stack overflow handling The exact configuration options available depend on exactly which device is being used. The PIC18 devices have many more configuration options that the PIC16/PIC12 devices.
section of the manual contains a condensed list of BoostC C compiler features. In no way intended to replace a complete C language manual or ANSI/ISO fication. It is targeted, instead, at the already expert C programmer that is a quick reference of BoostC and its peculiantities due to the specific PIC t platform. <b>Tram structure</b> r source file should include the general system header file, that in turn less target specific header (containing register mapped variables specific for arget), some internal functions prototypes needed for code generation and i manipulation function prototypes: ude csystem.hb <b>a types</b> <b>data types</b> <b>size Type</b> <b>1 bit</b> bit, bool <b>8 bits</b> char, unsigned char, signed char <b>6 bits</b> short, unsigned fint, signed short <b>6 bits</b> int, unsigned lint, signed int <b>2 bits</b> long, unsigned long, signed long Mifference between bit and bool data types is in the way how an expression er than 1 bit) is assigned to a bit or bool operands.	In order for a program to be able to run on a target device the device configuration options need to be correctly set. For example having the wrong oscillator configuration setting may mean that the device has no clock, making it impossible for any code to be executed. Configuration data is set using the pragma directive: #pragma DATA. Configuration options typically control: • Oscillator configuration • Brown out reset • Power up reset timer • Watchdog configuration • Peripheral configurations • Pin configurations • Pin configurations • Low voltage programming • Memory protection • Table read protection • Stack overflow handling The exact configuration options available depend on exactly which device is being used. The PIC18 devices have many more configuration options that the PIC16/PIC12 devices. Configuration for .PIC16/PIC12 devices.
<pre>section of the manual contains a condensed list of BoostC C compiler features. In no way intended to replace a complete C language manual or ANSI/ISO ification. It is targeted, instead, at the already expert C programmer that is a guick reference of BoostC and its peculiantities due to the specific PIC t platform. pram structure / source file should include the general system header file, that in turn des target specific header (containing register mapped variables specific for argect), source file should include the general system header file, that in turn des target specific header (containing register mapped variables specific for argect), source file should include the general system header file, that in turn des target specific header (containing register mapped variables specific for argect), source internal functions prototypes needed for code generation and g manipulation function prototypes: ude csystem.b a types size Type 1 bit bit, bool 8 bits char, unsigned char, signed char 6 bits short, unsigned short, signed short 6 bits int, unsigned int, signed int 2 bits long, unsigned long, signed long difference between bit and bool data types is in the way how an expression er than 1 bit is assigned to a bit or bool operands. it operands receive the least significant bit of the right side expression; ool operands receive the value of the right side expression casted to bool, xample: * * * * * * * * * *</pre>	In order for a program to be able to run on a target device the device configuration options need to be correctly set. For example having the wrong oscillator configuration setting may mean that the device has no clock, making it impossible for any code to be executed. Configuration data is set using the pragma directive: #pragma DATA. Configuration options typically control: • Oscillator configuration • Brown out reset • Power up reset three • Watchdog configuration • Peripheral configurations • Pin configurations • Pin configurations • Low voltage programming • Memory protection • Table read protection • Stack overflow handling The exact configuration options available depend on exactly which device is being used. The PIC18 devices have many more configuration options that the PIC16/PIC12 devices Configuration for PIC156/874A. #Pragma DATACONFIG. * Configuration for PIC156/874A. #Pragma DATACONFIG. * Stack overflow handling The exact configuration for PIC156/874A. #Pragma DATACONFIG. * Configuration for PIC156/874A. #Pragma DATACONFIG. * Stack overflow handling The configuration for PIC156/874A. #Pragma DATACONFIG. * Stack overflow handling ************************************
section of the manual contains a condensed list of BoostC C compiler features. In no way intended to replace a complete C language manual or ANSI/ISO fication. It is targeted, instead, at the already expert C programmer that a quick reference of BoostC and its peculiarities due to the specific PIC t platform. <b>gram structure</b> r source file should include the general system header file, that in turn des target specific header (containing register mapped variables specific for argech, some internal functions prototypes needed for code generation and g manipulation function prototypes: lude csystem.hb <b>a types</b> <b>size Type</b> 1 bit bit, bool 8 bits char, unsigned char, signed char 66 bits short, unsigned short, signed short 66 bits not, unsigned ling, signed ling 27 bits long, unsigned long, signed long difference between bit and bool data types is in the way how an expression ter than 1 bit) is assigned to a bit or bool operands. H operands receive the least significant bit of the right side expression; bool operands receive the value of the right side expression casted to bool, example: <b>b</b>	In order for a program to be able to run on a target device the device configuration options need to be correctly set. For example having the wrong oscillator configuration setting may mean that the device has no clock, making it impossible for any code to be executed. Configuration data is set using the pragma directive: #pragma DATA Configuration options typically control: • Oscillator configuration • Brown out reset • Power up reset thmer • Watchdog configuration • Peripheral configurations • Pin configurations • Low voltage programming • Memory protection • Table read protection • Table read protection • Table read protection • Stack overflow handling The exact configuration savailable depend on exactly which device is being used. The PIC18 devices have many more configuration options that the PIC16/PIC12 devices. <b>Configuration</b> for PIC166874A *pragma DATACONFIGCP_OFF & _METE_OFF & _MET_OFF & _MES_OSC & _LVP_OFF <b>Configuration for</b> PIC168875 <b>Configuration for</b> PIC168757 <b>Configuration for</b> PIC1687574 *pragma DATACONFIGCP_OFF & _METE_OFF & _MET_OFF & _MES_OSC & _LVP_OFF

Typedef New names for data types can be defined using typedef operation: typedef unsigned char uchar; <b>Enum</b> Enumerated data types are an handy type of automatically defined constant series, the declaration assigns a value of zero to the first symbolic constant of the list, and the assigns subsequent values (automatically incremented) to the following constants. The user can, as well, arbitrarily assign numerical (signed) values at the beginning as well as in the mildée of the series. Values following an explicit assignment use that value as a base and keep on incrementing from that point. The data type for an enum type or typedef variable is, as per ANSI definition, the smaller type that can contain the absolute maximum value of the constant series. <b>Curue ETYpes { ENONE-0, EXED; EGREEN, É.GLUE };</b> #define E.CREEN ? #
<pre>typedef unsigned char uchar; function assigns a value of zero to the first symbolic constant series, find declaration assigns a value of zero to the first symbolic constant of the list, and the assigns subsequent values (automatically incremented) to the following constants. The user can, as well, arbitrarily assign numerical (signed) values at the beginning as that middle of the series. Values following an explicit assignment use that value as a base and keep on incrementing from that point. The data type for an enum type or typedef variable is, as per ANSI definition, the smaller type that can contain the absolute maximum value of the constant series. for that can contain the absolute maximum value of the constant series. for the task is the finite is the following finite is in the finite of finite is intered of finite is intere</pre>
Enum Finumerated data types are an handy type of automatically defined constant series. The declaration assigns a value of zero to the first symbolic constant of the list, and the assigns subsequent values (automatically incremented) to the following constants. The care can, as well, arbitrarily assign numerical (signed) values at the beginning as well as in the middle of the series. Values following an explicit assignment use that value as a base and keep on incrementing from that point. The data type for an enum type or typedef variable is, as per ANSI definition, the smaller type that can contain the absolute maximum value of the constant series. cnum ETypes { ENONE 0, EAED; EGREDH, EGREDH, EGRUE }; #define E_BLUE 3 #define E_BLUE 3 Code size vs Data Types May solidow these rules of thurns. Use char (8-bit or byte) as the default, everywhere; Use short or int (16-bit or word) for common arithmetic, counters and to hold ADC conversion results on advanced cores (with 10-bit or more internal ADC). Only as a as last resort, and only where absolutely necessary, use long (32-bit, userd) variables. Lote rule that also affects the size of produced code, though In a much smaller degree, is about sign. Lote unsigned data types wherever you can, and signed only when necessary.
Enumerated data types are an handy type of automatically defined constant series. The declaration assigns a value of zero to the first symbolic constant of the list, and the assigns subsequent values (automatically incremented) to the following constants. The user can, as well, arbitrarily assign numerical (signed) values at the beginning as well as in the middle of the series. Values following an explicit assignment use that value as a base and keep on incrementing from that point. The data type for an enum type or typedef variable is, as per ANSI definition, the smaller type that can contain the absolute maximum value of the constant series. enum STypes { E_NONE-0, E_RED; E_GREDN, E_SLUE }; // stame as : // #define E_RED 1 // #define E_RED 1 // #define E_RED 2 // #define E_RED 1 // #define E_GRED 2 // #define E_GRUE 3 // #define E_GRUE 3 // stame to always use the smallest data types possible. The rule is simple: the bigger data types are used, the bigger code will be generated. Thus, always follow these rules of thumb: . Use char (8-bit or byte) as the default, everywhere; . Use short or int (16-bit or word) for common arithmetic, counters and to hold ADC conversion results on advanced cores (with 10-bit or more internal ADC). . Only as a as last resort, and only where absolutely necessary, use long (32-bit, dword) variables. Another rule that also affects the size of produced code, though in a much smaller degree, is about sign. Use unsigned data types wherever you can, and signed only when necessary. Unsigned math <u>always generates smaller (and typically faster) code</u> than signed.
The declaration assigns a value of zero to the first symbolic constant of the list, and the assigns subsequent values (automatically incremented) to the following constants. The user can, as well, arbitrarily assign numerical (signed) values at the beginning as well as in the milddle of the series. Values following an explicit assignment use that value as a base and keep on incrementing from that point. The data type for an enum type or typedef variable is, as per ANSI definition, the smaller type that can contain the absolute maximum value of the constant series. enum ETypes { ENONE-0, EARD, EARED, EARED, EARED, EARED, EARED, #define ERDD 1 #define ERDD 1 #define ERDD 2 #define ERDD 3 #define ERDD 3 #define ERDD 3 #define ERDD 4 #define ERDD 4
<ul> <li>and the assigns subsequent values (automatically incremented) to the following constants.</li> <li>The user can, as well, arbitrarily assign numerical (signed) values at the beginning as well as in the middle of the series. Values following an explicit assignment use that value as a base and keep on incrementing from that point.</li> <li>The data type for an enum type or typedef variable is, as per ANSI definition, the smaller type that can contain the absolute maximum value of the constant series.</li> <li>enum ETypes { ENONE-O, EAED; EGREDI, E.BLUE };</li> <li>Same as : #define E_RED 1 #d</li></ul>
<ul> <li>The user can, as well, arbitrarily assign numerical (signed) values at the beginning as well as in the middle of the series. Values following an explicit assignment use that value as a base and keep on incrementing from that point.</li> <li>The data type for an enum type or typedef variable is, as per ANSI definition, the smaller type that can contain the absolute maximum value of the constant series.</li> <li>enum ETypes { ENONE-O, EARD; EGREDN, EBLUE };</li> <li>// same as :</li> <li>// sdefine E_REDD 1</li> <li>// sdefin</li></ul>
<ul> <li>as well as in the milddle of the series. Values following an explicit assignment use that value as a base and keep on incrementing from that point.</li> <li>The data type for an enum type or typedef variable is, as per ANSI definition, the smaller type that can contain the absolute maximum value of the constant series.</li> <li>enum FTypes { ENONE-O, EARD, E.GREEN, E.BLUE };</li> <li>// same as :</li> <li>// #define E.RLUE 0</li> <li>// #define E.RLUE 1</li> <li>// #define E.RLUE 2</li> <li>// #define E.RLUE 3</li> </ul> Code size vs Data Types Be sure to always use the smallest data types possible. The rule is simple: the bigger data types are used, the bigger code will be generated. Thus, always follow these rules of thumb: <ul> <li>Use char (8-bit or byte) as the default, everywhere;</li> <li>Use short or int (16-bit or word) for common arithmetic, counters and to hold ADC conversion results on advanced cores (with 10-bit or more internal ADC). <ul> <li>Only as a sa last resort, and only where absolutely necessary, use long (32-bit, dword) variables.</li> </ul> Another rule that also affects the size of produced code, though In a much smaller degree, is about sign. Use unsigned data types wherever you can, and signed only when necessary. Unsigned math always generates smaller (and typically faster) code than signed.</li></ul>
The data type for an enum type or typedef variable is, as per ANSI definition, the smaller type that can contain the absolute maximum value of the constant series. enum ETypes { ENONE-O, EARD; EGRED, E.BLUE }; // sidefine E_RDD 1 // side
<ul> <li>smaller type that can contain the absolute maximum value of the constant series.</li> <li>enum ETypes ( E.NONE-O, E.RED; E.GREEN, E.BLUE );</li> <li>#define E.BLOB 0</li> <li>#define E.BLOB 1</li> <li>#define E.BLUE 1</li> </ul> Code size vs Data Types Be sure to always use the smallest data types possible. The rule is simple: the bigger data types are used, the bigger code will be generated. Thus, always follow these rules of thumb: <ul> <li>Use char (8-bit or byte) as the default, everywhere;</li> <li>Use short or int (16-bit or word) for common arithmetic, counters and to hold ADC conversion results on advanced cores (with 10-bit or more internal ADC). <ul> <li>Only as a as last resort, and only where absolutely necessary, use long (32-bit, dword) variables.</li> </ul> Another rule that also affects the size of produced code, though in a much smaller degree, is about sign. Use unsigned data types wherever you can, and signed only when necessary. Unsigned math always generates smaller (and typically faster) code than signed.</li></ul>
<ul> <li>cnum FTypes { ENONE-0, E.RED; E.GREDI, E.BLUE };</li> <li>// Same as :</li> <li>// #define E.RED 1</li> <li>// #define E.RED 2</li> <li>// #define E.RED 2</li> <li>// #define E.BLUE 3</li> </ul> <b>Code size vs Data Types</b> Be sure to always use the smallest data types possible. The rule is simple: the bigger data types are used, the bigger code will be generated. Thus, always follow these rules of thumb: <ul> <li>Use char (8-bit or byte) as the default, everywhere;</li> <li>Use short or int (16-bit or word) for common arithmetic, counters and to hold ADC conversion results on advanced cores (with 10-bit or more internal ADC). <ul> <li>Only as a as last resort, and only where absolutely necessary, use long (32-bit, dword) variables.</li> </ul> Use unsigned data types wherever you can, and signed only when necessary. Unsigned math <u>always generates smaller (and typically faster) code</u> than signed.</li></ul>
<ul> <li>// #define E_ROME 0</li> <li>// #define E_REP 1</li> <li>// #define E_BLUE 1</li> <li>Code size vs Data Types</li> <li>Be sure to always use the smallest data types possible. The rule is simple: the bigger data types are used, the bigger code will be generated. Thus, always follow these rules of thumb:</li> <li>Use char (8-bit or byte) as the default, everywhere;</li> <li>Use short or int (16-bit or word) for common arithmetic, counters and to hold ADC conversion results on advanced cores (with 10-bit or more internal ADC).</li> <li>Only as a as last resort, and only where absolutely necessary, use long (32-bit, dword) variables.</li> <li>Another rule that also affects the size of produced code, though In a much smaller degree, is about sign.</li> <li>Use unsigned data types wherever you can, and signed only when necessary. Unsigned math always generates smaller (and typically faster) code than signed.</li> </ul>
<ul> <li>// #define E_CRED 1</li> <li>// #define E_CRED 2</li> <li>// #define 2</li> <li>// #define E_CRED 2</li> <li>// #define E_CRED 2</li> <li>// #define E_CRED 2</li> <li>// #define E_CRED 2</li> <li>// #define 2</li> <li>// #define E_CRED 2</li> <li>// #define E_CRED 2</li> <li>// #define 2</li></ul>
<ul> <li>// édefine EBUE 3</li> <li>Code size vs Data Types</li> <li>Be sure to always use the smallest data types possible. The rule is simple: the bigger data types are used, the bigger code will be generated. Thus, always follow these rules of thumb: <ul> <li>Use char (8-bit or byte) as the default, everywhere;</li> <li>Use short or int (16-bit or word) for common arithmetic, counters and to hold ADC conversion results on advanced cores (with 10-bit or more internal ADC).</li> <li>Only as a as last resort, and only where absolutely necessary, use long (32-bit, dword) variables.</li> </ul> </li> <li>Another rule that also affects the size of produced code, though in a much smaller degree, is about sign.</li> <li>Use unsigned data types wherever you can, and signed only when necessary, Unsigned math always generates smaller (and typically faster) code than signed.</li> </ul>
Be sure to always use the smallest data types possible. The rule is simple: the bigger data types are used, the bigger code will be generated. Thus, always follow these rules of thumb: Use char (8-bit or <b>byte</b> ) as the default, everywhere; Use short or int (16-bit or word) for common arithmetic, counters and to hold ADC conversion results on advanced cores (with 10-bit or more internal ADC). Only as a as last resort, and only where absolutely necessary, use long (32-bit, <b>dword</b> ) variables. Another rule that also affects the size of produced code, though in a much smaller degree, is about sign. Use <b>unsigned data types</b> wherever you can, and signed only when necessary. Unsigned math <u>always generates smaller (and typically faster) code</u> than signed.
Be sure to always use the smallest data types possible. The rule is simple: the bigger data types are used, the bigger code will be generated. Thus, always follow these rules of thumb: Use char (8-bit or <b>byte</b> ) as the default, everywhere; Use short or int (16-bit or word) for common arithmetic, counters and to hold ADC conversion results on advanced cores (with 10-bit or more internal ADC). Only as a as last resort, and only where absolutely necessary, use long (32-bit, <b>dword</b> ) variables. Another rule that also affects the size of produced code, though in a much smaller degree, is about sign. Use <b>unsigned data types</b> wherever you can, and signed only when necessary. Unsigned math <u>always generates smaller (and typically faster) code</u> than signed.
<ul> <li>Thus, always follow these rules of thumb:</li> <li>Use char (8-bit or byte) as the default, everywhere;</li> <li>Use short or int (16-bit or word) for common arithmetic, counters and to hold ADC conversion results on advanced cores (with 10-bit or more internal ADC).</li> <li>Only as a as last resort, and only where absolutely necessary, use long (32-bit, dword) variables.</li> <li>Another rule that also affects the size of produced code, though in a much smaller degree, is about sign.</li> <li>Use unsigned data types wherever you can, and signed only when necessary. Unsigned math always generates smaller (and typically faster) code than signed.</li> </ul>
<ul> <li>Use char (8-bit or byte) as the default, everywhere;</li> <li>Use short or int (16-bit or word) for common arithmetic, counters and to hold ADC conversion results on advanced cores (with 10-bit or more internal ADC).</li> <li>Only as a as last resort, and only where absolutely necessary, use long (32-bit, dword) variables.</li> <li>Another rule that also affects the size of produced code, though in a much smaller degree, is about sign.</li> <li>Use unsigned data types wherever you can, and signed only when necessary. Unsigned math always generates smaller (and typically faster) code than signed.</li> </ul>
<ul> <li>Use short or int (16-bit or word) for common arithmetic, counters and to hold ADC conversion results on advanced cores (with 10-bit or more internal ADC).</li> <li>Only as a as last resort, and only where absolutely necessary, use long (32-bit, dword) variables.</li> <li>Another rule that also affects the size of produced code, though in a much smaller degree, is about sign.</li> <li>Use unsigned data types wherever you can, and signed only when necessary. Unsigned math <u>always generates smaller (and typically faster) code</u> than signed.</li> </ul>
ADC conversion results on advanced cores (with 10-bit or more internal ADC). • Only as a as last resort, and only where absolutely necessary, use long (32-bit, <b>dword</b> ) variables. Another rule that also affects the size of produced code, though in a much smaller degree, is about sign. Use <b>unsigned data types</b> wherever you can, and signed only when necessary. Unsigned math <u>always generates smaller (and typically faster) code</u> than signed.
dword) variables. Another rule that also affects the size of produced code, though in a much smaller degree, is about sign. Use <b>unsigned data types</b> wherever you can, and signed only when necessary. Unsigned math <u>always generates smaller (and typically faster) code</u> than signed.
Another rule that also affects the size of produced code, though in a much smaller degree, is about sign. Use <b>unsigned data types</b> wherever you can, and signed only when necessary. Unsigned math <u>always generates smaller (and typically faster) code</u> than signed.
Use <b>unsigned data types</b> wherever you can, and signed only when necessary. Unsigned math <u>always generates smaller (and typically faster) code</u> than signed.
Unsigned math <u>always generates smaller (and typically faster) code</u> than signed.
BoostC <sup>Te</sup> Manual SourceBoost Technologies Page 41
Strings or arrays of data can be placed into program memory. Such row variables are declared using regular data types and rom storage specifier. Such rom variables must be initialized within declaration: rom char "text = "Text string". // text with trailing Zero rom char "text = "Text string". // text with trailing Zero rom char "text = "Text string". // text with trailing Zero rom char "text = "Text string". // text with trailing Zero rom char "text = "Text string". // text with trailing Zero rom char "text = "Longel Line". // text with trailing Zero rom char "text = "Text string". // text with trailing Zero rom char "text = "Longel Line". // text with trailing Zero rom char string". // text with trailing Zero rom can be used with char data types only; • there is no implicit cast between rom and regular data types. Though BoostC will not generate an explicit error for such a cast, it is expected that the operand should be casted back to its original type. If this is not done, the resulting code will behave unpredictably. • a rom pointer is internally limited to 8-bits: the constant array size is thus ilmited to 256 elements. This is coherent with smaller cores constraints; • access to rom elements has to be done exclusively through the [] operators and they cannot be referenced with substring pointer initialized at runtime. Please keep in mind that rom variables <u>must always and exclusively</u> be lolkalized within declaration; Example of wrong referencing with a runtime initialized pointer: * for the fellowing code is woong and will <u>Ext</u> because BoostC cannot dynamically create the pointer to mystriofFSET] when the syster array is located in ROM. * momos: a rom pointer must be initialized in declaration */
<pre>rom char *substr; substr = &amp;aystr[OFFSET]; //** WRONG ** cc = substr[0]; //** WRONG ** cc = mystr[OFFSET]; // Carrect</pre>
Volatile
The volatile type specifier should be used with variables that: a) Can be changed outside the normal program flow, and
<ul> <li>a) Can be changed outside the normal program now, and</li> <li>b) Should not receive intermediate values within expressions.</li> </ul>
For example, if a bit variable is mapped to a port pin, it is a good programming
practice to declare such variable as volatile.
Code generated for expressions with volatile variables is a little longer when compared to 'regular' code:

Strings as function arguments If a function has one or more char* arguments, it can be can string passed as an argument. Examples: X = 10; C = X - ; // Post-decrement. // After the operation x = 9, c. = 10 X = 10; C = -x; // After the operation x = 9, c. = 10 X = 10; C = -x; // After the operation x = 9, c. = 10 Signment = += -= *= /= %= &=  = ^= <<<>>= Signment Operator Examples If an operator multiplication will be stored in a 16-bit long variable, the will be then divided by b. This 16-bit long results of the multiplication will be stored in a 16-bit long result of the wariable on the laft side of the equal; is assigned, to the variable on the laft side of the equal; Strings as function arguments If a function has one or more char* arguments, it can be can string passed as an argument. The compiler will reuse the same RAM memory allocated for su several similar calls are made within same code block. For example, the code below will use the same memory block the strings "Date" and "Time": **********************************	uch arguments when to temporarily store by default to 16 bit ables g (word) temporary Jt will eventually be
ssignment       If an operation result is not explicitly casted, it is promoted precision. For example, given the following expression:         ssignment Operator Examples       long 1 = a * 100 / b; ///a * and /b' are 16 bit long varia         ' = is the ASSIGN operator. The value of the variable or expression on the right side of the equal is assigned. To he variable on the variable of the equal is assigned.       This behavior can be changed using the -Op compiler commando to the variable of the equal is assigned.	ables g <b>(word</b> ) temporary ult will eventually be
<pre>/ Examples: x = 3; // whatever was in the variable x // has been replaced with 3; x = 2; 4; // whatever was in the variable x // has been replaced with 5; x = 2; 4; // whatever was in the variable x // has been replaced with 6; c = x + y; // if x has a value of 12 and y has a value of 16; // whatever was in the variable c will // be replaced with 28; c = x + y; // if x has a value of 12 and y has a value of 16; // whatever was in the variable c will // be replaced with 28; c = x + y; // if x has an initial value of i4. After // the operator. x will be 16; x + 2; // if x has an initial value of 14. After // the operator x will be 16; // the value 12 and y has the value 16; // After the operation c will be 38; // Examples; x + 2; // if c has an initial value of 10 and x has // the value 12 and y has the value 16; // After the operation c will be 38; // Examples; x + 2; // if c has an initial value of 10 and x has // the value 12 and y has the value 16; // After the operation c will be 38; // Examples; x + 2; // if c has an initial value of 10 and x has // the value 12 and y has the value 16; // After the operation c will be 38; // Examples; // examples; // examples; // example; // or // example; // or</pre>	ie, that will then be be stored in i.
Is the combined subtract and asster operator. The variable     Image: C = S + 7; Image: C = S	Page <b>45</b>
<pre>or supression on the right side of the operator will be performed from the operating with side of the second spectrum. Second from the operating with the transfer operator. * - 2: // if a has an initial value of 10 and x has // After the operation x all be 200 // After the operation x all be 200 // Second from the initial value of 10 and x has // Second from the initial value of 10 and x has // Second from the initial value of 10 and x has // Second from the initial value of 10 and x has // Second from the initial value of 10 and x has // Second from the initial value of 10 and x has // Second from the initial value of 10 and x has // Second from the initial value of 10 and x has // Second from the initial value of 10 and x has // Second from the initial value of 10 and x has // Second from the operation x all be 200 // Second from the initial value of 10 and x has // Second from the initial value of 10 and x has // Second from the operation x all be 200 // Second from the ope</pre>	second. uce
: 48 SourceBoost Technologies BoostC <sup>m</sup> Manual Page 46 SourceBoost Technologies B	BoostC'n Manual

s GREATER than another operand.	<pre>// Comparison // Comparis</pre>
s a binary operator, it is used to see if one operand s GREATER than another operand. el:	x <∽ y: // If x has an initial value of 14 and y has the // value 2. After the operation x will be 56.
e Clear_bit( PORTA, LED_bit ); // Turn LED OFF Is example the LED will be turned OM.	<pre>// on the left side of the operator will be shifted left by the number of places indicated by the variable or constant on the right. The result is then assigned to the variable on the left side of the operator. // Examples:</pre>
stC™ Manual SourceBoost Technologies Page 51	BoostC <sup>™</sup> Manual SourceBoost Technologies Page 49
<pre>ff(x - y) // If x has a value of 46 and y has a value of 46 set_bit( PORTA, LED_bit ); // Turn LED ON</pre>	// <<= is the combined SHIFT-LEFT and ASSIGN operator. The variable
<pre>if( x ~ y ) // If x has a value of 22 and y has a value of 33. set_bit( PORTA, LED_bit ); // Turn LED OFF clear_bit( PORTA, LED_bit ); // Turn LED OFF this example the LED will be turned GN. ample2: if( x ~ y ) // If x has a value of 15 and y has a value of 8. set_bit( PORTA, LED_bit ); // Turn LED OFF clear_bit( PORTA, LED_bit ); // Turn LED OFF this example the LED will be turned OFF. ample3;</pre>	<pre>// A= is the combined structs:xon and assign operator. The variable on the left side of the operator will be xoked on a bit by bit basis with the variable or constant on the right side. The result is then assigned to the variable on the left side of the operator. // Examples: x 'm y; // If x has an initial value of 14 and y has the x 'm y; // value 5. After the operation x will be 11. c 'm 0x07: // If c has a value of 0x00; After the operation c will be 0x09; y 'm 0billio00; // If y has an initial value of 0b0001110. // If y has an initial value of 0b0001110. // Jf y has an initial value of 0b0001110. // Jf y has an initial value of 0b0001110. // Jf y has an initial value of 0b0001110. // be obl110010.</pre>
is a binary operator. It is used to see if one operand is LESS than or EQUAL to another operand. (ample):	y (= 051110000; // If y has an initial value of 0610001110; // fifer the operation y aff) // be 06111110;
ample2: if(x < y) // If c has an initial value of 0, and // x has the value of and y thas the value 40. c = y - x; // The final value for c will be 0. }	<pre>// Examples: x i= y: // If x has an initial value of 14 and y has the y value 5. After the operation x will be 15. c i= 0x07; // if c has an initial value of 0x06. After the operation c will be 0x0p.</pre>
cample1: if( $x < y$ ) // If c has an initial value of 0, and $\begin{cases} y - x; \\ x + final value for c will be 25, \\ \end{bmatrix}$	// [= is the combined BITWISE-OR and ASSIGN operator. The variable on the left side of the operator will be OREd on a bit-by-bit basis with the variable or constant on the right side. The result is then assigned to the variable on the left side of the operator.
is à bi∩ary operator. It is used to see if one operand is LESS than another operand.	<pre>// the operation c will be 0x00; y &amp;= 0b1110001; // If y has an initial value of 0b10001111. // After the operation y will // be 0b10000001.</pre>
$ \begin{array}{l} f(\mathbf{x} \mid = \mathbf{y}) & \text{if } c \text{ has an initial value of } 0, \text{ and} \\ &  &  &      $	<pre>// Examples: x &amp;= y; // If x has an initial value of 14 and y has the // value 5. After the operation x will be 4 c &amp;= 0x07; // If c has an initial value of 0x0c. After // the operation c will be 0x00c. After</pre>
cample2: if(x = y) // If c has an initial value of 0, and	
	is the combined STIMISE-AND and ASSIGN operator. The variable on the left side of the operator will be ANDed on a bit-by-bit basis with the variable or constant on the right side. The result is then assigned to the variable on the left side of the operator.

is a binary operator. It is used to produce the logical sum of two operands. The individual bits of two operands are ored together to produce the final results.	Logical &&    !	
nples: • x   y; // If x has a value of 14 and y has a value // of S. After the operation c will be 15.		
<pre>// of 5. After the operation c will be 15. = y   0x07; // If y has a value of 0x0E. After the // operation x will be 0x0F.</pre>	Logical Operator Examples	ed to determinabilit both operands
= v / 0b11110000: // If v bas a value of 0b10001110	// are true. The operator, are exp // or false.	ressions that evaluate to true /
<pre>// After the operation x will // be oblighting.</pre>	// Example1: if( (temp> 50-) && (temp < 100 clear_bit( PORTA, ALANGLbit );	•
is a binary operator. It is used to produce the logical difference of two operands, the individual bits of two operands are XORed together to produce the final results.	clear_bit( PORTA, ALANG(bit ); else set_bit( PORTA, ALANG(bit );//	
mples:	<pre>// If temp has a value of 70 the alarm // Eventlab</pre>	will be turned OFF.
<pre>:= y A 0x07; // If y has a value of 0x0E. After the // operation x will be 0x09.</pre>	<pre>// Example2: if( ( temp &gt; 50 ) &amp;&amp; ( temp &lt; 100</pre>	)),,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,
: - y A 0b11111000; // If y has a value of 0b00011110. // After the operation x will be 0b1100110.	else set.bit( PORTA, ALARALDIT ); //	
is a whary operator. It is used to produce the complement of an	<pre>// If temp has a value of 105 the alara // Example3:</pre>	will be turned ON.
operand. The individual bits of the operand are complemented. The ones become zeros and the zeros become ones.	if( ( temp > 50 ) && ( temp < 100 clear_bit( PORTA, ALARM_Bit ); alse	)) //.Turn alarm OFF
mples: <	Set_bit( PORTA, ALARALbit ); //	Turn alarm on
// operation x m 11 be Oxfl. = -0b01010111; // After the operation x will // be 0b101000.	// If temp has a value of 25 the alarm	na an a
	// [] is a binary operator. It is u operand is true. The operands to true or false.	sed to determine if sither are expressions that evaluate
S a binary operator. The operand on the left side of the operator will be shifted left by the number of places indicated by the operand on the right.	//.Examplel:	
mpTes:	if( (volt > 7.)    ( volt > 5 ) set_bit( PORTA, LED_bit ); else	医小心神经病毒 动物植物 网络海门加加 人名法布尔人
<pre>r = X &lt;&lt; y; // If x has a value of 14 and y has a value // of 2. After the operation c will be 56.</pre>	clear_bit( PORTA, LED_bit ); // // If volt has a value of 8 the LED wi	그는 가슴 전에서 물건을 가지 않는 것이 같아. 이 것이 많이
<pre>c = y &lt;&lt; 0x01; // If y has a value of 0x0E. After the // operation x will be 0x1C.</pre>	// Ekample2:	
t = y << 060000010; // If y has a value of 050001110. // After the operation x will // be 090111000.	if( (volt > 7 ).    ( volt < 5 ) set_bit( PORTA, LED_bit ); else	
. is a binary operator. The operand on the left side of the	cTear_bit('PORTA, LED_bit ); // // If volt has a value of 6 the LED wi	
aperator will be shifted right by the number of places	BoostC <sup>m</sup> Manual SourceBoost 1	echnologies Page 53
	// Example3:	// Turn CED ON
aperator will be shifted right by the number of places indicated by the operand on the right. es: x > y: // If x bas a value of 14 and y bas a value of 2.	<pre>// Example3: if( C voit &gt; 7.)    ( voit &lt; 5.) )</pre>	// Terri CED on In LED OF Re turned on
<pre>operator will be shifted right by the number of places indicated by the operand on the right. es; x &gt;&gt; y: // If x has a value of 14 and y has a value of 2. After the operation c will be 3. y &gt;&gt; OnO1; // If y has a value of 0:00E. After the operation // x will be 0:007</pre>	<pre>// Example3:</pre>	// Terr LED ON in LED OFF Ne turned ON. complement an evaluated
<pre>aperator will be shifted right by the number of places - indicated by the operand on the right. es; x&gt;&gt; y; // If x has a value of 14 and y has a value of 2. After the operation c will be 3.</pre>	<pre>// Example3: ff( ( volt &gt; 7.)    ( volt &lt; 5.) )</pre>	// Term CED ON min LED OFF Ne turned ON. complement an evaluated fon that evaluates to
<pre>operator will be shifted right by the number of places indicated by the operand on the right. es; x &gt;&gt; y: // If x has a value of 14 and y has a value of 2. After the operation c will be 3. y &gt;&gt; OnO1; // If y has a value of 0:00E. After the operation // x will be 0:007</pre>	<pre>// Example3: ff( ( volt &gt; 7.)    ( volt &lt; 5.) )</pre>	// Term CED ON min LED OFF Ne turned ON. complement an evaluated fon that evaluates to
<pre>aperator will be shifted right by the number of places indicated by the operand on the right. es; x &gt;&gt; y: // If x has a value of 14 and y has a value of 2. After the operation c will be 3. y &gt;&gt; OnO1: // If y has a value of 0:00E. After the operation // x will be 0:00. y &gt;&gt; 0b00000000; // If y has a value of 0:00001110. After the operation x will // the 0:00000111.</pre>	<pre>// Example3: ff( ( volt &gt; 7.)    ( volt &lt; 5.) ). set_bit( PORTA, LED_bit ); clear_bit( PORTA, LED_bit ); // Ti clear_bit( PORTA, LED_bit ); // Ti // If volt has a value of 4 the LED.will ) // If volt has a value of 4 the LED.will ) // If sa unary operator. It is used to operand. The operand is an express true or Talse // Example1: true or Talse // Example1: if ( pressure &gt; 120.)</pre>	// Turn (ED ON in LED OFF a turned ON. complement an evaluated ion that evaluates to ion that evaluates to // Turn alarm OFF in alars ON
<pre>aperator will be shifted right by the number of places indicated by the operand on the right. es; x &gt;&gt; y; // If x has a value of 14 and y has a value of 2. After the operation c will be 3. y &gt;&gt; 0x00; // If y has a value of 0x00; After the operation y &gt;&gt; 0x0000000; // If y has a value of 0x00; After the operation y &gt;&gt; 0x00000000; // If y has a value of 0x00000000000; // X will be 0x07 y &gt;&gt; 0x00000000; // If y has a value of 0x00000000000; // Sher the operation x will // be 0b000000111; // be 0b000000111;</pre>	<pre>// Example3:</pre>	// Turn (ED ON nin, LED OFF. he turned ON. ; complement an evaluated ; ion that evaluates to ; // Turn alarm OFF urn alarm OFF a will be turned OFF.
<pre>operator will be shifted right by the number of places indicated by the operand on the right. es; x &gt;&gt; y! // If x has a value of 14 and y has a value of 2. After the operation c will be 3. y &gt; 0x01; // x will be 0x07 y &gt;&gt; 0b00000010; // if y has a value of 0b0001110. After the operation x will // the ob00000111. mais</pre>	<pre>// Example3:</pre>	// Turn (ED ON im (ED OF be turned ON. complement an evaluated iton that evaluates to / // Turn alarm OFF im alarm ON a will be turned OFF.
<pre>aperator will be shifted right by the number of places indicated by the operand on the right. es; x&gt;&gt; y; // If x has a value of 14 and y has a value of 2. After the operation c will be 3. y&gt; 0x01; // If y has a value of 0x0E. After the operation y&gt; 0x002; // If y has a value of 0x0E. After the operation y &gt; 0x00000000; // If y has a value of 0x0E. After the operation // the operation x will // the operation x will // the operation x will // the obego000111.</pre>	<pre>// Example3:</pre>	// Turn (ED ON im (ED OF be turned ON. complement an evaluated iton that evaluates to / // Turn alarm OFF a will be surned OFF. // Turn alarm OFF urn alarm OFF
<pre>operator will be shifted right by the number of places indicated by the operand on the right. es; x &gt;&gt; y! // If x has a value of 14 and y has a value of 2. After the operation c will be 3. y &gt; 0x01; // If y has a value of 0x0E. After the operation y &gt; 0x0000000; // If y has a value of 0x0E. After the operation y &gt; 0x00000000; // If y has a value of 0x00001110; After the operation x will // be 0b00000111. nais else statement tch statement tch statement tch statement ternary operator</pre>	<pre>// Example3:</pre>	// Turn (ED ON im (ED OF be turned ON. complement an evaluated iton that evaluates to / // Turn alarm OFF un alarm OH a will be surned OFF.
<pre>aperator will be shifted right by the number of places indicated by the operand on the right. es; x&gt;&gt; y; // If x has a value of 14 and y has a value of 2. After the operation c will be 3. y &gt; 0x01; // If y has a value of 0x0E After the operation // x will be 0x07 y &gt; 0b0000000; // If y has a value of 0b0001110; After the operation x will // be 0b00000111; be 0b00000111; nais else statement th statement th statement the statement al Examples else is a two-way decision making statement. If the expression evaluates to true, the first statement will be executed. If if walluates to false the second statement will be executed.</pre>	<pre>// Example3:</pre>	// Turn (ED ON im (ED OF be turned ON. complement an evaluated iton that evaluates to / // Turn alarm OFF un alarm OH a will be surned OFF.
<pre>aperator will be shifted right by the number of places indicated by the operand on the right. es; x&gt;&gt; y; // If x has a value of 14 and y has a value of 2. After the operation c will be 3. y&gt; 0x01; // If y has a value of 0x0E. After the operation // x will be 0x07 y&gt; 0b0000000; // If y has a value of 0b0001110. After the operation x will // the obegoutient will // be 0b0000011. // be 0b00000011. // be 0b0000011. // be 0b0000011. // be 0b0000011. // be 0b00000011. // be 0b00000000000000000000000000000000</pre>	<pre>// Example3: ff( ( volt &gt; 7.)    ( volt &lt; 5.) ). set_bit( PORTA, LED_bit ): clear_bit( PORTA, LED_bit ): // Ti clear_bit( PORTA, LED_bit ): // Ti // If volt has a value of 4 the LED.will ) // If volt has a value of 4 the LED.will ) // If volt has a value of 4 the LED.will ) // If saunary operator. It is used to operand. The operand is an express true or false // Example1: if ( [ pressure &gt; 120 )) if clear_bit( PORTA, ALAMALDIt ); eset.bit( PORTA, ALAMALDIt ); // If pressure has a value of 75 the alam // Example2: if( [ pressure &gt; 120.) ) clse ar_bit( PORTA, ALAMALDIt ); else set.bit( PORTA, ALAMALDIt ); // If pressure has a value of 125 the alam // If pressure has a value of 125 the alam</pre>	// Turn (ED ON im (ED OF be turned ON. complement an evaluated iton that evaluates to / // Turn alarm OFF un alarm OH a will be surned OFF.
<pre>aperator will be shifted right by the number of places indicated by the operand on the right. es; x&gt;&gt; y; // If x has a value of 14 and y has a value of 2. After the operation c will be 3. y &gt; 0x01; // If y has a value of 0x0E After the operation // x will be 0x07 y &gt; 0b0000000; // If y has a value of 0b0001110; After the operation x will // be 0b00000111; be 0b00000111; nais else statement th statement th statement the statement al Examples else is a two-way decision making statement. If the expression evaluates to true, the first statement will be executed. If if walluates to false the second statement will be executed.</pre>	<pre>// Example3:</pre>	<pre>// Turn EED ON ifin LED OFF se turned ON Complement an evaluated ifon that evaluates to // Turn alarm OFF urn alarm ON A will be turned ON.</pre>
<pre>aperator will be shifted right by the number of places indicated by the operand on the right. es; x &gt;&gt; y: // IF x has a value of 14 and y has a value of 2. // After the operation C will be 3; y &gt;&gt; OrOl; // If y has a value of 0xOE after the operation // x will be 0xO7 y &gt;&gt; 0xO0000000; // IF y has a value of 0xOE of 0b0001110; // After the operation x will // the operation x will // be 0b00000111. // be 0b000000111. // be 0b00000111. // be 0b00000111. // be 0b00000111. // be 0b00000111. // be 0b000000111. // be 0b00000000000000000000000000000000</pre>	<pre>// Example3:</pre>	<pre>// Turn EED ON ifin LED OFF se turned ON Complement an evaluated ifon that evaluates to // Turn alarm OFF urn alarm ON A will be turned ON.</pre>
<pre>aperator will be shifted right by the number of places indicated by the operand on the right. es; x&gt;&gt; y; // If x has a value of 14 and y has a value of 2. After the operation c will be 3: y &gt; 0x01; // If y has a value of 0x0E After the operation // x will be 0x07 y &gt; 0b0000010; // If y has a value of 0b00011100 // After the operation x will // be 0b00000111; // be 0b000000000; // the statement the statement intermation and be the operation of a statement will be executed. If it wallutes to false the second statement will be executed. (el: x &gt; y ) /// If x has a value of 25 and y has a value of 10; et_bb1( PoxFA, LED_b12); // Turn LED OFF s example the LED will be turned GM. e2;</pre>	<pre>// Example3:</pre>	<pre>// Turn LED ON in LED OFF be turned ON. . complement an evaluated ion that evaluates to ion that evaluates to // Turn alarm OFF urn alarm OW a will be turned OFF. // Turn alarm OFF urn alarm OM in will be turned ON. in will be turned ON.</pre>
<pre>aperator will be shifted right by the number of places indicated by the operand on the right. es; x &gt;&gt; y: // If x has a value of 14 and y has a value of 2. After the operation cwill be 3. y &gt;&gt; 0x00; // If y has a value of 0x0E. After the operation y &gt;&gt; 0x0000000; // If y has a value of 0x0E. After the operation // x will be 0x07 y &gt;&gt; 0x00000000; // If y has a value of 0b00011110 After the operation 'x will // be 0b00000011. // be 0b00000011. // be 0b00000011. // be 0b00000111. // be 0b00000011. // the secure of 25 and y has a value of 10. /*Cubit( POKTA, LED_bit ): // Turn LED OFF s example the LED will be turned OM. e2: x &gt; y ) /// If x has a value of 8 and y has a value of 15. e2: x &gt; y ) /// If x has a value of 8 and y has a value of 15. e2: x &gt; y ) /// If x has a value of 8 and y has a value of 15. e2: x &gt; y ) /// If x has a value of 8 and y has a value of 15. e2: x &gt; y ) /// If x has a value of 8 and y has a value of 15. e2: x &gt; y ) /// If x has a value of 8 and y has a value of 15. e2: x &gt; y ) /// If x has a value of 8 and y has a value of 15. e2: her beft( POKTA, LED_bit ): // Turn LED OW</pre>	<pre>// Example3: ff( ( volt &gt; 7.)    ( volt &lt; 5.) ). set_bit( PORTA, LED_Bit ): clear_bit( PORTA, LED_Bit ): // T // If volt has a value of 4 the LED.will ) // If volt has a value of 4 the LED.will ) // If volt has a value of 4 the LED.will ) // If volt has a value of 3 an express // true or false // ( pressure &gt; 120 )) ( ( pressure &gt; 120 )) clear_bit( PORTA, ALARALDIT ); else estable ( PORTA, ALARALDI</pre>	<pre>// Turn (ED ON in LED OFF se turned ON. . complement an evaluated in that evaluates to in that evaluates to in alarm OFF in alarm ON a will be turned OFF. .// Turn alarm OFF un alarm ON ne will be turned ON. </pre>
<pre>querator will be shifted right by the number of places indicated by the operand on the right. ss; x &gt;&gt; y; // If x has a value of 14 and y has a value of 2. // After the operation C will be 3; y &gt;&gt; 0x01; // If y has a value of 0x0E. After the operation // x will be 0x02 y &gt;&gt; 0b0000000; // If y has a value of 0b0011100 // free the operation x will // be 0b0000011. // be 0b00000011. // be 0b0000011. // be 0b0000011. // be 0b0000011. // be 0b00000011. // be 0b00000011. // be 0b00000011. // be 0b00000011. // be 0b00000011. // be 0b00000000000000000000000000000000</pre>	<pre>// Example3:</pre>	<pre>// Turn LED ON in LED OFF be turned ON. complement an evaluated ifon that evaluates to // Turn alarm OFF urn alarm OH a will be turned OFF. // Turn alarm OHF urn alarm OH m will be turned OFF. // Turn alarm OHF urn alarm OH for will be turned ON.</pre>
<pre>aperator will be shifted right by the number of places indicated by the operand on the right. es; x &gt;&gt; y: // If x has a value of 14 and y has a value of 2. After the operation cwill be 3. y &gt;&gt; 0x00; // If y has a value of 0x0E. After the operation y &gt;&gt; 0x0000000; // If y has a value of 0x0E. After the operation // x will be 0x07 y &gt;&gt; 0x00000000; // If y has a value of 0b00011110 After the operation 'x will // be 0b00000011. // be 0b00000011. // be 0b00000011. // be 0b00000111. // be 0b00000011. // the secure of 25 and y has a value of 10. /*Cubit( POKTA, LED_bit ): // Turn LED OFF s example the LED will be turned OM. e2: x &gt; y ) /// If x has a value of 8 and y has a value of 15. e2: x &gt; y ) /// If x has a value of 8 and y has a value of 15. e2: x &gt; y ) /// If x has a value of 8 and y has a value of 15. e2: x &gt; y ) /// If x has a value of 8 and y has a value of 15. e2: x &gt; y ) /// If x has a value of 8 and y has a value of 15. e2: x &gt; y ) /// If x has a value of 8 and y has a value of 15. e2: x &gt; y ) /// If x has a value of 8 and y has a value of 15. e2: her beft( POKTA, LED_bit ): // Turn LED OW</pre>	<pre>// Example3:</pre>	<pre>// Turn LED ON in LED OFF be turned ON. complement an evaluated ifon that evaluates to // Turn alarm OFF urn alarm OH a will be turned OFF. // Turn alarm OHF urn alarm OH m will be turned OFF. // Turn alarm OHF urn alarm OH for will be turned ON.</pre>
<pre>upperator will be shifted right by the number of places indicated by the operand on the right. es; x&gt;&gt; y: // If x has a value of 14 and y has a value of 2. // After the operation c will be 3: y&gt;&gt; 0x01; // If y has a value of 0x0E. After the operation // x will he 0x07 y&gt;&gt; 0b00000010; // If y has a value of 0b0001110 // After the operation x will // be 0b00000111. // be 0b000000111. // be 0b000000111. // be 0b000000111. // be 0b000000111. // be 0b000000111. // be 0b000000111. // fr x has a value of 25 and y has a value of 10. elbit (rokra, LED_bit 2): // Turn LED OFF s example the LED will be turned OM. e2: // X y j // If x has a value of 8 and y has a value of 15: elbit(rokra, LED_bit 2): // Turn LED OFF s example the LED will be turned OFF. // turn LED OFF s example the LED will be turned OFF. // is a multi-way decision paking statement. The variable is</pre>	<pre>// Example3:</pre>	<pre>// Turn LED ON // Turn LED ON // Turn alarn OF // Tu</pre>

continue the execution.	switch(weight.)
Example1: do { factorial *= number; // 'factorial' is initialized to 1	<pre>case 5: set_bit( FORTA, red_LED ); // Turn red,LED.ON clear_bit( FORTA, green_LED ); // Turn green LED OFF break; ( FORTA, green_LED ); // Turn green LED OFF</pre>
number; // before entering the loop. } while( number > 0 );	case 10; set_bit( PONTA, green_LED ); // Turn green LED ON clear_bit( PONTA; red_LED ); // Turn red_LED OFF break;
If 'number' has a value of 4 'factorial' will become 24. factorial = $4 \times 3 \times 2 \times 1$ ; Example2:	default: clear_bit( PORTA, green LED ); // Turm green LED OFF clear_bit( PORTA, red_LED ); // Turm red LED OFF
do { factorial *= number; // 'factorial' is initialized to 1	<pre>// If the 'weight' variable has a value of 5 the red LED will // be turned ON and the green LED will be turned OFF. // Example2:</pre>
	, readprice. switch( weight ) Case 5:
If 'number' has a value of 0, 'factorist' will become 0 because the loop was entered, before the expression was evaluated.	set_bit( PORTA; (red_LED ); // Turn red LED ON clear_bit( PORTA, green JED ); // Turn green LED OFF break; case lo:
for is a loop control construct. It controls the number of times a block of statements is executed. The construct has an initial value, a final value, and a roop-count value that is incremented each time after the block is executed.	set_bit( PORTA, green_LED ); // Turn green_LED ON clear_bit( PORTA, red_LED ); // Turn red_LED OFF break; default;
<pre>Example1: for( volts = 0; volts &lt; 7; volts++ )</pre>	clear_bit( PORTA, green_LED ); // Turn green_LED OFF (clear_bit( PORTA, red_LED ); // Turn red_LED OFF
<pre>sum += volts: // 'sum' is initialized to 0. before entering the loop.</pre>	<pre>// If the 'weight' variable has a value of 10 the green LED will // be turned GN and the red LED will be turned OFF. // Example3:</pre>
Upon exiting the loop 'sum' will have a value of 21	switch( weight ) { case 5: 
break is an option that can be used to exit out of a for-loop, based upon the evaluation of an expression. Example1:	clear_bit( PORTA, green_LED ); // Turn green_LED OFF break; case 10;
<pre>forC volts = 0; volts &lt; 7; volts+; ) {     if( volts = 5 ) </pre>	<pre>clear_bit( PORTA, red_LED ); // Turn red LED OFF -</pre>
break; sum += volts; // 'sum' is initialized to 0 ]	<pre>clear_bit( PORTA, red_LED ); // Turn red LED DFF }</pre>
Upon exiting the loop 'sum' will have a value of only 20	<pre>// If the Weight variable has any value other than 5 or 10, // both the green and red LEDs will be turned OFF // 7 : is an if/else operator. This operator can be used inside an</pre>
continue is an option used to redirect a for loop based upon the evaluation of an expression. If the expression evaluates to true, the block of statements will not be executed. Example1:	// expression to determine if a part of it is true or false. // Example1: turn the LED ON turn the LED OFF.
oostC <sup>m</sup> Manual SourceBoost Technologies Page 59	BoostC <sup>m</sup> Manual SourceBoost Technologies Page 57
<pre>for( volts = 0; volts &lt; 7; volts++ ) if( volts = 5 ) continue; sum ++ volts; // sum .is initialized to 0 }</pre>	(volts > 5) ? set_bit(PORTA: LED_bit) : clear_bit(PORTA, LED_bit); // If 'volts' has a value of 7 turn the LED ON // Example7: turn the LED ON turn the LED OFF (volts > 5) ? set_bit(PORTA, LED_bit) : clear_bit(PORTA, LED_bit);
on exiting the loop 'sum' will have a value of only 16	// If volts' has a value of 3 turn the LED OFF.
e vast majority of programming books, the usage of 'goto' is heavily	Program Flow
cated. This is true for BoostC and PIC C coding as well: It should normally be ed.	while do/while for
: are, anyway, some very specific circumstances where it may still be useful: timize early exit cases within complex nested control structures or to simplify error handling (it can somehow minic try/catch exception handling syntax).	break continue
	Program Flow Examples
riec) ήlie() goto exit;	<pre>// while is a loop control construct. It controls the execution of // a block of statements for as long as an expression evaluates // to true the expression is availated first and if true, executes the block. If it evaluates to faise, stop the execution.</pre>
	<pre>// Example1; while( number &gt; 0 )</pre>
ne assembly the asm or _asm operators to embed assembly into C code.	factorial.*= number; // factorial'is initialized to 1
switching and code page switching code should NOT be added to inline why code. The linker will add the appropriate Bank switching and code page hing code.	<pre>// if 'number' has a value of 3 'factorial' will become 5. // factorial = 3 × 2 × 1 ; // Example2;</pre>
will be affected as follows: Bank switching added automatically.	<pre>while(number&gt; 0) factorial *= number; // 'factorial' is initialized to 1number; // 'before entering the loop;</pre>
Code page switching added automatically.	<pre>1 // If 'number' has a value of 0, 'factorial' will stay // equal to 1.becruise the loop was never entered.</pre>
will be affected as follows:	
Bank switching added automatically.	//
Bank switching added automatically. Code page switching added automatically. Other optimizations applied (including dead code removal).	// do / while is a loop control construct. It controls the execution of a block of statements for as long as an expression evaluates to true. The block is executed at least once before the expression is evaluated. If it evaluates to false, stop the execution. If it evaluates to true,

#### Variable Referencing in asm To refer to a C variable from inline assembly, simply prefix its name with an underscore '\_'. If a C variable name is used with the 'moviw' instruction, the address of this variable is copied into W. a heavy usage of inline functions obviously augments code size. cial functions Labels are identified with a trailing semicolon ':' after the label name. id main(void) Inline assembly example 1 // Example showing use of bit tests and labels in inline assembly #include <system.b> ogram entry point. This function is mandatory for every C program. void fooO id interrupt(void) unsigned char 1, b; 1 = 0; b = 12; asm { terrupt handler function. Is linked to high priority interrupts for PIC18 parts. rid interrupt\_low(void) w priority interrupt handler, can be used only on the PIC18 family. start: btfsc btfsc\_i.4 goto end btfss\_b,0 goto iter . Ale est eneral functions and interrupts iter: moviw 0 movwf\_b end: andard user functions are not thread-safe: their local variables are not saved hen function execution gets interrupted by an interrupt. This can lead to very ard to trace errors. prevent this pitfall, the linker does not allow to call a given function from both ain() and Interrupt threads. you really need to use same function in both threads, you need to duplicate its Inline assembly example 2 ide and assign a different name to the second copy. // Example for PIC18F8720 target showing how to access bytes of // integer arguments finclude <system ho int GetTurlValO int x; asa sour\_\_tarlh, w wovwr\_\_xx1 :: write to high byte of variable x wovwr\_\_xx1 :: write to low byte of variable x write to low byte of variable x Ť return x; 3 v statistick de T oostC™ Manual SourceBoost Technologies Page 63 BoostC™ Manual SourceBoost Technologies Page 61 s function gets called from main thread Inline assembly example 3 // Example of how to access structure members from inline assembly // Note: This code may not work as expected if the data structure // is modified causing member count2 to have a different offset. y of foo' that will be called from interrupt thread foo\_interrupt() struct Stats unsigned int count0; // stored in bytes 0 & 1 unsigned char count1;// stored in bytes 0 & 1 unsigned int count2; // stored in bytes 3 & 4 errupt thread interrupt( void ) **}**# struct Stats myStats; -interrupt(): void AddCount20 int x; ssm movf \_myStats+3, W addlw 0x01 movMr\_myStats+3 btfsc \_status, c incf \_myStats+4,F n thread sain( void ) 0 $\mathcal{J}_{1,2} = \mathcal{J}_{1,2}$ amic memory management User Data imic memory management is used to dynamically create and destroy objects User data can be placed at the current location using the 'data' assembly n time. a sample, this functionality may be needed when a program needs to keep rai data packets. Memory for this packets can also be allocated at compile , but this way the memory may not be available for other variables even if it's instruction followed by comma separated numbers or strings. Example: Example: // Code below will place bytes 10,11,116,101,115,116,0 // ar current code location asm data Oxi, Ox8, 'test' ised. solution is to use dynamic memory allocation. Objects to store data are ed as soon as they are needed and destroyed after data gets processed. way all available target data memory is used most efficiently. Functions amount of possible objects that can be allocated depends on the specific PIC at hand, and on the application. Inline functions n the application is built, the linker uses RAM memory left after allocation of Functions declared as inline are repeatedly embedded into the code for each occurrence. When a function is defined as inline, its body must be defined before it al and local variables as a heap. When some memory gets allocated at run by the 'alloc' call, it gets allocated from this heap. The bigger the heap, the gets called for the first time. a run time objects can exist at any given time. Though any function can be declared as inline, procedures (functions with no return value and a possibly empty argument list) are best suited to be used as inline. An exception to this rule are inline functions with reference arguments. Such functions will not overload variables passed as arguments but will operate directly an kang \* alloc(unsigned char size) imically allocate memory 'size' bytes long. Max size is 127 bytes. Returns . If memory can't be allocated. If memory can't be awocated. free(vold \*ptr) on them: inline void foo( char &port ) { port = 0xFF) // set all pins of a port BoostC™ Manual 64 SourceBoost Technologies BoostC<sup>IM</sup> Manual Page 62 SourceBoost Technologies

d foo( char a, c	ar b)	//ˈfoo' number	3
	2		
d main( void )			
po(); po( test" ); po( 10, 20 );	//'foo' i //'foo' i //'foo' i	number 1 gets cal number 2 gets cal number 3 gets cal	led led led

compiler will generate internal references to the functions so that no biguity is possible (name mangling), and will select which function will be oked for each call analyzing how many parameters are passed, as well as their €.

// foo' number 1

//'foo' number 2

#### nction templates

d foo( void )

d foo( char \*ptr )

actions can be declared and defined using data type placeholders. s feature allows writing very general code (for example, linked lists handling) it is not tied to a particular data type and, what may be more important, allows user to create template libraries contained in header files:

## plate <class T> d foo( T tt )

mlate-eclass D g foo(T tt ) d math( void ) hort s: oocchar>("test"); // foo( char\* )' gets called vocshort>( &s ); // foo( short\* )' gets called

**25 );** //'fop{ 

ostC™ Manual

SourceBoost Technologies

Page 67

RoostC™ Manual

unpredictable results.

SourceBoost Technologies

Free memory previously allocated by 'alloc'. Passing any other pointer will lead to

Page 65

#### netric timing functions

f software based timing functions are strictly dependent on clock speed. s parameter is usually well known at linking time, depending only on are design and implementation, such functions can be dynamically ited, once the clock frequency is correctly assigned with the CLOCK\_FREQ а.

functions can be used in the standard way when writing any program for

#### elay\_us( unsigned char t )

ated function) Delays execution for 't' micro seconds. Declared in boostc.h nction gets generated every time a project is linked and is controlled by the \_REQ pragma. In some cases when clock frequency is too low it's not ally possible to generate this function. If that's the case linker will issue a a.

#### elay\_10us( unsigned char t )

ated function) Delays execution for 't\*10' micro seconds. Declared in .h This function gets generated every time a project is linked and is lied by the CLOCK\_FREQ pragma. In some cases, when clock frequency is y it's not physically possible to generate this function. 's the case, the linker will issue a warning.

#### lelay\_100us( unsigned char t )

ated function) Delays execution for 't\*100' micro seconds. Declared in .h This function gets generated every time a project is linked and is lied by the CLOCK\_FREQ progma. In some cases, when clock frequency is y, it's not physically possible to generate this function. 's the case, the linker will issue a warning.

#### lelay\_ms( unsigned char t )

ated function) Delays execution for 't' milli seconds. Declared in boostc.h inction gets generated every time a project is linked and is controlled by the (\_FREQ pragma.

#### lelay\_s( unsigned char t )

ated function) Delays execution for 't' seconds, Declared in boostc.h This in gets generated every time a project is linked and is controlled by the (\_FREQ pragma.

about delays: The delays provided are at least the value specified, the will be longer rather than shorter. The delays produced may be larger than red if the delay routine is interrupted by an interrupt.

the clock frequency is such that the delay becomes highly inaccurate then lay overhead, unit delay and delay resolution of the delay are displayed the linking process.

58

SourceBoost Technologies

BoostC™ Manual

C language superset

The BoostC compiler has some advanced features "borrowed" from C++ language, These features allow development of more flexible and powerful code, but their use is merely optional.

#### References as function arguments

Function arguments can be references to other variables.

When such argument changes inside a function the original variable used in function call changes too.

This is a very powerful way to alter the data flow without blowing up the generated code:

ł votd main(votd) char a = 0; foo( a ); //upon return 'a' will have value of 100 

#### Notes on using references as function arguments

For general efficiency, the mechanism used to pass a variable by reference is that of taking a copy of the variable data when the function is called, and by copying the data back to the original variable after the function has been exited.

Passing a large structure by reference will generate a large amount of code to copy the data back and fourth. Passing volatile variables (those declared using the volatile type specifier) may result in not the behavior you would expect, despite being a volatile variable its value will only get updated on exit of the function. General guidelines:

- Don't pass large data structures by reference.
- Don't pass volatile data by reference.

#### Function overloading

There can be more than one function in the same application having a given name. Such functions must anyway differ by the number and type of their arguments:

Page 66

SourceBoost Technologies

BoostC<sup>m</sup> Manual

лđ	strcat(	char	*dst,	cons	it cha	r *src)	
2ld	strcat(	char	*dst,	rom	char	*src )	

old strncat( char \*dst, const char \*src, unsigned char len ) old strncat( char \*dst, rom char \*src, unsigned char len ) unction) Appends zero terminated string 'src' to destination string 'dst'. estination buffer must be big enough for string to fit. Declared in string,h

har\* strpbrk( const char \*ptr1, const char \*ptr2 ) har\* strpbrk( const char \*src, rom char \*src )

atch any character in the search string. Declared in string.

nsigned char strcspn( const char \*src1, const char \*src2 ) nsigned char strcspn( rom char \*src1, const char \*src2 ) nsigned char strcspn( const char \*src1, rom char \*src2 ) nsigned char strcspn( rom char \*src1, rom char \*src2 ) unction) Locates the first occurrence of a character in the string that doesn't

rsigned char strspn( const char \*src1, const char \*src2 ) rsigned char strspn( rom char \*src1, const char \*src2 ) rsigned char strspn( const char \*src1, rom char \*src2 ) rsigned char strspn( rom char \*src1, rom char \*src2 ) unction) Locates the first occurrence of a character in the string. Declared in ring.h

iar\* strtok( const char \*ptr1, const char \*ptr2 ) iar\* strtok( const char \*src, rom char \*src )

unction) Breaks string pointed into a sequence of tokens, each of which is slimited by a character from delimiter string. Declared in string.h

## ar\* strchr( const char \*src, char ch )

unction) Locates the first occurrence of a character in the string. Declared in ring.h

### ar\* strichr( const char \*src, char ch )

unction) Locates the last occurrence of a character in the string. Declared in ring.h

# iar\* strstr( const char \*ptr1, const char \*ptr2 ) iar\* strstr( const char \*src, rom char \*src )

unction) Locates the first occurrence of a sub-string in the string. Declared in ring.h

oostC™ Manual

SourceBoost Technologies

Page 71

Delay Overhead - The delay created in calling, setting up and returning from the delay function.

Unit Delay - The amount of additional delay generated for a delay value increase of 1.

Delay Resolution – The amount the delay value has to be increased before an actual increase in the delay occurs. A delay resolution of 4 would mean that the delay value may need to be increased by a value of up to 4 in order to see an increase in the delay.

#### System Libraries

A number of standard functions are included into BoostC installations. The number of such functions isn't static. It increases from release to release as new features are added. Most of these functions are declared in boostc.h (It's not recommended to include boostc.h directly into your code. Instead include system.h which in turn included boostc.h)

#### **General purpose functions**

clear bit( var. num )

(macro) Clears bit 'num' in variable 'var'. Declared in boostc.h

set\_bit( var, num )

(macro) Sets bit 'num' in variable 'var'. Declared in hooste h

test\_bit( var, num )

(macro) Tests if bit 'num' in variable 'var' is set. Declared in boostc.h

#### MAKESHORT( dst, lobyte, hibyte )

(macro) Makes a 16 bit long value (stored in 'dst') from two 8-bit long values (low byte 'lobyte' and high byte 'hibyte'). 'dst' must be a 16-bit long variable. Declared in boostc.h

. unsigned short res; MAXESHORT(res; adres], adresh ); //make 16 bit value from adresh:adresh registers and write i into variable res?

LOBYTE( dst. src )

(macro) Gets low byte from 'src' and writes it into 'dst'. Declared in boostc.h

### HIBYTE( dst. src )

(macro) Gets high byte from 'src' and writes it into 'dst', 'src' must be a 16-bit long variable. Declared in boostc.h

SourceBoost Technologies

#### void nop( void )

(inline function) Generates one 'nop' Instruction. Declared in boostc.h

BoostC<sup>™</sup> Manual

void clear wdt( void )

void sleep( void )

Page 69

#### rersion Functions

When using conversion functions that store the ASCII result in a buffer, be o provide a buffer of sufficient size or other memory may get overwritten. uffer needs to be enough to store the resulting characters and a null lator.

med char sprintf( char\* buffer, const char \*format, unsigned int val ) its a numerical value to a string in the specified format. The buffer must be nough to hold the result. Only one numerical value can be output at a time. red in stdio.h.

it specified in the format string with the following format: gs][width][radix specifier]

<	Example output	Description	
	"-120"	decimal signed integer	
	"150"	decimal unsigned integer	
	*773″	octal unsigned Integer	
	"ABF1"	hex unsigned integer	
	"101101"	binary unsigned integer	

fication	Example output	Description
17	*231 *	left justified, padded to 8 characters length
. <b>6</b> u″	-0000000000045102*	left justified, padded with zeroes to 16 characters length
b"	* 10"	right justified, padded 8 characters length
ay of	Example output	Description
30"	*+972 *	left justified, padded 8 characters length, signed always displayed
d″	* 765 *	left justified, padded 8 characters length, positive signed displayed as ''

o show complete format specification

72

SourceBoost Technologies

BoostC<sup>m</sup> Manual

Page 70

BoostC<sup>m</sup> Manual

## String and Character Functions void strcpy( char \*dst, const char \*src ) void strcpy( char \*dst, rom char \*src )

void strncpy( char \*dst, const char \*src, unsigned char len ) void strncpy( char \*dst, rom char \*src, unsigned char len )

(inline function) Generates one 'cirwdt' instruction, Declared in boostc.h

(Inline function) Generates one 'sleep' Instruction. Declared in boostc.h

(function) Copies zero terminated string 'src' into destination buffer 'dst'. Destination buffer must be big enough for string to fit. Declared in string.h

unsigned char strien( const char \*src ) unsigned char strien( rom char \*src )

(function) Returns length of a string. Declared in string.h

signed char strcmp( const char \*src1, const char \*src2 ) signed char strcmp( rom char \*src1, const char \*src2 ) signed char strcmp( const char \*src1, rom char \*src2 ) signed char strcmp( rom char \*src1, rom char \*src2 )

signed char stricmp( const char \*src1, const char \*src2 signed char stricmp( rom char \*src1, const char \*src2 ) signed char stricmp( const char \*src1, rom char \*src2 ) signed char stricmp( rom char \*src1, rom char \*src2 )

(function) Compares two strings. Returns -1 if string #1 is less than string #2, 1 if string #1 is greater than string #2 or 0 is string #1 is same as string #2. Declared in string.h

signed char strncmp( char \*src1, char \*src2, unsigned char ien ) signed char strncmp( rom char \*src1, char \*src2, unsigned char ien ) signed char strncmp( char \*src1, rom char \*src2, unsigned char ien ) signed char strncmp( rom char \*src1, rom char \*src2, unsigned char ien )

signed char strnicmp( char \*src1, char \*src2, unsigned char len ) signed char strnicmp( rom char \*src1, char \*src2, unsigned char len ) signed char strnicmp( char \*src1, rom char \*src2, unsigned char len ) signed char strnicmp( rom char \*src1, rom char \*src2, unsigned char len ) (function) Compares first 'len' characters of two strings. Returns -1 if string #1 is less than string #2, 1 if string #1 is greater than string #2 or 0 is string #1 is same as string #2. Declared in string.h

	bin( const char* buffer ) nsigned integer, binary representation.	a. This function converts	Implementation of field width is non standard - If a justification width is specified the width will be padded <b>or truncated</b> to match the width provided. The most
inary string value i	into 16 bit unsigned integer. dec( const char* buffer)		significant digits and sign maybe truncated. Standard implementations do not truncate the output, which can cause unexpected buffer overrun.
nction) ASCII to ur	nsigned integer, decimal representation ring value into 16 bit unsigned integer.		unsigned char sprintf32( char* buffer, const char *format, unsigned long
aracter	ing value into 10 bit ansighed integel.		<b>val</b> ) Outputs a numerical value to a string in the specified format. The buffer must be
ar toupper( char (			long enough to hold the result. Only one numerical value can be output at a time. Declared in stdio.h.
nction) Converts lo ar tolower( char a	wercase character to uppercase. Decla	ared in ctype.h	This function operates as sprintf, but it handles a 32bit value. It also supports the "%I" radix specifier, which is handled the same as "%d".
-	ppercase character to lowercase. Decla	ared in ctype.h	int strtol( const char* buffer, char** endPtr, unsigned char radix )
ar isdigit( char ch	•		(Function) String to integer. A function that converts the numerical character string supplied into a signed integer (16 bit) value using the radix specified. Radix unlid mere 3 at a 20
clared in ctype.h	haracter 'ch' is a digit. Returns non zer	ro ir triis is a digic.	valid range 2 to 26. <b>buffer:</b> Pointer to a numerical string.
ar isalpha( char c Inction) Checks if cl	カ) haracter 'ch' is a letter. Returns non ze	era if this is a letter	endPtr: Address of a pointer. This is filled by the function with the address where string scan has ended. Allows determination of where there is the first non-
clared in ctype.h			numerical character in the string. Passing a NULL is valid and causes the end scan address not to be saved.
	haracter 'ch' is a letter or a digit. Retu	rns non zero if this is a	radix: The radix (number base) to use for the conversion, typical values: 2 (binary), 8 (octal), 10 (decimal), 16 (hexadecimal).
ter or a digit. Decia ar isbiank( char c			<b>Return:</b> The converted value.
inction) Returns a 1	If the argument is a standard blank c		long strtoi( const char* buffer, char** endPtr, unsigned char radix ); (Function) String to long integer. A function that converts the numerical character
	The following are the standard blank or rizontal tab). Declared in ctype.h	cnaracters:	string supplied into a signed long integer (32 bit) value using the radix specified, Radix valid range 2 to 26.
ar iscntri( char cl nction) Returns a 1		stactor All other traver	<b>buffer</b> : Pointer to a numerical string endPtr: Address of a pointer. This is filled by the function with the address where
inction) Returns a 1 Il return a 0. Declar	l if the argument is a valid control cha red in ctype.h	racter. All other inputs	string scan has ended. Allows determination of where there is the first non- numerical character in the string. Passing a NUL is valid and causes the end scan
ar isgraph( char o inction) Returns a 1	<b>ch )</b> L if the argument is a valld displayable	ASCII character All	address not to be saved. radix: The radix (number base) to use for the conversion, typical values: 2
ner inputs will retur	n a 0. Declared in ctype.h	, were one-outer. All	(binary), 8 (octal), 10 (decimal), 16 (hexadecimal). Return: The converted value.
ar islower( char c inction) Returns a 1	<b>:h )</b> I if the argument is a valid lower-case	ASCII letter, All other	Return: The converted value.
	Declared in ctype,h		(Macro) ASCII to integer. A macro that converts the numerical character string supplied into a signed integer (16 bit) value using a radix of 10.
ostC <sup>m</sup> Manual			· · · · · · · · · · · · · · · · · · ·
isprint( char ch )	the accument is a valid printable ASCO	L'eborster All	buffer: Pointer to a numerical string,
ion) Returns a 1 if t	the argument is a valid printable ASCII 0. Declared in ctype.h	I character. All	<b>buffer</b> : Pointer to a numerical string. <b>Return</b> : The converted value. Note: Macro implemented as: #define atoi( buffer ) strtoi( buffer, NULL, 10 )
ion) Returns a 1 if t Inputs will return a Ispunct( char ch )	0. Declared in ctype.h		Retura: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtoi( buffer, NULL, 10 ) Iong atoi( const char* buffer )
ion) Returns a 1 if t Inputs will return a <i>ispunct( char ch )</i> ion) Returns a 1 if t s will return a 0. The	0. Declared in ctype.h the argument is a valid punctuation ch e following are the implemented punct	aracter. All other	Retura: The converted value. Note: Macro implemented as: #define atoi( buffer ) strtoi( buffer, NULL, 10 )
on) Returns a 1 if t inputs will return a spunct( char ch ) on) Returns a 1 if t will return a 0. The ; % & ' ( ) * + ,	0. Declared in ctype.h the argument is a valid punctuation ch a following are the implemented punctuation ();; < = > ? @ [\]^_` \ [] ~	aracter. All other	Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtoi( buffer, NULL, 10 ) <i>long atoi( const char* buffer )</i> (Macro) ASCII to long integer. A macro that converts the numerical character
ion) Returns a 1 if t inputs will return a <i>ispunct</i> ( <i>char ch</i> ) ion) Returns a 1 if t will return a 0. The is % & '()*+, <i>isspace</i> ( <i>char ch</i> ) ion) Returns a 1 if 1 inputs will return a	0. Declared in ctype.h the argument is a valid punctuation ch a following are the implemented punctuation ();; < = > ? @ [\]^_` \ [] ~	naracter. All other wation characters: ce character. All	Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtoi( buffer, NULL, 10 ) <i>long atoi( const char* buffer )</i> (Macro) ASCII to long integer. A macro that converts the numerical character string supplied into a signed long integer (32 bit) value using a radix of 10. buffer: Pointer to a numerical string.
ion) Returns a 1 if t inputs will return a ispunct( char ch ) ion) Returns a 1 if t will return a 0. The $\$ \ \% \ \$' () * + ,$ isspace( char ch ) ion) Returns a 1 if i inputs will return a space characters:	<ul> <li>0. Declared in ctype.h</li> <li>the argument is a valid punctuation che following are the implemented punctuation (;; &lt; = &gt;? @[\]^_``{[}~</li> <li>the argument is a standard white-space</li> <li>0. Declared in ctype.h. The following</li> <li>Character Character Escape</li> </ul>	naracter. All other wation characters: ce character. All	Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtol( buffer, NULL, 10 ) Iong atol( const char* buffer ) (Macro) ASCII to long integer. A macro that converts the numerical character string supplied into a signed long integer (32 bit) value using a radix of 10. buffer: Pointer to a numerical string. Return: The converted value.
ion) Returns a 1 if t Inputs will return a <i>ispunct( char ch )</i> ion) Returns a 1 if t will return a 0. The \$ % & ( ) * + , <i>isspace( char ch )</i> ion) Returns a 1 if i inputs will return a -space characters:	0. Declared in ctype.h the argument is a valid punctuation ch a following are the implemented punct / :; < = > ? @ [\]^_` ( [ } ~ the argument is a standard white-spac 0. Declared in ctype.h. The following	naracter. All other wation characters: ce character. All	Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtoi( buffer, NULL, 10 ) Iong atoi( const char* buffer ) (Macro) ASCII to long integer. A macro that converts the numerical character string supplied into a signed long integer (32 bit) value using a radix of 10. buffer: Pointer to a numerical string. Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtoi( buffer, NULL, 10 )
ion) Returns a 1 if t Inputs will return a ispunct( char ch ) ion) Returns a 1 if t will return a 0. The % % () * +, -. isspace( char ch ) ion) Returns a 1 if 1 inputs will return a space characters: icter Description intal tab	0. Declared in ctype.h         the argument is a valid punctuation ch         a following are the implemented punct         /;;<<=>? @ [\]^_` ( ] ^_` ( ] > ~         the argument is a standard white-space         0. Declared in ctype.h. The following         Character       Character Escape         ASCII code       sequence         0x20       ''         0x09       '\t'	naracter. All other wation characters: ce character. All	Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtol( buffer, NULL, 10 ) Iong atoi( const char* buffer ) (Macro) ASCII to long integer. A macro that converts the numerical character string supplied into a signed long integer (32 bit) value using a radix of 10. buffer: Pointer to a numerical string. Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtoi( buffer, NULL, 10 ) char* itoa( int val, char* buffer, unsigned char radix ) (Function) Integer to ASCII. function that converts an integer (16 bit) value into a char* itoa( long val, char* buffer, unsigned char radix )
ion) Returns a 1 if t Inputs will return a ispunct( char ch ) ion) Returns a 1 if t will return a 0. The \$ % & ' () * + , isspace( char ch ) ion) Returns a 1 if 1 inputs will return a -space characters: sacter Description inter the second inter the second i	0. Declared in ctype.h the argument is a valid punctuation ch a following are the implemented punct /;; < = > ? $\odot$ [\]^_` ( ] ~ the argument is a standard white-spac 0. Declared in ctype.h. The following Character Character Escape ASCII code sequence 0x20 '' 0x09 '\t' 0x08 '\v' 0x0A '\n'	naracter. All other wation characters: ce character. All	Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtoi( buffer, NULL, 10 ) Iong atoi( const char* buffer ) (Macro) ASCII to long integer. A macro that converts the numerical character string supplied into a signed long integer (32 bit) value using a radix of 10. buffer: Pointer to a numerical string. Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtoi( buffer, NULL, 10 ) char* itoa( Int val, char* buffer, unsigned char radix ) (Function) Integer to ASCII. function that converts an integer (16 bit) value into a character string.
ion) Returns a 1 if t Inputs will return a ispunct( char ch ) ion) Returns a 1 if t will return a 0. The \$ % & ' () * + , isspace( char ch ) ion) Returns a 1 if i inputs will return a -space characters: inter Description intel tab intel tab age return	0. Declared in ctype.h         the argument is a valid punctuation che         following are the implemented punctuation         /;;<<=>? @ [\]^` ( ] ^`         the argument is a standard white-space         0. Declared in ctype.h. The following         Character       Character Escape         ASCII code       sequence         0x20       ''         0x08       '\v'	naracter. All other wation characters: ce character. All	Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtol( buffer, NULL, 10 ) Iong atoi( const char* buffer ) (Macro) ASCII to long integer. A macro that converts the numerical character string supplied into a signed long integer (32 bit) value using a radix of 10. buffer: Pointer to a numerical string. Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtoi( buffer, NULL, 10 ) char* itoa( Int val, char* buffer, unsigned char radix ) (Function) Integer to ASCII. function that converts an integer (16 bit) value into a char* itoa( long val, char* buffer, unsigned char radix ) (Function) Long Integer to ASCII. function that converts an long integer (32 bit) value into a character string. Lightweight Conversion Functions
ion) Returns a 1 if t Inputs will return a ispunct( char ch ) ion) Returns a 1 if t will return a 0. The isspace( char ch ) ion) Returns a 1 if i inputs will return a space characters: icter Description intal tab al tab ne ge return feed	0. Declared in ctype.h         the argument is a valid punctuation che following are the implemented punct         for a standard white-space         0. Declared in ctype.h. The following         Character       Character Escape         ASCII code       sequence         0x20       ''         0x09       '\t'         0x08       '\v'         0x00       '\t'         0x00       '\t'         0x00       '\t'         0x00       '\t'	naracter. All other wation characters: ce character. All	Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtol( buffer, NULL, 10 ) Iong atoi( const char* buffer ) (Macro) ASCII to long integer. A macro that converts the numerical character string supplied into a signed long integer (32 bit) value using a radix of 10. buffer: Pointer to a numerical string. Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtoi( buffer, NULL, 10 ) char* itoa( Int val, char* buffer, unsigned char radix ) (Function) Integer to ASCII. function that converts an integer (16 bit) value into a char* itoa( long val, char* buffer, unsigned char radix ) (Function) Long Integer to ASCII. function that converts an long integer (32 bit) value into a character string. Lightweight Conversion Functions The standard conversion functions offer a lot of flexibility at the cost of ROM, RAM and execution time. For application that are short of RAM and ROM, or require
ion) Returns a 1 if t Inputs will return a ispunct( char ch ) ion) Returns a 1 if t will return a 0. The % & ' () * + , isspace( char ch ) ion) Returns a 1 if 1 inputs will return a -space characters: acter Description antal tab ne age return feed isupper( char ch ) ion) Returns a 1 if	0. Declared In ctype.h         the argument is a valid punctuation che         a following are the Implemented punctuation         the argument is a standard white-space         0. Declared in ctype.h. The following         Character       Character Escape         ASCII code       sequence         0x20       ''         0x09       '\t'         0x08       '\v'         0x00       '\v'	haracter. All other kuation characters: ce character. All are the standard	Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtoi( buffer, NULL, 10 ) Iong atoi( const char* buffer ) (Macro) ASCII to long integer. A macro that converts the numerical character string supplied into a signed long integer (32 bit) value using a radix of 10. buffer: Pointer to a numerical string. Return: The converted value. Note: Macro implemented as: #define atoi( buffer ) strtoi( buffer, NULL, 10 ) char* itoa( int val, char* buffer, unsigned char radix ) (Function) Integer to ASCII. function that converts an integer (16 bit) value into a char* itoa( long val, char* buffer, unsigned char radix ) (Function) Long Integer to ASCII. function that converts an iong integer (32 bit) value into a character string. Lightweight Conversion Functions The standard conversion functions offer a lot of flexibility at the cost of ROM, RAM
ion) Returns a 1 if t inputs will return a ispunct( char ch ) ion) Returns a 1 if t will return a 0. The $\frac{1}{3}$ % % () * + , isspace( char ch ) ion) Returns a 1 if t inputs will return a -space characters: acter Description antal tab age return feed isupper( char ch ) tion) Returns a 1 if t is will return a 0. De	0. Declared in ctype.h         the argument is a valid punctuation che         e following are the implemented punctuation         is following are the implemented punctuation         /;; < = > ? @ [\]^ ` (] > ~         the argument is a standard white-space         0. Declared in ctype.h. The following         Character       Character Escape         ASCII code       sequence         0x09       '\t'         0x08       '\v'         0x00       '\v         )       the argument is a valid upper-case AS clared in ctype.h	haracter. All other kuation characters: ce character. All are the standard	Return: The converted value. Note: Macro Implemented as; #define atoi( buffer ) strtol( buffer, NULL, 10 ) Iong atol( const char* buffer ) (Macro) ASCII to long integer. A macro that converts the numerical character string supplied into a signed long integer (32 bit) value using a radix of 10. buffer: Pointer to a numerical string. Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtoi( buffer, NULL, 10 ) char* itoa( int val, char* buffer, unsigned char radix ) (Function) Integer to ASCII. function that converts an integer (16 bit) value into a character string. char* itoa( long val, char* buffer, unsigned char radix ) (Function) Long Integer to ASCII. function that converts an long integer (32 bit) value into a character string. Lightweight Conversion Functions The standard conversion functions offer a lot of flexibility at the cost of ROM, RAM and execution time, it maybe desirable to use the following lightweight functions. void uitoa_hex( char* buffer, unsigned int val, unsigned char digits )
ion) Returns a 1 if t Inputs will return a ispunct( char ch ) ion) Returns a 1 if t will return a 0. The \$ % & ( ) * + , isspace( char ch ) ion) Returns a 1 if 1 inputs will return a space characters: inter Description is intal tab intal tab intab intal tab	0. Declared in ctype.h the argument is a valid punctuation ch a following are the implemented punct /;; < = > ? $(   ) ^ _ (   ) ~$ the argument is a standard white-spac 0. Declared in ctype.h. The following Character Character Escape ASCII code sequence 0x09 '\t' 0x00 '\t'	naracter. All other wation characters: ce character. All are the standard SCII letter. All other	Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtol( buffer, NULL, 10 ) Iong atoi( const char* buffer ) (Macro) ASCII to long integer. A macro that converts the numerical character string supplied into a signed long integer (32 bit) value using a radix of 10. buffer: Pointer to a numerical string. Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtoi( buffer, NULL, 10 ) char* itoa( int val, char* buffer, unsigned char radix ) (Function) Integer to ASCII. function that converts an integer (16 bit) value into a character string. char* itoa( long val, char* buffer, unsigned char radix ) (Function) Long Integer to ASCII. function that converts an long integer (32 bit) value into a character string. Lightweight Conversion Functions The standard conversion functions offer a lot of flexibility at the cost of ROM, RAM and execution time, for application that are short of RAM and ROM, or require shorter execution time, it maybe desirable to use the following lightweight functions.
ion) Returns a 1 if t Inputs will return a ispunct( char ch ) ion) Returns a 1 if t will return a 0. The \$ % & ' () * + , isspace( char ch ) ion) Returns a 1 if 1 inputs will return a space characters: icter Description antal tab at ab ne ge return feed isupper( char ch ) ion) Returns a 1 if s will return a 0. De iswill return a 0. De	0. Declared in ctype.h the argument is a valid punctuation ch a following are the implemented punct /;; < = > ? $(   ) ^ _ (   ) ~$ the argument is a standard white-spac 0. Declared in ctype.h. The following Character Character Escape ASCII code sequence 0x09 '\t' 0x00 '\t'	haracter. All other wation characters: ce character. All are the standard SCII letter. All other character. All other	Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtol( buffer, NULL, 10 ) Iong atoi( const char* buffer ) (Macro) ASCII to long integer. A macro that converts the numerical character string supplied into a signed long integer (32 bit) value using a radix of 10. buffer: Pointer to a numerical string. Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtoi( buffer, NULL, 10 ) char* itoa( int val, char* buffer, unsigned char radix ) (Function) Integer to ASCII. function that converts an integer (16 bit) value into a character string. char* itoa( long val, char* buffer, unsigned char radix ) (Function) Long Integer to ASCII. function that converts an long integer (32 bit) value into a character string. Lightweight Conversion Functions The standard conversion functions offer a lot of flexibility at the cost of ROM, RAM and execution time. For application that are short of RAM and ROM, or require shorter execution time, it maybe desirable to use the following lightweight functions. vold uitoa_hex( char* buffer, unsigned int val, unsigned char digits ) (Function) Unsigned integer to ASCII, hexadecimal representation. This function converts a 16 bit unsigned integer into a hex value with leading zeros. The number of digits is specified using by the digits parameter. void uitoa_bin( char* buffer, unsigned int val, unsigned char digits )
ion) Returns a 1 if t inputs will return a ispunct(char ch ) ion) Returns a 1 if t will return a 0. The isspace(char ch ) ion) Returns a 1 if 1 inputs will return a space characters: cter Description intal tab al tab ne ige return feed isupper(char ch ) ion) Returns a 1 if will return a 0. De iswill re	0. Declared in ctype.h the argument is a valid punctuation ch a following are the implemented punct /;; < = > ? $(   ) ^ _ (   ) ~$ the argument is a standard white-spac 0. Declared in ctype.h. The following Character Character Escape ASCII code sequence 0x20 '' 0x09 ''tt' 0x08 ''v' 0x08 ''v' 0x00 ''v' 0x00 ''v' 0x0C ''v'	aaracter. All other wation characters: ce character. All are the standard SCII letter. All other character. All other en ) nemory.h	Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtol( buffer, NULL, 10 ) Iong atoi( const char* buffer ) (Macro) ASCII to long integer. A macro that converts the numerical character string supplied into a signed long integer (32 bit) value using a radix of 10. buffer: Pointer to a numerical string. Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtoi( buffer, NULL, 10 ) char* itoa( int val, char* buffer, unsigned char radix ) (Function) Integer to ASCII. function that converts an integer (16 bit) value into a character string. char* itoa( long val, char* buffer, unsigned char radix ) (Function) Long Integer to ASCII. function that converts an long integer (32 bit) value into a character string. Lightweight Conversion Functions The standard conversion functions offer a lot of flexibility at the cost of ROM, RAM and execution time. For application that are short of RAM and ROM, or require shorter execution time, it maybe desirable to use the following lightweight functions. vold uitoa_hex( char* buffer, unsigned int val, unsigned char digits ) (Function) Unsigned integer to ASCII, hexadecimal representation. This function converts a 16 bit unsigned integer into a hex value with leading zeros. The number of digits is specified using by the digits parameter. vold uitoa_bin( char* buffer, unsigned int val, unsigned char digits ) (Function) Unsigned integer to ASCII, hexadecimal representation. This function converts a 16 bit unsigned integer into a hex value with leading zeros. The number of digits is specified using by the digits parameter. vold uitoa_bin( char* buffer, unsigned int val, unsigned char digits ) (Function) Unsigned integer into a binary value with leading zeros. The number of digits is specified using by the digits parameter.
ion) Returns a 1 if t Inputs will return a ispunct(char ch ) ion) Returns a 1 if t will return a 0. The % & '()* +, isspace(char ch ) ion) Returns a 1 if 1 inputs will return a space characters: teter Description intal tab at tab ne ge return feed isupper(char ch ) ion) Returns a 1 if s will return a 0. De iswill return a 0. De iswill return a 1 if s will return a 0. De iswill return a 0. De	0. Declared in ctype.h         the argument is a valid punctuation che         the following are the implemented punct         /;; < = > ? @ [\]^` ( ] ^         the argument is a standard white-space         0. Declared in ctype.h. The following         Character         Ox00         0x00         '\t'         '         '         '         '         '         '	aaracter. All other wation characters: ce character. All are the standard SCII letter. All other character. All other en ) nemory.h	Return: The converted value. Note: Macro Implemented as; #define atoi( buffer ) strtol( buffer, NULL, 10 ) Iong atoi( const char* buffer ) (Macro) ASCII to long integer. A macro that converts the numerical character string supplied into a signed long integer (32 bit) value using a radix of 10. buffer: Pointer to a numerical string. Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtoi( buffer, NULL, 10 ) char* itoa( int val, char* buffer, unsigned char radix ) (Function) Integer to ASCII. function that converts an integer (16 bit) value into a character string. char* floa( long val, char* buffer, unsigned char radix ) (Function) Long Integer to ASCII. function that converts an long integer (32 bit) value into a character string. Lightweight Conversion Functions The standard conversion functions offer a lot of flexibility at the cost of ROM, RAM and execution time, it maybe desirable to use the following lightweight functions. void uitoa_hex( char* buffer, unsigned int val, unsigned char ing is function converts a 16 bit unsigned integer to ASCII, hexadecimal representation. This function converts a 16 bit unsigned integer into a hex value with leading zeros. The number of digits is specified using by the digits parameter. void uitoa_bin( char* buffer, unsigned int val, unsigned char digits ) (Function) Unsigned integer to ASCII, hexadecimal representation. This function converts a 16 bit unsigned integer into a hex value with leading zeros. The number of digits is specified using by the digits parameter.
ion) Returns a 1 if t Inputs will return a Ispunct( char ch ) ion) Returns a 1 if t will return a 0. The \$ % & ' () * + , isspace( char ch ) ion) Returns a 1 if t Inputs will return a -space characters: cuter Description 	0. Declared in ctype.h         the argument is a valid punctuation ch         a following are the implemented punct         i; ; < = > ? @ [\]^`(]^_         the argument is a standard white-space         0. Declared in ctype.h. The following         Character       Character Escape         ASCII code       sequence         0x09       '\t'         0x00       '\t'         0x0A       '\n'         0x0D       '\t'         0x0C       '\t'         0       '\t'         0x0C       '\t'         0x0C       '\t'         0x0C       '\t'         0x0C       '\t'         0x0C       '\t'         0x0C       '\t'         0       '\to'         0       '\to'         0       '\to'	aracter. All other kuation characters: ce character. All a are the standard SCII letter. All other character. All other en ) nemory.h 2, unsigned char	Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtol( buffer, NULL, 10 ) Iong atoi( const char* buffer ) (Macro) ASCII to long integer. A macro that converts the numerical character string supplied into a signed long integer (32 bit) value using a radix of 10. buffer: Pointer to a numerical string. Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtoi( buffer, NULL, 10 ) char* itoa( Int val, char* buffer, unsigned char radix ) (Function) Integer to ASCII. function that converts an integer (16 bit) value into a character string. char* itoa( long val, char* buffer, unsigned char radix ) (Function) Long Integer to ASCII. function that converts an long integer (32 bit) value into a character string. Lightweight Conversion Functions The standard conversion functions offer a lot of flexibility at the cost of ROM, RAM and execution time. For application that are short of RAM and ROM, or require shorter execution time, it maybe desirable to use the following lightweight functions. void uitoa_hex( char* buffer, unsigned int val, unsigned char digits ) (Function) Unsigned integer to ASCII, hexadecimal representation. This function converts a 16 bit unsigned integer into a hex value with leading zeros. The number of digits is specified using by the digits parameter. void uitoa_bin( char* buffer, unsigned int val, unsigned char digits ) (Function) Unsigned integer to ASCII, hexadecimal representation. This function converts a 16 bit unsigned integer into a hex value with leading zeros. The number of digits is specified using by the digits parameter.
ion) Returns a 1 if t Inputs will return a ispunct( char ch ) ion) Returns a 1 if t will return a 0. The \$ % & ' () * + , isspace( char ch ) ion) Returns a 1 if t inputs will return a -space characters: acter Description acter Description antal tab cal tab c	0. Declared in ctype.h         the argument is a valid punctuation che         e following are the implemented punct         i; i < = > ? @ [\]^_ ` ` { [ } ~         the argument is a standard white-space         0. Declared in ctype.h. The following         Character       Character Escape         ASCII code       sequence         0x09       '\t'         0x08       '\v'         0x00       '\r'         0x00       '\t'         0x00       '\t' <td>aracter. All other kuation characters: ce character. All a are the standard SCII letter. All other character. All other en ) nemory.h 2, unsigned char</td> <td>Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtol( buffer, NULL, 10 ) Iong atoi( const char* buffer ) (Macro) ASCII to long integer. A macro that converts the numerical character string supplied into a signed long integer (32 bit) value using a radix of 10. buffer: Pointer to a numerical string. Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtoi( buffer, NULL, 10 ) char* itoa( int val, char* buffer, unsigned char radix ) (Function) Integer to ASCII. function that converts an integer (16 bit) value into a character string. char* floa( long val, char* buffer, unsigned char radix ) (Function) Long Integer to ASCII. function that converts an long integer (32 bit) value into a character string. Lightweight Conversion Functions The standard conversion functions offer a lot of flexibility at the cost of ROM, RAM and execution time. For application that are short of RAM and ROM, or require shorter execution time, it maybe desirable to use the following lightweight functions. void uitoa_hex( char* buffer, unsigned int val, unsigned char digits ) (Function) Unsigned integer to ASCII, hexadecimal representation. This function converts a 16 bit unsigned integer into a hex value with leading zeros. The number of digits is specified using by the digits parameter. void uitoa_bin( char* buffer, unsigned int val, unsigned char digits ) (Function) Unsigned integer into a SCII, binary representation. This function converts a 16 bit unsigned integer into a binary value with leading zeros. The number of digits is specified using by the digits parameter. void uitoa_dec( char* buffer, unsigned int val, unsigned char digits ) (Function) Unsigned integer to ASCII, binary representation. This function converts a 16 bit unsigned integer into a binary value with leading zeros. The number of digits is specified using by the digits parameter.</td>	aracter. All other kuation characters: ce character. All a are the standard SCII letter. All other character. All other en ) nemory.h 2, unsigned char	Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtol( buffer, NULL, 10 ) Iong atoi( const char* buffer ) (Macro) ASCII to long integer. A macro that converts the numerical character string supplied into a signed long integer (32 bit) value using a radix of 10. buffer: Pointer to a numerical string. Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtoi( buffer, NULL, 10 ) char* itoa( int val, char* buffer, unsigned char radix ) (Function) Integer to ASCII. function that converts an integer (16 bit) value into a character string. char* floa( long val, char* buffer, unsigned char radix ) (Function) Long Integer to ASCII. function that converts an long integer (32 bit) value into a character string. Lightweight Conversion Functions The standard conversion functions offer a lot of flexibility at the cost of ROM, RAM and execution time. For application that are short of RAM and ROM, or require shorter execution time, it maybe desirable to use the following lightweight functions. void uitoa_hex( char* buffer, unsigned int val, unsigned char digits ) (Function) Unsigned integer to ASCII, hexadecimal representation. This function converts a 16 bit unsigned integer into a hex value with leading zeros. The number of digits is specified using by the digits parameter. void uitoa_bin( char* buffer, unsigned int val, unsigned char digits ) (Function) Unsigned integer into a SCII, binary representation. This function converts a 16 bit unsigned integer into a binary value with leading zeros. The number of digits is specified using by the digits parameter. void uitoa_dec( char* buffer, unsigned int val, unsigned char digits ) (Function) Unsigned integer to ASCII, binary representation. This function converts a 16 bit unsigned integer into a binary value with leading zeros. The number of digits is specified using by the digits parameter.
ion) Returns a 1 if t Inputs will return a ispunct( char ch ) ion) Returns a 1 if t will return a 0. The \$ % & ' () * + , isspace( char ch ) ion) Returns a 1 if t inputs will return a -space characters: acter Description antal tab cal tab cal tab cal tab cal tab me age return freed Isupper( char ch ) tion) Returns a 1 if s will return a 0. De * memchr( const i tion) Locates the fir so char memcmp( vition) Compares memor * memcor( void * tion) Copies memor * memcove( void	0. Declared in ctype.h         the argument is a valid punctuation ch         a following are the implemented punct         i; ; < = > ? @ [\]^_ ` ` { [ } ~         the argument is a standard white-spac         0. Declared in ctype.h. The following         Character       Character Escape         ASCII code       sequence         0x09       '\t'         0x08       '\t'         0x00       '\t'	aracter. All other kuation characters: ce character. All are the standard SCII letter. All other character. All other en ) nemory.h 2, unsigned char r len )	Return: The converted value. Note: Macro Implemented as; #define atoi( buffer ) strtol( buffer, NULL, 10 ) Iong atoi( const char* buffer ) (Macro) ASCII to long integer. A macro that converts the numerical character string supplied into a signed long integer (32 bit) value using a radix of 10. buffer: Pointer to a numerical string. Return: The converted value. Note: Macro Implemented as: #define atoi( buffer ) strtoi( buffer, NULL, 10 ) char* itoa( int val, char* buffer, unsigned char radix ) (Function) Integer to ASCII. function that converts an integer (16 bit) value into a character string. char* floa( long val, char* buffer, unsigned char radix ) (Function) Long Integer to ASCII. function that converts an long integer (32 bit) value into a character string. Lightweight Conversion Functions The standard conversion functions offer a lot of flexibility at the cost of ROM, RAM and execution time, it maybe desirable to use the following lightweight functions. void uitoa_hex( char* buffer, unsigned int val, unsigned char ingits ) (Function) Unsigned integer to ASCII, hexadecimal representation. This function converts a 16 bit unsigned integer into a hex value with leading zeros. The number of digits is specified using by the digits parameter. void uitoa_bin( char* buffer, unsigned int val, unsigned char digits ) (Function) Unsigned integer to ASCII, hexadecimal representation. This function converts a 16 bit unsigned integer into a hex value with leading zeros. The number of digits is specified using by the digits parameter. void uitoa_bin( char* buffer, unsigned int val, unsigned char digits ) (Function) Unsigned integer to ASCII, hinary representation. This function converts a 16 bit unsigned integer to ASCII, hinary representation. This function converts a 16 bit unsigned integer into a decimal value with leading zeros. The number of digits is specified using by the digits parameter. void uitoa_dec( char* buffer, unsigned int val, unsigned char digits ) (Function) Unsigned integ
ion) Returns a 1 if t Inputs will return a ispunct( char ch ) ion) Returns a 1 if t will return a 0. The \$ % & ' () * + , isspace( char ch ) ion) Returns a 1 if t inputs will return a -space characters: acter Description antal tab cal tab cal tab cal tab cal tab me age return freed Isupper( char ch ) tion) Returns a 1 if s will return a 0. De * memchr( const i tion) Locates the fir so char memcmp( vition) Compares memor * memcor( void * tion) Copies memor * memcove( void	0. Declared in ctype.h         the argument is a valid punctuation ch         a following are the implemented punct         i; ; < = > ? @ [\]^_ ` ` { [ } ~         the argument is a standard white-spac         0. Declared in ctype.h. The following         Character       Character Escape         ASCII code       sequence         0x09       '\t'         0x09       '\t'         0x00       '\t'	aracter. All other kuation characters: ce character. All are the standard SCII letter. All other character. All other en ) nemory.h 2, unsigned char r len )	Return: The converted value. Note: Macro Implemented as: #define atol( buffer ) strtol( buffer, NULL, 10 ) <i>lang atol( const char* buffer )</i> (Macro) ASCII to long integer. A macro that converts the numerical character string supplied into a signed long integer (32 bit) value using a radix of 10. buffer: Pointer to a numerical string. Return: The converted value. Note: Macro Implemented as: #define atol( buffer ) strtol( buffer, NULL, 10 ) <i>char* lica( Int val, char* buffer, unsigned char radix )</i> (Function) Integer to ASCII. function that converts an integer (16 bit) value into a character string. <i>char* lica( long val, char* buffer, unsigned char radix )</i> (Function) Long Integer to ASCII. function that converts an long integer (32 bit) value into a character string. <i>Lightweight Conversion Functions</i> The standard conversion functions offer a lot of flexibility at the cost of ROM, RAM and execution time. For application that are short of RAM and ROM, or require shorter execution time, it maybe desirable to use the following lightweight functions. <i>vold uitoa_hax( char* buffer, unsigned int val, unsigned char digits )</i> (Function) Unsigned integer to ASCII, hexadecimal representation. This function converts a 16 bit unsigned integer to a ASCII, hexadecimal representation. This function converts as the bit unsigned integer to a ASCII, hexadecimal representation. This function converts as the digits parameter. <i>vold uitoa_hax( char* buffer, unsigned int val, unsigned char digits )</i> (Function) Unsigned Integer to ASCII, binary representation. This function converts a 16 bit unsigned integer to ASCII, parameter. <i>vold uitoa_dec( char* buffer, unsigned int val, unsigned char digits )</i> (Function) Unsigned integer to ASCII, binary representation. This function converts and 16 bit unsigned integer into a dex value with leading zeros. The number of digits is specified using by the <i>digits</i> parameter. <i>vold uitoa_dec( char* buffer, unsigned int val, unsigned char digits )</i> (Function) Unsigne

/ To be able to use the one wire library two global bit variables need to edeclared in the code. / These are the variables that control port pin ised for one wire ammunication. For example. / if the one wire interface is connected to pin 6 of port 8 the aclaration will look like this:

# define OO\_PORT PORTS define OO\_TRIS TRISS define OO\_PIN 6

platile bit oo bus & CO\_PORT ; CO\_PIN; platile bit oo bus\_tris & CO\_TRIS , CO\_PIN;

## 

/ start the conversion (non-blocking function)
\_start\_conversion();

/ Wait for completion, you could do other stuff here / But make sure that this function returns zero before / reading the scratchpad [Codumat\_for\_completion()]

//handle conversion time out

## / Read the scratchpad F( oo\_read\_scratchpad() )

//handle conversion error

/ And extract the temperature information .
nort data = 00\_get\_data();

## hort oo\_get\_data()

unction) Reads data from one wire bus. Declared in oo.h Defined in oo.pic16.lib nd oo.pic18.lib

£. .

يا المحدي

#### har oo read scratchpad()

unction) Reads scratchpad. Declared in oo.h Defined in oo.pic16.lib and p.plc18.lib

#### oid oo\_start\_conversion()

unction) Starts conversion. Declared in oo.h Defined in oo.pic16.lib and 3.plc18.lib

#### har oo\_conversion\_busy()

oo\_wait\_for\_completion()

unction) Checks if conversion is in progress, Returns 0 if no conversion is active. eclared in oo.h Defined in oo.pic16.lib and oo.pic18.lib

tion) Waits for a conversion to complete. Returns 0 if conversion completed

sec, Declared in oo.h Defined in oo.pic16-lib and oo.pic18.ilb

oostC™ Manual

11

SourceBoost Technologies

Page 79

SourceBoost Technologies

void\* memset( void \*ptr, char ch, unsigned char len ) (function) sets memory. Declared in memory.h

(function) Generates pseudo random number. Declared in rand,h Defined in

(function) Sets seed for pseudo random number generator. Declared in rand.h

(Macro) Returns the value of the argument with the largest value.

(Macro) Returns the value of the argument with the smallest value.

i2c\_init, i2c\_start, i2c\_restart, i2c\_stop, i2c\_read, i2c\_write (for more information look into i2c\_driver.h and i2c\_test.c files)

uart\_init, kbhit, getc, getch, putc, putch (for more information look into serial\_driver.h and serial\_test.c files)

icd\_setup, iprintf, icd\_clear, icd\_write, icd\_funcmode, icd\_datamode (for more information look into icd\_driver.h and icd.c files)

(function) Reads flash content from address 'addr'. Works with PIC16F87X devices. Declared in flash.h Defined in flash.pic16.lib

(function) Stores 'data' in an Internal buffer of 4 shorts long. Must be called four times to fill the Internal buffer. Data in this buffer is used by <u>flash\_write</u> to store data in flash. Works with PIC16F87X devices. Declared in flash.h Defined in

**Miscellaneous Functions** 

unsigned short rand( void )

void srand( unsigned short seed )

rand Bb

Defined in rand lib

max(a,b)

min(a,b)

**12C** functions

RS232 functions

LCD functions

Flash functions

flash.pic16,llb

BoostC™ Manual

short flash\_read(short addr)

vold flash\_loadbuffer(short data)

Page 77

void flash\_write(short addr)

(function) Writes data from an internal buffer into flash at address 'addr'. The Internal buffer that is 4 shorts long must be filled using 4 calls to <u>flash loadbuffer</u>. Works with PIC16F87X devices. Declared in flash.h Defined in flash.pic16.lib

#### **EEPROM** functions

char eeprom\_read(char addr)

(function) Reads eeprom content from address 'addr'. Works with PIC16F87X devices. Declared in eeprom.h Defined in eeprom.pic16.lib

#### void eeprom\_write(char addr, char data)

(function) Writes 'data' into eeprom at address 'addr'. Works with PIC16F87X devices, Declared in eeprom.h Defined in eeprom.plc16.lib

#### ADC functions

short adc\_measure(char ch)

(function) Reads ADC channel 'ch'. ADC must be initialized before using this function. Works with PIC16F devices that have ADC units. Declared in adc.h Defined In adc.pic16,lib

### A sample ADC initialization can look like:

A sample ADC initialization can look like: volatile bit adc\_on @ ADCOMO . ADON; //AC activate flag set\_bit(adcon1, PCFG); // AD result needs to be right justified set\_bit(adcon1, PCFG); // All nailog inputs set\_bit(adcon1, PCFG); // Vrof\* = Vdd set\_bit(adcon1, PCFG); // Vrof\* = Vdd set\_bit(adcon1, PCFG); // Select Tad = 32 \* Tosc (this depends on the X-tal here 10 Hzz, should work up to 20 HHz] clear\_bit(adcon0, CHS1); // Select Tad = 32 \* Tosc (this depends on the X-tal here 10 Hzz, should work up to 20 HHz] clear\_bit(adcon0, CHS1); // Channel 0 clear\_bit(adcon0, CHS1); // Activate AD module

#### One wire bus functions

#### char oo busreset()

(function) Resets the one wire bus. Declared in oo.h Defined in oo.pic16.lib and oo.pic18.lib

Here is a typical scenario how to use the one wire library:

80

BoostC<sup>™</sup> Manual

Page 78

BoostC<sup>m</sup> Manual

SourceBoost Technologies

PC System Requirements

In order to install and run the Compiler/SourceBoost Integrated Development Environment, a PC with the following specification is required:

Minimum System Specification Microsoft Windows 95/98/ME/NT/2000/XP, Adobe Reader and a web browser (to allow access to help files and manuals). Pentium Processor or equivalent, 128MB of RAM, CD ROM Drive,

80MB of disk space.

16Bit Color display Adapter at 800x600 Resolution.

**Recommended System Specification** As the Minimum System Specification, plus:

2.0GHz (or faster) Processor, 512MByte (or more) RAM,

16Bit Color display Adapter at 1024x768 Resolution (or higher).

SourceBoost Technologies

Page 83

BoostC™ Manual

SourceBoost Technologies

Page 81

#### al Information

IS NO WARRANTY FOR THE PROGRAM, TO THE EXTENT PERMITTED BY APPLICABLE LAW. T WHEN OTHERWISE STATED IN WRITING THE COPYRIGHT HOLDERS AND/OR OTHER ES PROVIDE THE PROGRAM "AS IS" WITHOUT WARRANTY OF ANY KIND, EITHER EXPRESSED PLED, INCLUDING, BUT NOT LIMITED TO, THE IMPLED WARRANTIES OF MERCHANTABILITY FITNESS FOR A PARTICULAR PURPOSE. THE ENTIRE RISK AS TO THE QUALITY AND IRMANCE OF THE PROGRAM PROVE DEFECTIVE, YOU WE THE COST OF ALL NECESSARY SERVICING, REPAIR OR CORRECTION.

HE THE COST OF ALL INCLESSART SERVICING, VENARO IN CONCRECTION.
I EVENT UNLESS REQUIRED BY APPLICABLE LAW OR AGREED TO IN WRITING WILL ANY LIGHT HOLDER, OR ANY OTHER PARTY WHO MAY MODIPY AND/OR REDISTRIBUTE THE RAM AS PERMITTED ABOVE, BE LIABLE TO YOU FOR DAMAGES, INCLUDING ANY GENERAL, AL, INCIDENTAL OR CONSEQUENTIAL DAMAGES ANSING OUT OF THE USE OR INABILITY TO HE PROGRAM (INCLUDING BUT NOT LIMITED TO LOSS OF DATA OR DATA BEING RENDERED JURATE OR LOSSES SUSTAINED BY YOU OR THIRD PARTIES OR A FAILURE OF THE PROGRAM FRATE WITH ANY OTHER PROGRAMS, LEVEN IF SUCH HOLDER OR OTHER PARTY HAS BEEN IED OF THE POSSIBILITY OF SUCH DAMAGES.

UTHOR RESERVES THE RIGHT TO REJECT ANY LICENSE (REGISTRATION) REQUEST WITHOUT INING THE REASONS WHY SUCH REQUEST HAS BEEN REJECTED. IN CASE YOUR LICENSE STRATION) REQUEST GETS REJECTED YOU MUST STOP USING THE SOURCEBOOST IDE, BOOST, C++, BooStBasic, C2C+) and P2C-plus COMPILERS AND REMOVE THE WHOLE EBOOST IDE INSTALLATION FROM YOUR COMPUTER.

schip, PIC, PICmicro and MPLAB are registered trademarks of Microchip nology Inc.

tC, BoostC++ and BoostLink are trademarks of SourceBoost Technologies. r trademarks and registered trademarks used in this document are the erty of their respective owners.

http://www.sourceboost.com Copyright@ 2004-2007 Pavel Bara Copyright@ 2004-2007 David Hobday

#### **Technical support**

For example projects and updates please refer to our website: http://www.sourceboost.com

We operate a forum where technical and license issue problems can be posted. This should be the first place to visit: http://forum.sourceboost.com

## **BoostC Support Subscription**

By buying a support subscription you will receive priority technical support via email. This ensures that your query or problem will be at the front of the queue and receive the highest priority attention.

BoostC Support Subscriptions are here: http://www.sourceboost.com/Products/BoostC/BuyLicense/SupportSubscription.html

#### Licensing Issues

If you have licensing issues, then please send a mail to: support@sourceboost.com

#### **General Support**

For general support issues, please use our support forum: http://forum.sourceboost.com

We are always pleased to hear your comments, this helps us to satisfy your needs. Post your comments on the SourceBoost Forum or send an email to: support@sourceboost.com

1 84

Page 82